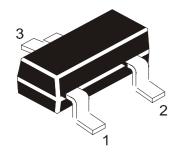
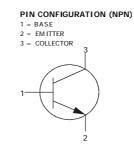




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#### NPN SILICON PLANAR EPITAXIAL TRANSISTOR





#### CMBTA44

SOT-23 Formed SMD Package

# Marking Code is =3Z

# **Designed for Extremely High Voltage Applications**

# ABSOLUTE MAXIMUM RATINGS (T<sub>a</sub>=25°C)

DESCRIPTION	SYMBOL	VALUE	UNITS
Collector Base Voltage	$V_{CBO}$	450	V
Collector Emitter Voltage	$V_{CEO}$	400	V
Emitter Base Voltage	$V_{EBO}$	6	V
Collector Current	I <sub>C</sub>	300	mA
Collector Power Dissipation	$P_{D}$	350	mW
Operating And Storage Junction Temperature Range	$T_{j},T_{stg}$	- 65 to+150	°C

#### THERMAL RESISTANCE

Junction to Ambient in free air	R <sub>th(j-a)</sub>	357	°C/W	ĺ
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#### Electrical Characterstics (T<sub>a</sub>=25°C unless specified otherwise)

DESCRIPTION	SYMBOL CONDITIONS		MIN	TYP	MAX	UNIT	
Collector Cut Off Current	I <sub>CBO</sub>	$V_{CB} = 400 V, I_{E} = 0$			100	nA	
Collector Cut Off Current	I <sub>CES</sub>	V <sub>CE</sub> =400V, V <sub>BE</sub> =0			500	nA	
Emitter Cut Off Current	I <sub>EBO</sub>	$V_{EB}=4V, I_{C}=0$			100 nA		
Collector Base Voltage V <sub>CBO</sub>		I <sub>C</sub> =100μA, I <sub>E</sub> =0	450			V	
Collector Emitter Voltage	oltage $V_{CES}$ $I_C=100\mu A, V_{BE}=0$ 450		V				
Collector Emitter Voltage	tor Emitter Voltage V <sub>CEO</sub> I <sub>C</sub> =1mA, I <sub>B</sub> =0		400			V	
Emitter Base Voltage	$V_{EBO}$	$I_E=10\mu A, I_C=0$ 6				V	
Collector Emitter Saturation	V <sub>CE(sat)</sub>	I <sub>C</sub> =1mA, I <sub>B</sub> =0.1mA			0.40	V	
Voltage		I <sub>C</sub> =10mA, I <sub>B</sub> =1mA I <sub>C</sub> =50mA, I <sub>B</sub> =5mA			0.50 0.75	V V	
Base Emitter Saturation Voltage	$V_{BE(sat)}$	I <sub>C</sub> =10mA, I <sub>B</sub> =1mA			0.75	V	
DC Current Gain	h <sub>FE</sub>	$V_{CE} = 10V, I_{C} = 1mA$	40				
		$V_{CE} = 10V, I_{C} = 10mA$	50		200		
		$V_{CE} = 10V, I_{C} = 50mA$	45				
		$V_{CE} = 10V, I_{C} = 100mA$	20				
Transition Frequency	f <sub>T</sub>	$V_{CE}$ =10V, $I_{C}$ =10mA, f=10MHz	20			MHz	
Output Capacitance	C <sub>ob</sub>	$V_{CB}$ =20V, $I_E$ =0, f=1MHz			7.0 pF		
Input Capacitance	$C_{ib}$	V <sub>EB</sub> =0.5V, Ic=0, f=1MHz			130	pF	

# **SOT-23 Formed SMD Package**

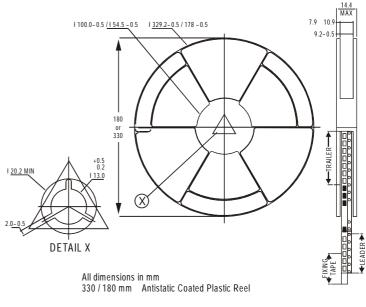
# +/- 0.05 0.62 1.30----+/-0.05 0.62 +/-0.025 1.90 cL 3 - 0.05 - 1.30 +/- 0.05 0.62 |← 0.62 0.08 0.08 MIN MIN PARTING LINE RO.08

2.50 +/-0.10

0.21

0.06

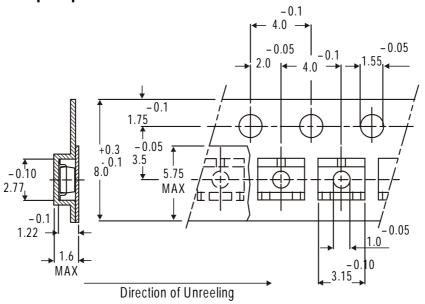
# SOT-23 Package Reel Information Reel specifications for Packing (13"/7" reels)



 NOTES:
 8mm Tape
 8mm Tape
 Size of Reel
 Size of Reel
 Size of Reel
 180 mm (7")
 180 mm (7")
 180 mm (7")
 3,000 Pcs
 3,000 Pcs

- 1. The bandolier of 330 mm reel contains at least 10,000 devices.
- 2. The bandolier of 180 mm reel contains at least 3,000 devices.
- No more than 0.5% missing devices / reel. 50 empty compartments for 330 mm reel.
   15 empty compartments for 180 mm reel.
- Three consecutive empty places might be found provided this gap is followed by 6 consecutive devices.
- The carrier tape (leader) starts with at least 75 empty positions (equivalent to 330 mm). In order to fix the carrier tape a self adhesive tape of 20 to 50 mm is applied. At the end of the bandolier at least 40 empty positions (equivalent to 160 mm) are there.

#### **Tape Specification for SOT-23 Surface Mount Device**



All dimensions in mm

#### **Packing Detail**

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
SOT-23 T&R	3K/reel	136 gm/3K pcs	3" x 7.5" x 7.5" 9" x 9" x 9"	12.0K 51.0K	17" x 15" x 13.5" 19" x 19" x 19"	192.0K 408.0K	12 kgs 28 kgs
	10K/reel	415 gm/10K pcs	13" x 13" x 0.5"	10.0K	17" x 15" x 13.5"	300.0K	16 kgs

#### **Customer Notes**

#### **Component Disposal Instructions**

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
  - 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

#### **Disclaimer**

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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CDIL is a registered Trademark of
Continental Device India Limited
C-120 Naraina Industrial Area, New Delhi 110 028, India.

Telephone + 91-11-2579 6150, 4141 1112 Fax + 91-11-2579 5290, 4141 1119

email@cdil.com www.cdilsemi.com