

N-Channel Enhancement Mode Power MOSFET

MTN15N50F3

BV_{DSS} : 500V
$R_{DS(ON)}$: 0.38 Ω (typ.)
I_D : 15A

Description

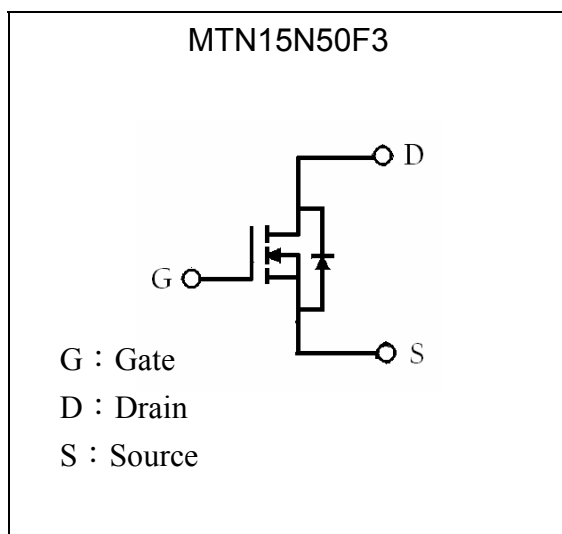
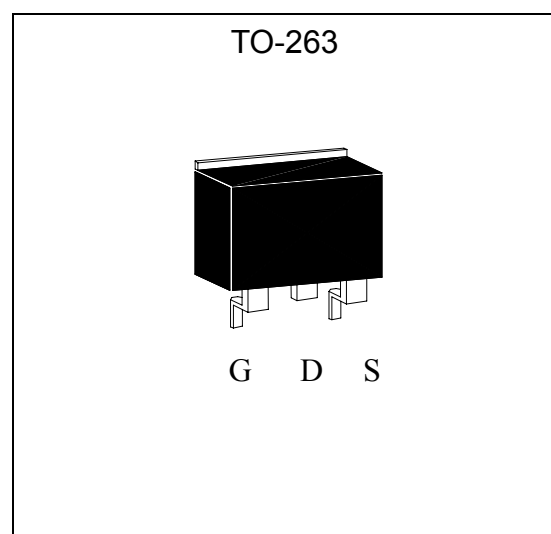
The MTN15N50F3 is a N-channel enhancement-mode MOSFET, providing the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost effectiveness. The TO-263 package is universally preferred for all commercial-industrial applications

Features

- Low On Resistance
- Simple Drive Requirement
- Low Gate Charge
- Fast Switching Characteristic
- RoHS compliant package

Applications

- Power Factor Correction
- Flat Panel Power
- Full and Half Bridge Power Supplies
- Two-Transistor Forward Power Supplies

Symbol**Outline**

**Absolute Maximum Ratings** ($T_C=25^{\circ}\text{C}$)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage (Note 1)	V_{DS}	500	V
Gate-Source Voltage	V_{GS}	± 30	V
Continuous Drain Current	I_D	15*	A
Continuous Drain Current @ $T_C=100^{\circ}\text{C}$	I_D	9*	A
Pulsed Drain Current @ $V_{GS}=10\text{V}$ (Note 2)	I_{DM}	60*	A
Single Pulse Avalanche Energy (Note 3)	E_{AS}	750	mJ
Avalanche Current (Note 2)	I_{AR}	15	A
Repetitive Avalanche Energy (Note 2)	E_{AR}	25	mJ
Peak Diode Recovery dv/dt (Note 4)	dv/dt	4.5	V/ns
Maximum Temperature for Soldering @ Lead at 0.125 in(3.175mm) from case for 10 seconds	T_L	300	$^{\circ}\text{C}$
Total Power Dissipation ($T_C=25^{\circ}\text{C}$)	P_D	250	W
Linear Derating Factor above 25°C		2	W/ $^{\circ}\text{C}$
Operating Junction and Storage Temperature	T_j, T_{stg}	-55~+150	$^{\circ}\text{C}$

*Drain current limited by maximum junction temperature

- Note : 1. $T_J=+25^{\circ}\text{C}$ to $+150^{\circ}\text{C}$.
2. Repetitive rating; pulse width limited by maximum junction temperature.
3. $I_{SD}=15\text{A}$, $dI/dt < 100\text{A}/\mu\text{s}$, $V_{DD} < BV_{DSS}$, $T_J=+150^{\circ}\text{C}$.
4. $I_{AS}=15\text{A}$, $V_{DD}=50\text{V}$, $L=6\text{mH}$, $R_G=25\Omega$, starting $T_J=+25^{\circ}\text{C}$.

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{th,j-c}$	0.5	$^{\circ}\text{C}/\text{W}$
Thermal Resistance, Junction-to-ambient, max	$R_{th,j-a}$	62.5	$^{\circ}\text{C}/\text{W}$



Characteristics (Tj=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	500	-	-	V	V _{GS} =0, I _D =250μA
ΔBV _{DSS} /ΔT _j	-	0.5	-	V/°C	Reference to 25°C, I _D =250μA
V _{GS(th)}	2.0	-	4.0	V	V _{DS} = V _{GS} , I _D =250μA
*G _{FS}	-	8	-	S	V _{DS} =15V, I _D =7.5A
I _{GSS}	-	-	±100	nA	V _{GS} =±30
I _{DSS}	-	-	1	μA	V _{DS} =500V, V _{GS} =0
	-	-	25		V _{DS} =400V, V _{GS} =0, T _j =125°C
*R _{DS(ON)}	-	0.38	0.42	Ω	V _{GS} =10V, I _D =7.5A
Dynamic					
*Q _g	-	45	-	nC	I _D =15A, V _{DD} =250V, V _{GS} =10V
*Q _{gs}	-	11	-		
*Q _{gd}	-	22	-		
*t _{d(ON)}	-	40	-	ns	V _{DD} =250V, I _D =15A, V _{GS} =10V, R _G =10Ω
*t _r	-	140	-		
*t _{d(OFF)}	-	100	-		
*t _f	-	85	-		
C _{iss}	-	3090	-	pF	V _{GS} =0V, V _{DS} =25V, f=1MHz
C _{oss}	-	250	-		
C _{rss}	-	120	-		
Source-Drain Diode					
*V _{SD}	-	-	1.5	V	I _S =15A, V _{GS} =0V
*I _S	-	-	15	A	
*I _{SM}	-	-	60		
*t _{rr}	-	420	-	ns	V _{GS} =0, I _F =15A, dI/dt=100A/μs
*Q _{rr}	-	5	-	μC	

*Pulse Test : Pulse Width ≤300μs, Duty Cycle ≤2%

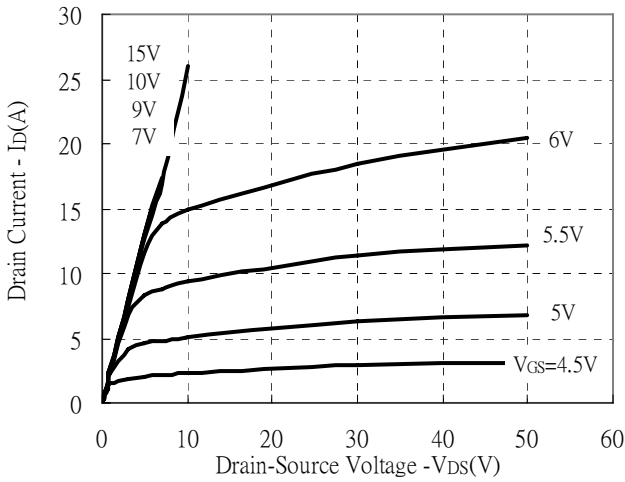
Ordering Information

Device	Package	Shipping	Marking
MTN15N50F3	TO-263 (RoHS compliant)	800 pcs / Tape & Reel	15N50

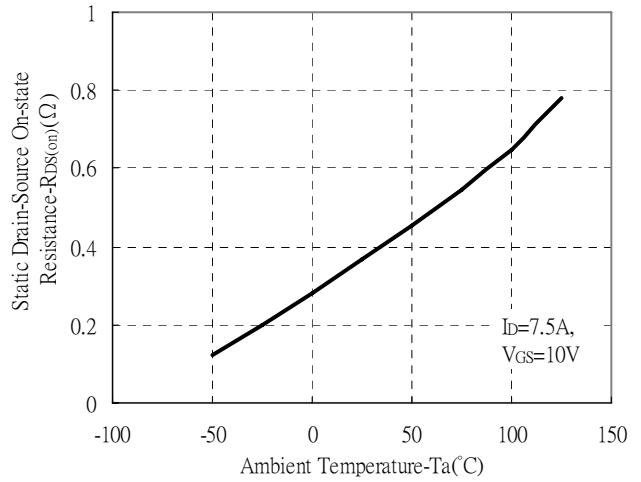


Characteristic Curves

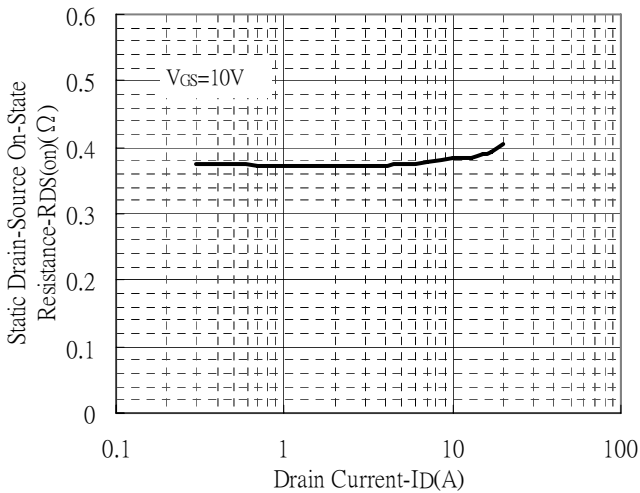
Typical Output Characteristics



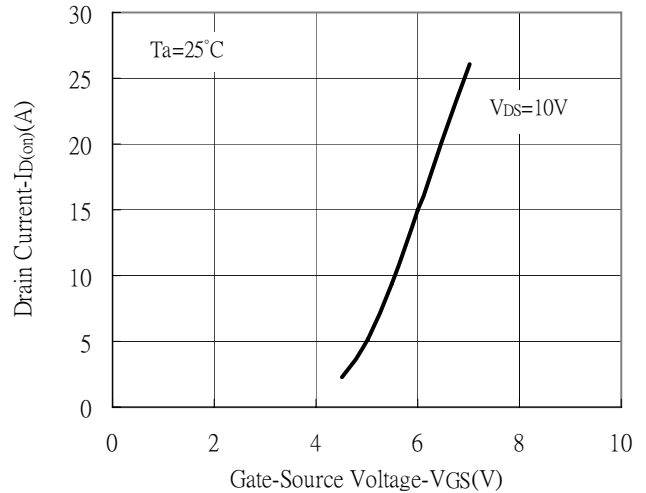
Static Drain-Source On-resistance vs Ambient Temperature



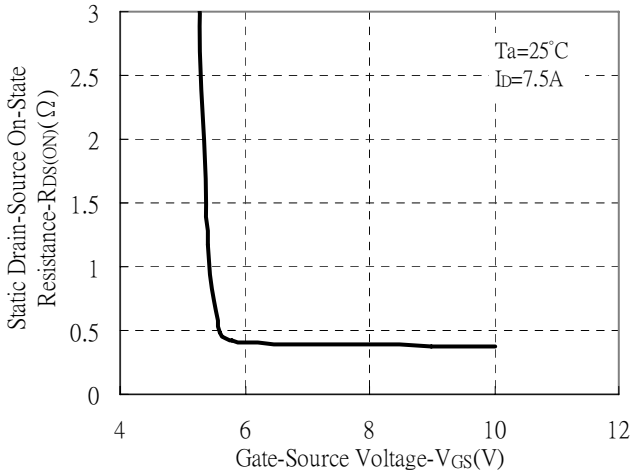
Static Drain-Source On-State resistance vs Drain Current



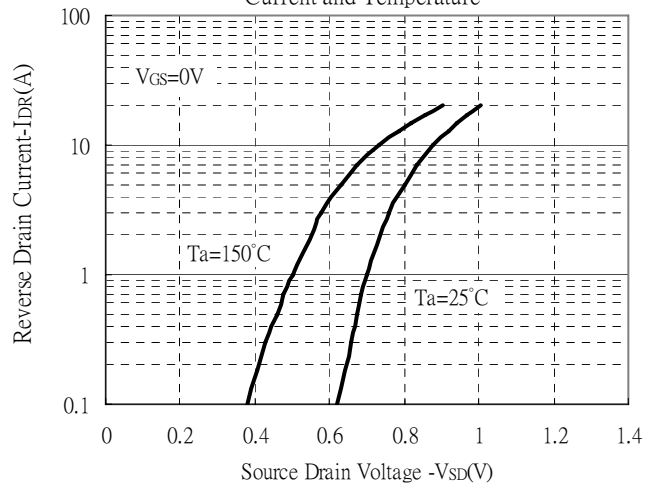
Drain Current vs Gate-Source Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage



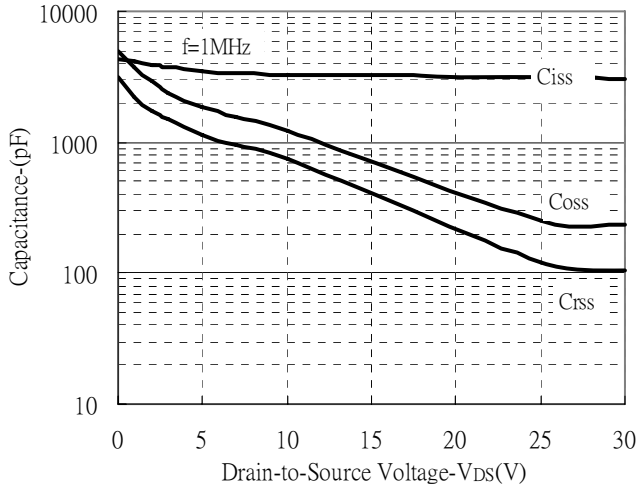
Body Diode Forward Voltage Variation vs Source Current and Temperature



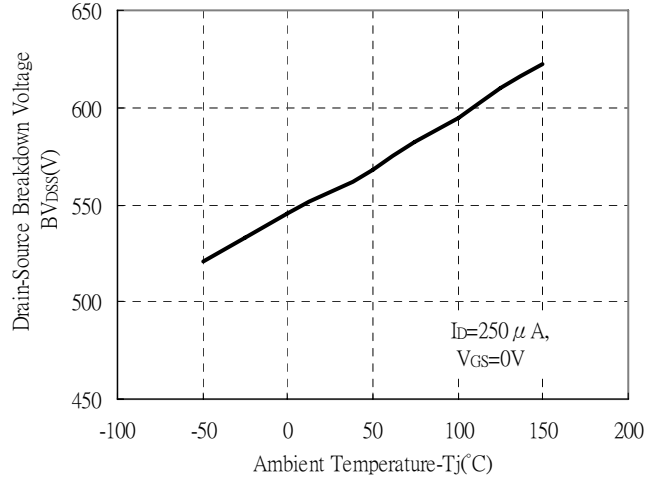


Characteristic Curves(Cont.)

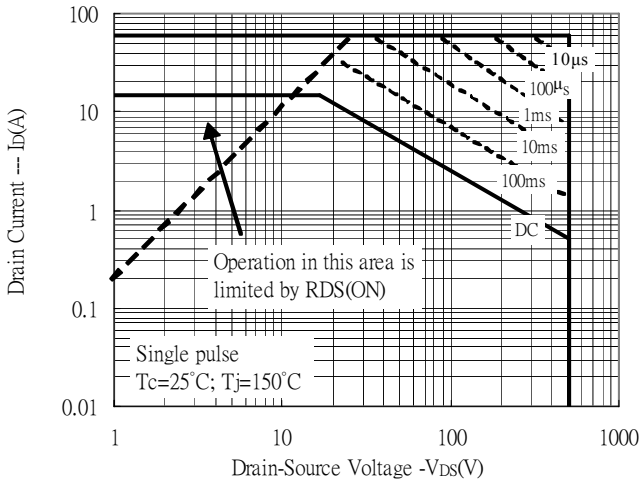
Capacitance vs Reverse Voltage



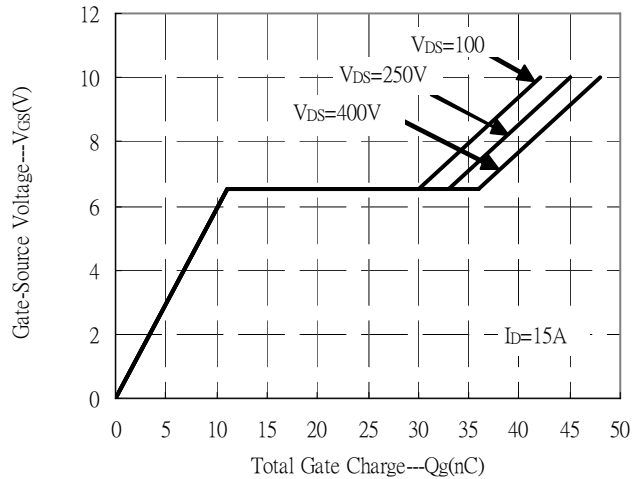
Brekdown Voltage vs Ambient Temperature



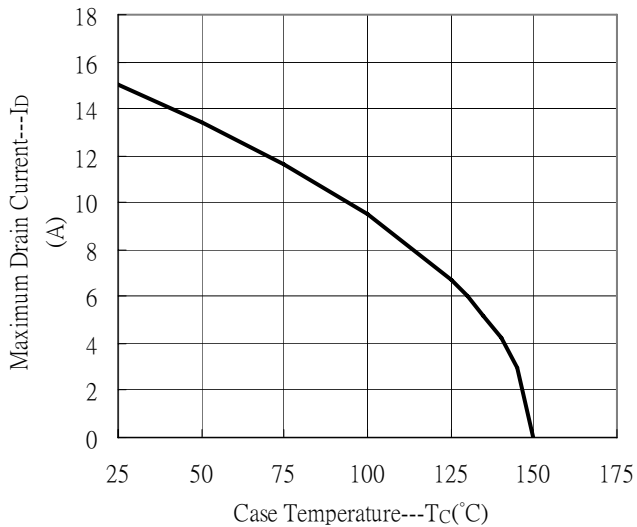
Maximum Safe Operating Area



Gate Charge Characteristics



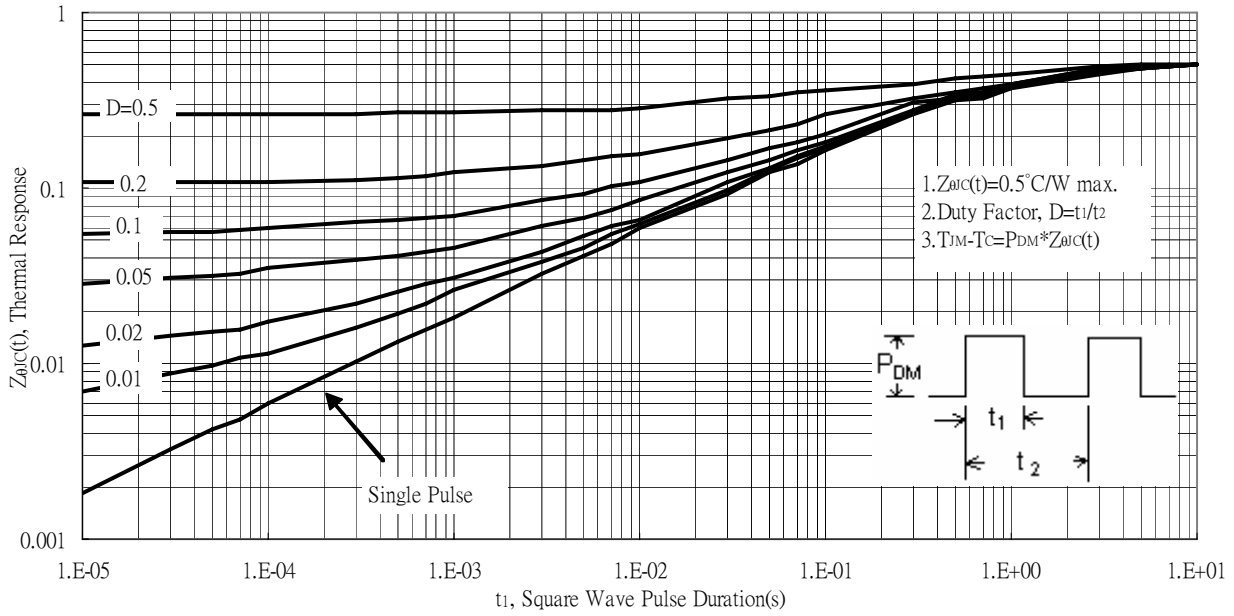
Maximum Drain Current vs Case Temperature



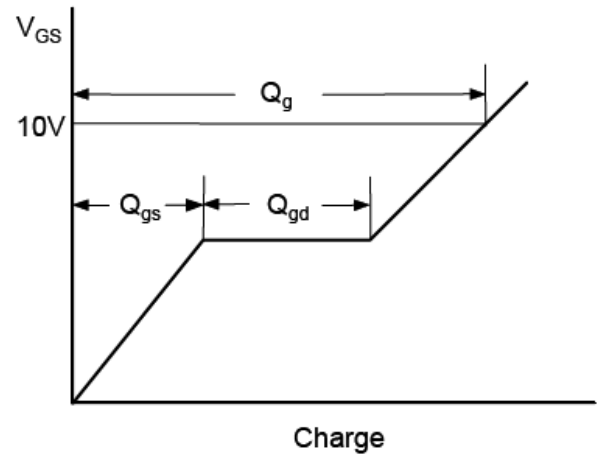
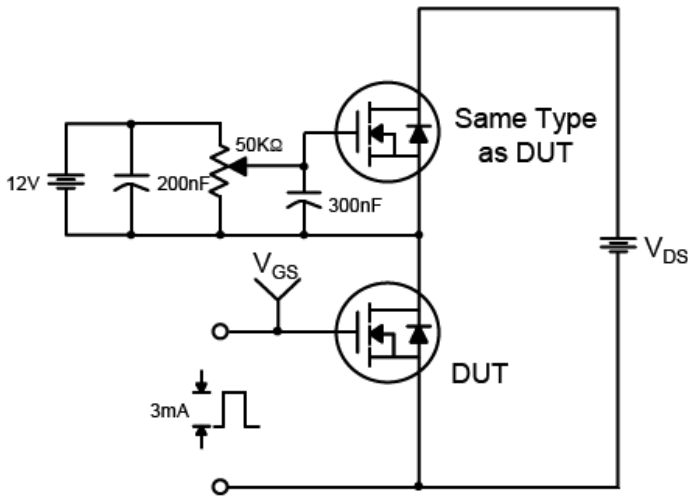


Characteristic Curves(Cont.)

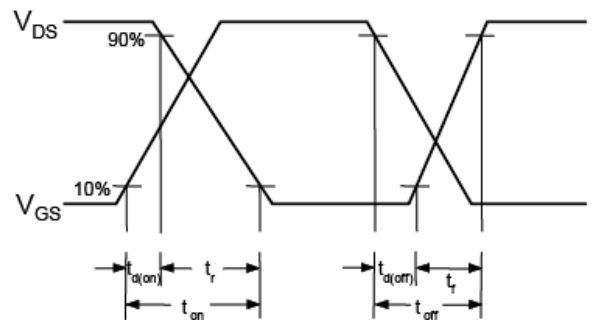
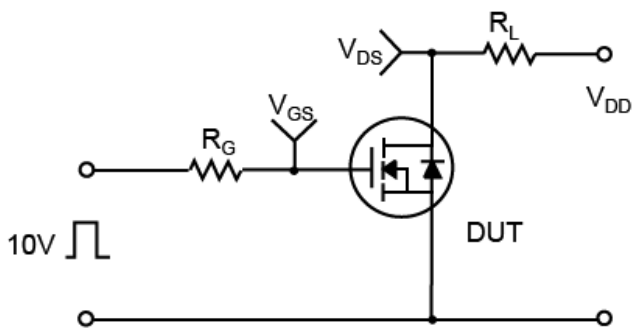
Transient Thermal Response Curves



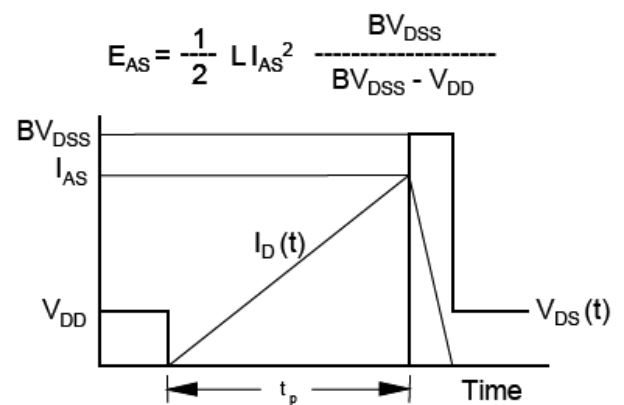
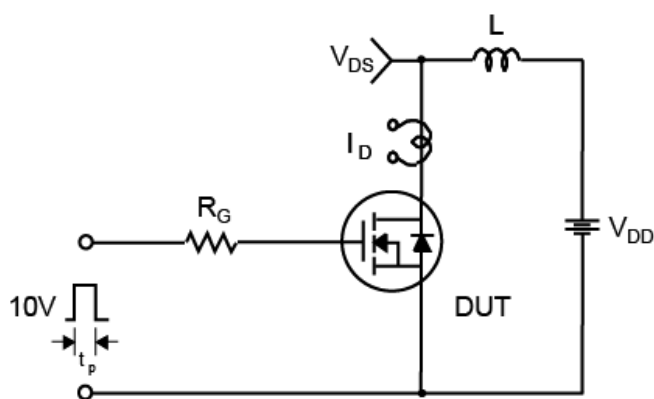
Test Circuit and Waveforms



Resistive Switching Test Circuit & Waveforms

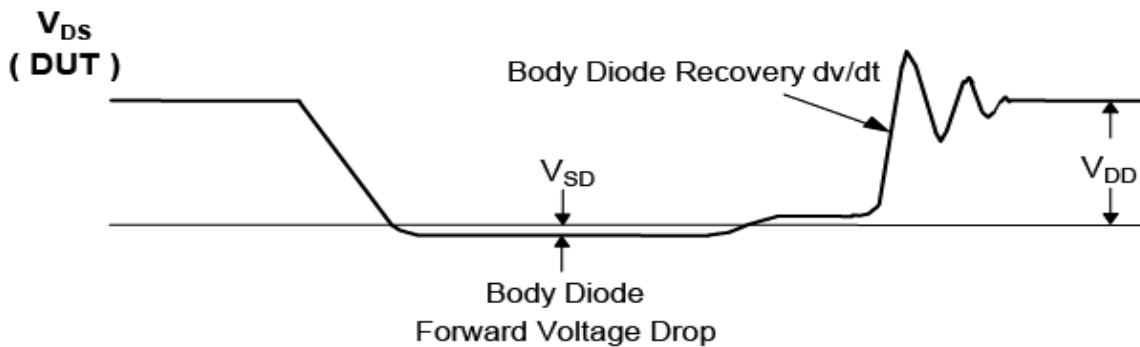
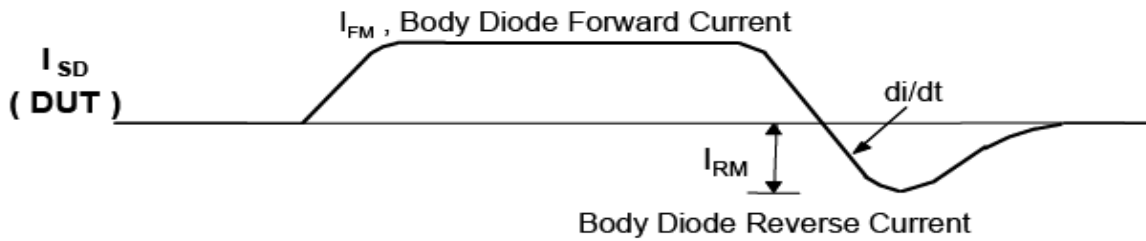
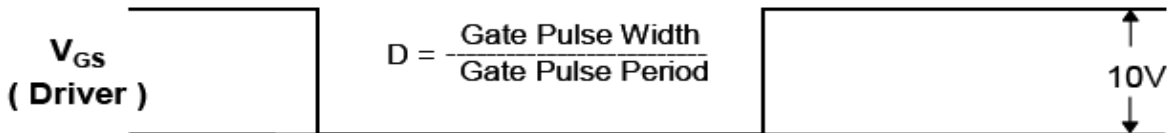
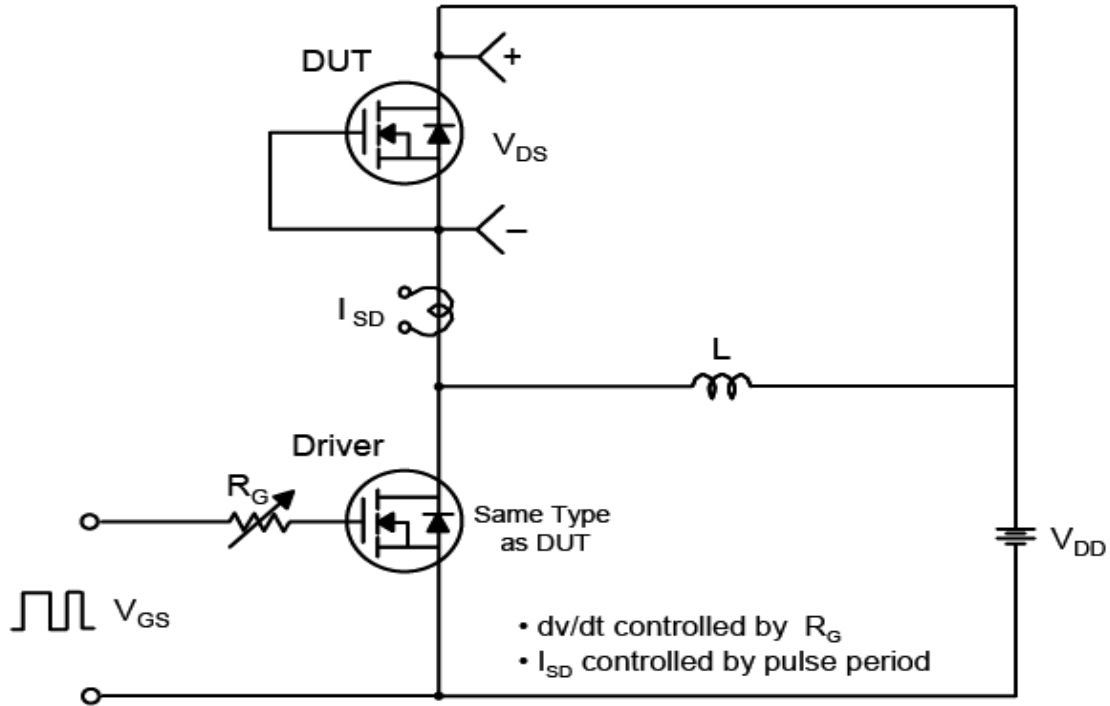


Unclamped Inductive Switching Test Circuit & Waveforms

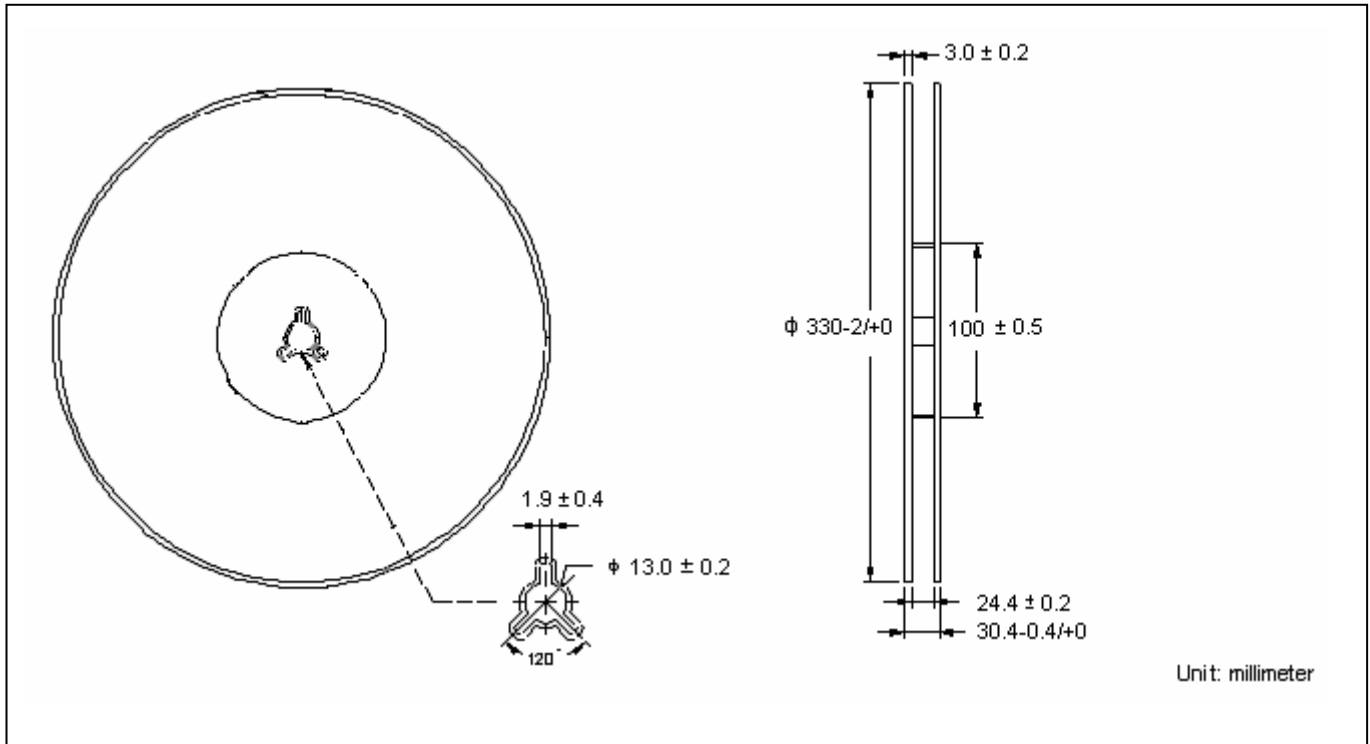


Test Circuit and Waveforms(Cont.)

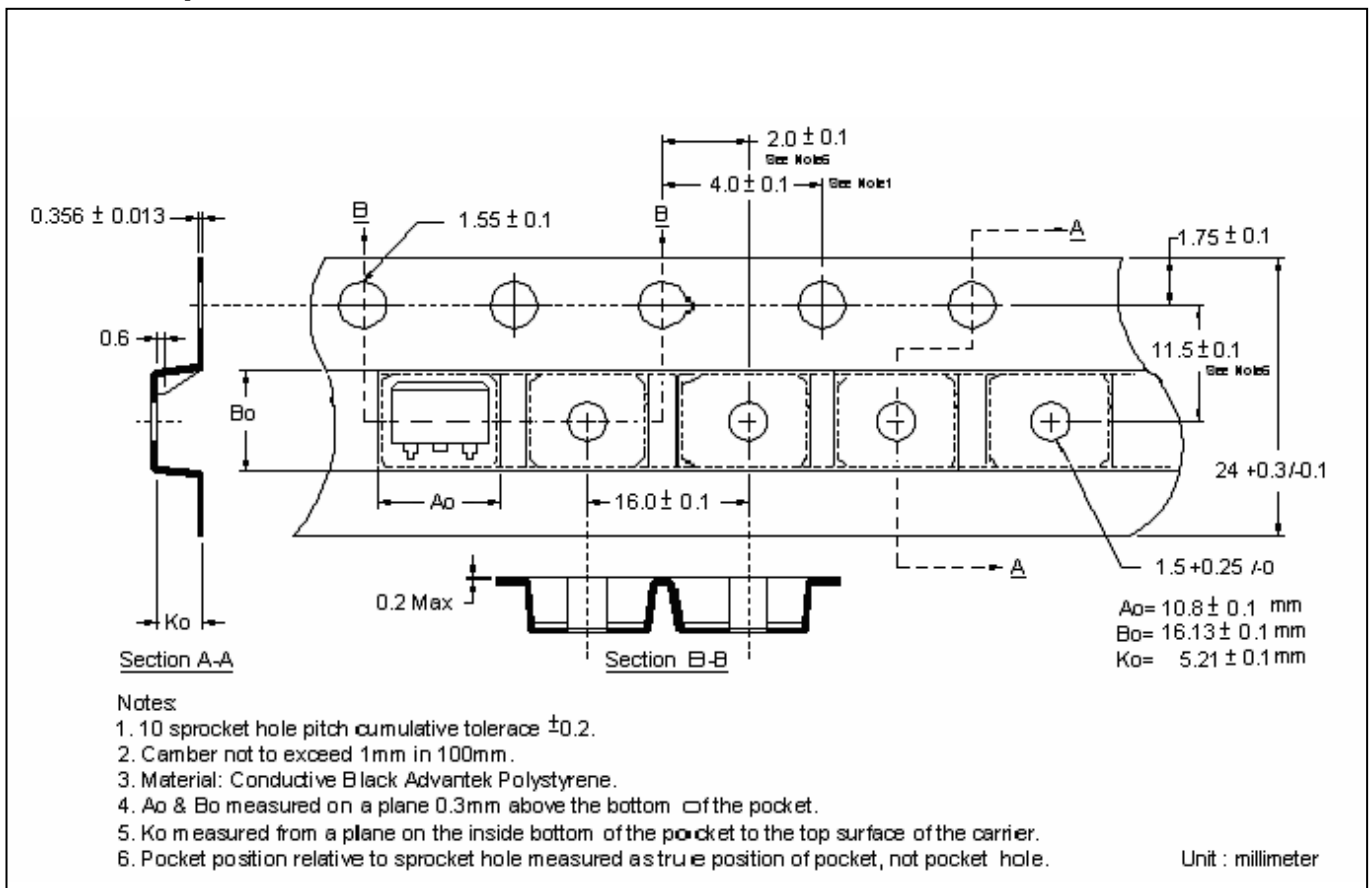
Peak Diode Recovery dv/dt Test Circuit & Waveforms



Reel Dimension



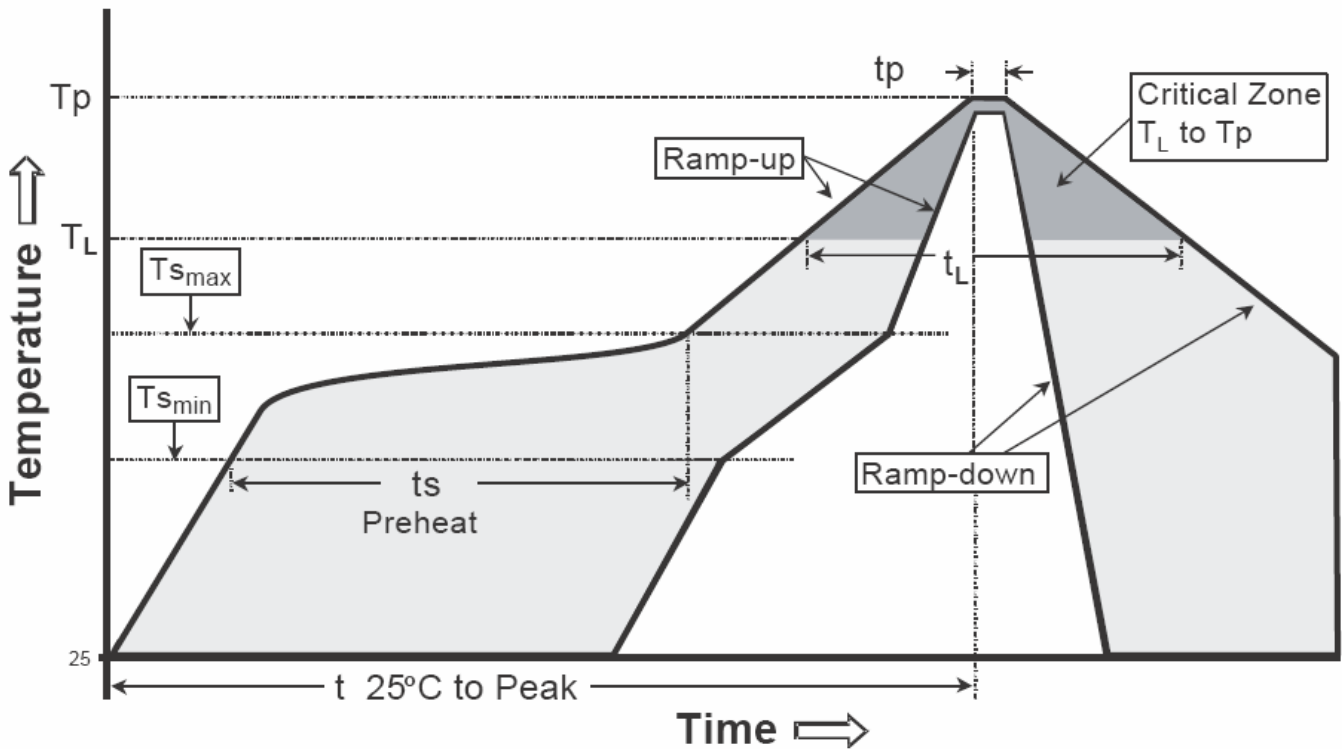
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

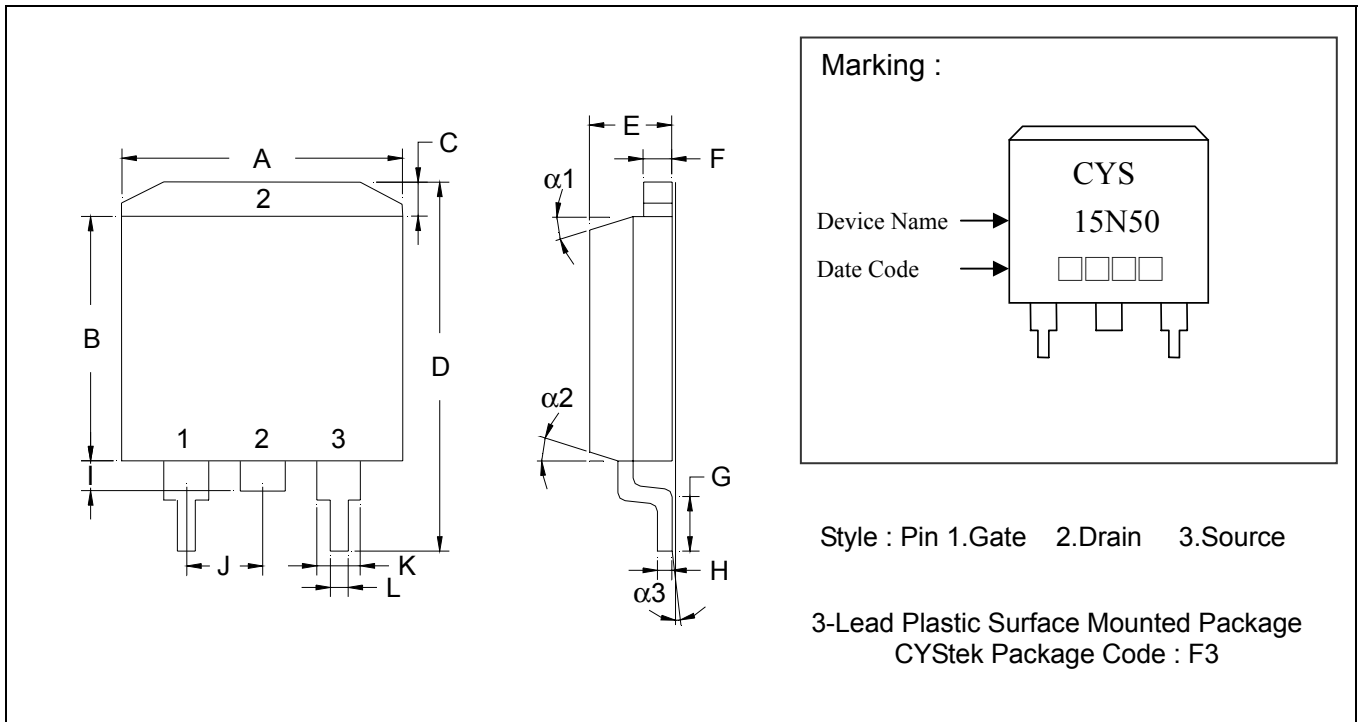
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (Tl)	183°C	217°C
- Time (tL)	60-150 seconds	60-150 seconds
Peak Temperature(TP)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

TO-263 Dimension



*:Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.3800	0.4050	9.65	10.29	I	0.0500	0.0700	1.27	1.78
B	0.3300	0.3700	8.38	9.40	J	-	*0.1000	-	*2.54
C	-	0.0550	-	1.40	K	0.0450	0.0550	1.14	1.40
D	0.5750	0.6250	14.61	15.88	L	0.0200	0.0390	0.51	0.99
E	0.1600	0.1900	4.06	4.83	$\alpha 1$	-	-	6°	8°
F	0.0450	0.0550	1.14	1.40	$\alpha 2$	-	-	6°	8°
G	0.0900	0.1100	2.29	2.79	$\alpha 3$	-	-	0°	5°
H	0.0180	0.0290	0.46	0.74					

- Notes :**
- Controlling dimension : millimeters.
 - Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 - If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material :

- Lead : Pure tin plated.
- Mold Compound : Epoxy resin family, flammability solid burning class:UL94V-0.

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