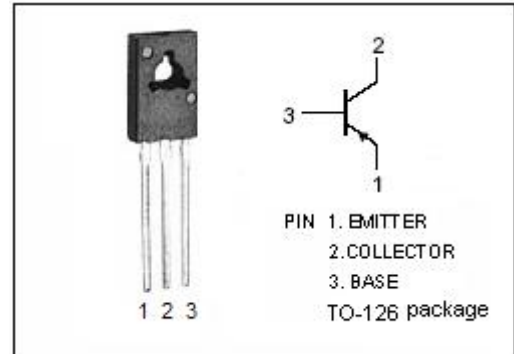


**isc Silicon PNP Power Transistor**
**2SA1214**
**DESCRIPTION**

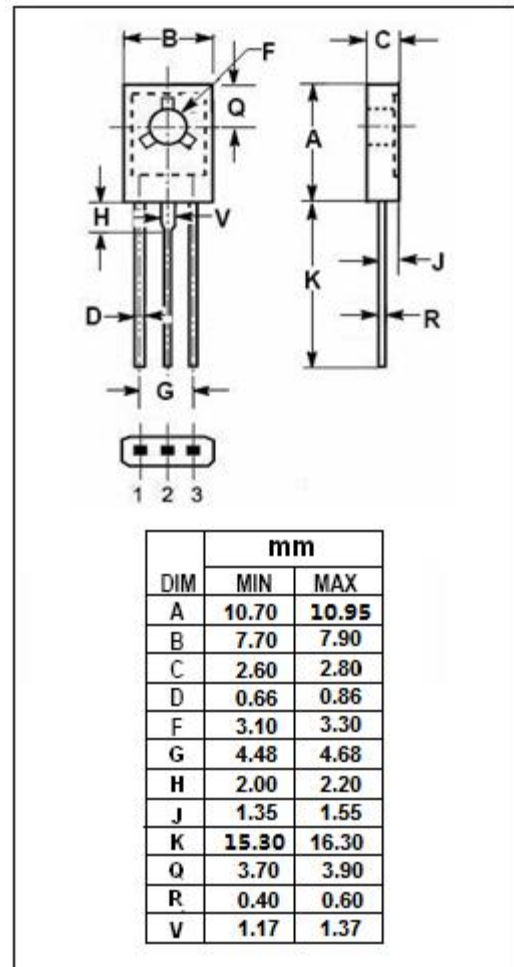
- Collector-Emitter Breakdown Voltage-  
 $V_{(BR)CEO} = -50V$  (Min)
- Good Linearity of  $h_{FE}$
- Wide Area of Safe Operation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation


**APPLICATIONS**

- Designed for low frequency power amplifier applications.

**ABSOLUTE MAXIMUM RATINGS(Ta=25°C)**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CBO}$	Collector-Base Voltage	-60	V
$V_{CEO}$	Collector-Emitter Voltage	-50	V
$V_{EBO}$	Emitter-Base Voltage	-5	V
$I_C$	Collector Current-Continuous	-2	A
$P_C$	Collector Power Dissipation @ $T_a=25^\circ C$	1.5	W
	Total Power Dissipation @ $T_C=25^\circ C$	25	
$T_J$	Junction Temperature	150	°C
$T_{stg}$	Storage Temperature Range	-55~150	°C



**isc Silicon PNP Power Transistor****2SA1214****ELECTRICAL CHARACTERISTICS**T<sub>C</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -1mA; I <sub>B</sub> = 0	-50			V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = -0.1mA; I <sub>C</sub> = 0	-5			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -1A; I <sub>B</sub> = -0.1A			-1.0	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> = -1A; I <sub>B</sub> = -0.1A			-1.5	V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = -60V; I <sub>E</sub> = 0			-1.0	μ A
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = -5V; I <sub>C</sub> = 0			-1.0	μ A
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = -150mA; V <sub>CE</sub> = -2V	60		320	
f <sub>T</sub>	Current-Gain—Bandwidth Product	I <sub>C</sub> = -100mA; V <sub>CE</sub> = -10V		35		MHz
C <sub>OB</sub>	Output Capacitance	I <sub>E</sub> = 0; V <sub>CB</sub> = -10V; f= 1.0MHz		45		pF

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