

## isc Silicon PNP Power Transistor

2SA2004

## DESCRIPTION

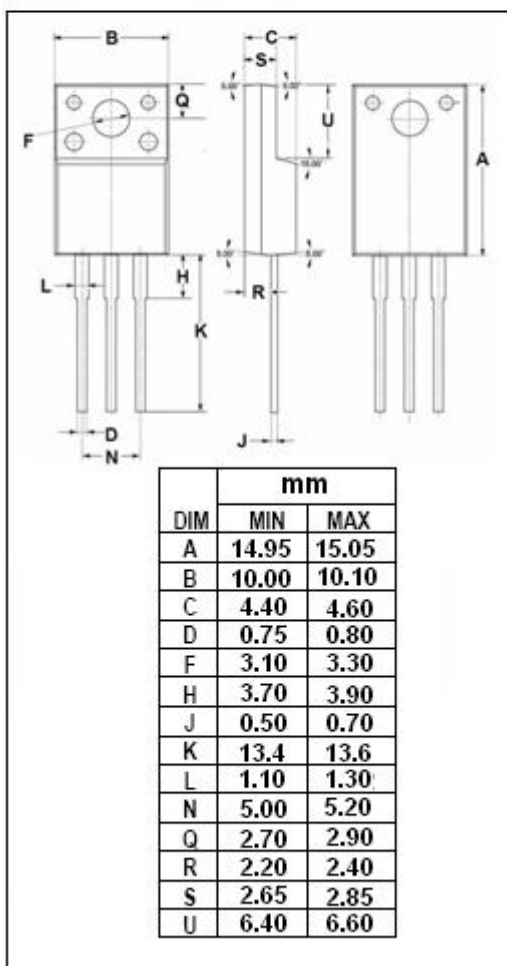
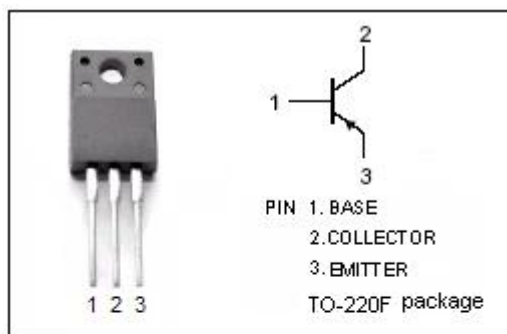
- Silicon PNP epitaxial planar type
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

## APPLICATIONS

- Power amplifier applications
- Driver stage amplifier applications

ABSOLUTE MAXIMUM RATINGS( $T_a=25^{\circ}\text{C}$ )

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CBO}$	Collector-Base Voltage	-60	V
$V_{CEO}$	Collector-Emitter Voltage	-60	V
$V_{EBO}$	Emitter-Base Voltage	-5	V
$I_C$	Collector Current-Continuous	-8	A
$I_B$	Base Current-Continuous	-1	A
$P_c$	Collector Power Dissipation @ $T_c=25^{\circ}\text{C}$	20	W
$T_J$	Junction Temperature	150	$^{\circ}\text{C}$
$T_{stg}$	Storage Temperature Range	-55~150	$^{\circ}\text{C}$



**isc Silicon PNP Power Transistor****2SA2004****ELECTRICAL CHARACTERISTICS****T<sub>C</sub>=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>CEQ(SUS)</sub>	Collector-Emitter Sustaining Voltage	I <sub>C</sub> = -10mA; I <sub>B</sub> = 0	-60			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -5A; I <sub>B</sub> =- 0.25A			-1.2	V
V <sub>BE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -5A; I <sub>B</sub> =- 0.25A			-1.7	V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = -60V ; I <sub>E</sub> = 0			-10	μ A
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = -5V ; I <sub>C</sub> = 0			-10	μ A
h <sub>FE-1</sub>	DC Current Gain	I <sub>C</sub> = -0.1A ; V <sub>CE</sub> = -2V	100		320	
h <sub>FE-2</sub>	DC Current Gain	I <sub>C</sub> = -5A ; V <sub>CE</sub> = -2V	30			

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