

Silicon PNP Power Transistors

2SA489

DESCRIPTION

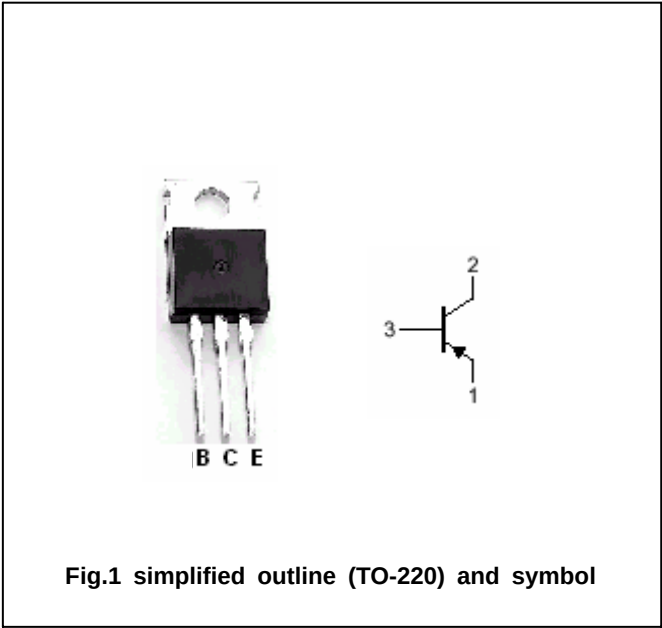
- With TO-220 package
- Low collector saturation voltage
- Short switching time

APPLICATIONS

- Various inductance lamp drivers for electrical equipment
- Inverters;converters
- Power amplification
- Switching regulator ,driver

PINNING

PIN	DESCRIPTION
1	Emitter
2	Collector;connected to mounting base
3	Base



Absolute maximum ratings(Ta=25℃)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	-70	V
V _{CEO}	Collector-emitter voltage	Open base	-60	V
V _{EBO}	Emitter-base voltage	Open collector	-5	V
I _C	Collector current		-4	A
P _C	Collector power dissipation	T _C =25℃	30	W
T _j	Junction temperature		150	℃
T _{stg}	Storage temperature		-55~150	℃

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CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-30mA, I _B =0	-60			V
V _{(BR)CBO}	Collector-base breakdown voltage	I _C =-1mA; I _E =0	-70			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =-1mA; I _C =0	-5			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-3A; I _B =-0.3A			-1.0	V
V _{BEsat}	Base-emitter saturation voltage	I _C =-3A; I _B =-0.3A			-1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =-70V; I _E =0			-10	μ A
I _{EBO}	Emitter cut-off current	V _{EB} =-5V; I _C =0			-10	μ A
h _{FE}	DC current gain	I _C =-0.5A; V _{CE} =-5V	40		240	
f _T	Transition frequency	I _C =-0.5A; V _{CE} =-5V	3			MHz

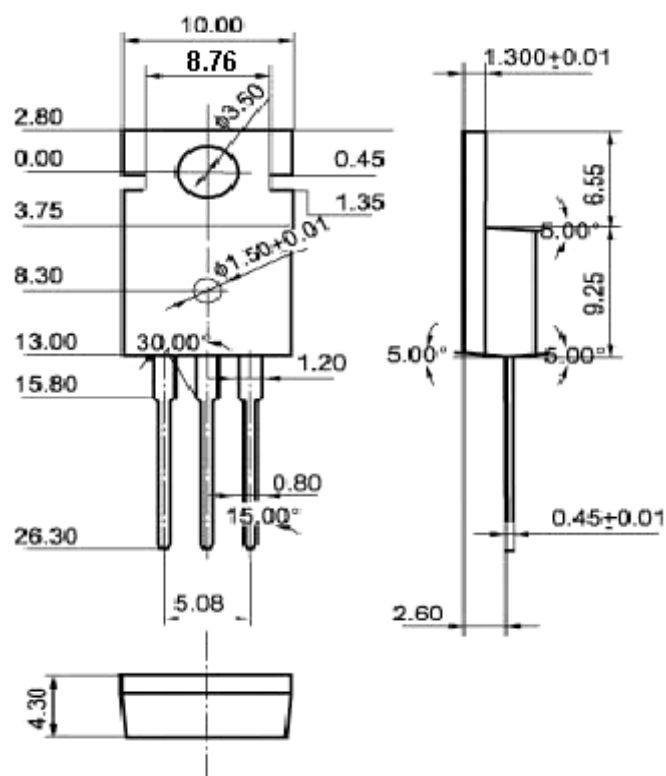
◆ h_{FE} Classifications

R	O	Y
40-80	70-140	120-240

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PACKAGE OUTLINE

Fig.2 Outline dimensions(unindicated tolerance: ± 0.10 mm)