

isc Silicon NPN Power Transistor

2SC1343

DESCRIPTION

- With TO-3 Package
- High voltage
- Wide area of safe operation
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

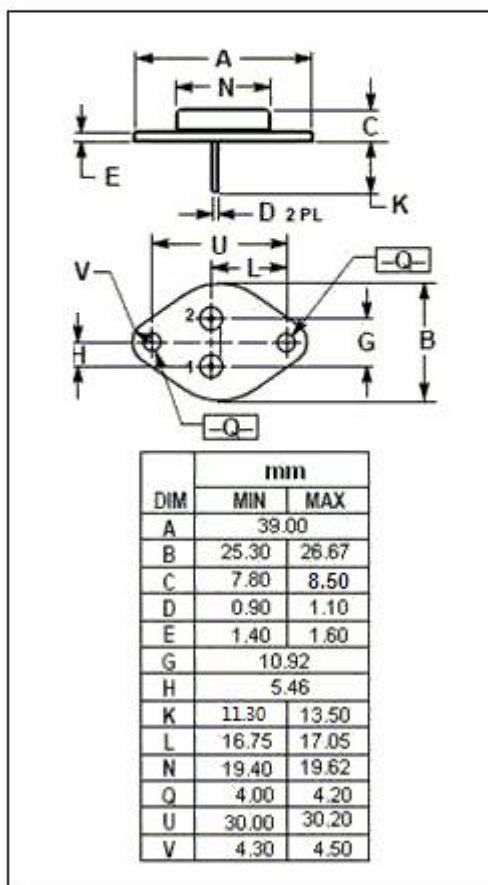
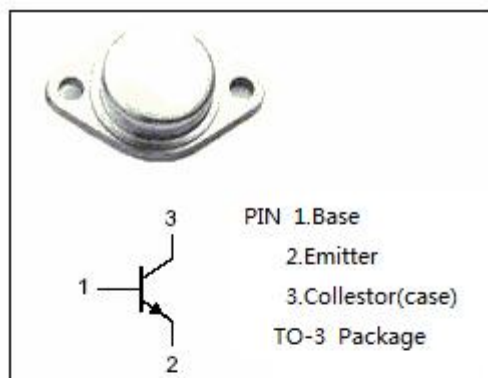
- For large screen color deflection circuits

ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	150	V
V_{CEO}	Collector-Emitter Voltage	110	V
V_{EBO}	Emitter-Base Voltage	5	V
I_C	Collector Current-Continuous	10	A
P_C	Collector Power Dissipation	100	W
T_J	Junction Temperature	-65~200	$^{\circ}\text{C}$
T_{stg}	Storage Temperature Range	-65~200	$^{\circ}\text{C}$

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th\ j-c}$	Thermal Resistance, Junction to Case	1.75	$^{\circ}\text{C/W}$



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ELECTRICAL CHARACTERISTICS

 $T_c=25^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C=5A; I_B=0.5A$			2.0	V
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	$I_C=5A; I_B=0.5A$			2.5	V
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage	$I_C=25mA; I_B=0$	110			V
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage	$I_E=1mA; I_C=0$	5			V
h_{FE-1}	DC Current Gain	$I_C=1A; V_{CE}=5V$	30		200	
h_{FE-2}	DC Current Gain	$I_C=5A; V_{CE}=5V$	20			
I_{CBO}	Collector Cutoff Current	$V_{CB}=150V; I_E=0$			100	μA
I_{EBO}	Emitter Cutoff Current	$V_{EB}=5V; I_C=0$			100	μA

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