

isc Silicon NPN Power Transistor

2SD1136

DESCRIPTION

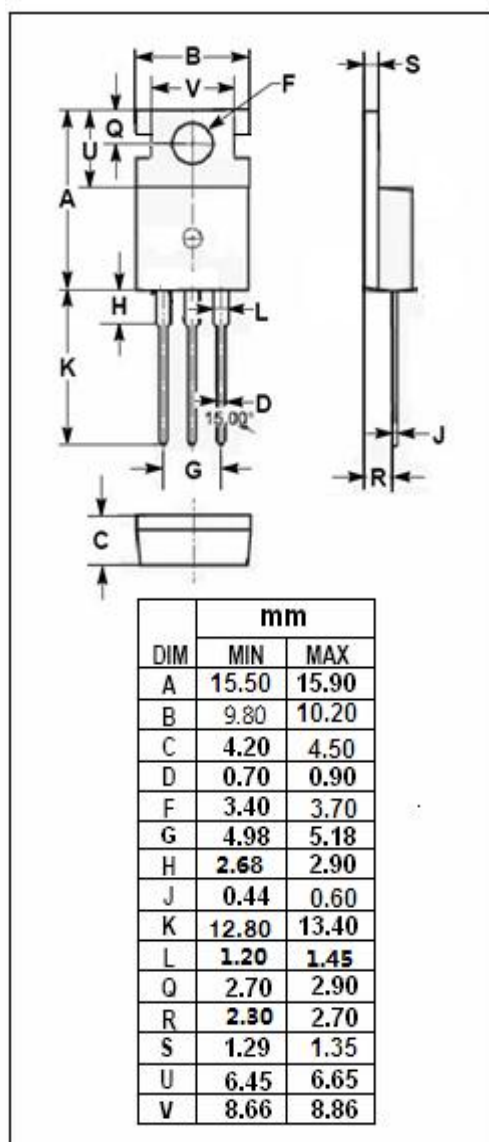
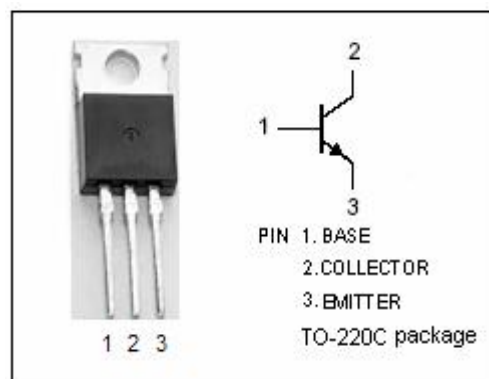
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = 80V$ (Min)
- Wide Area of Safe Operation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for power switching and TV horizontal deflection output applications.

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	200	V
V_{CEO}	Collector-Emitter Voltage	80	V
V_{EBO}	Emitter-Base Voltage	5	V
I_C	Collector Current-Continuous	4	A
I_{CM}	Collector Current-Peak	5	A
$I_{C(surge)}$	Collector Current-Surge	15	A
P_C	Collector Power Dissipation @ $T_a=25^\circ\text{C}$	1.8	W
	Collector Power Dissipation @ $T_c=25^\circ\text{C}$	30	
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-45~150	$^\circ\text{C}$



isc Silicon NPN Power Transistor**2SD1136****ELECTRICAL CHARACTERISTICS****T_c=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 10mA ; R _{BE} = ∞	80			V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = 1mA; I _E = 0	200			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 1mA; I _C = 0	5			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 4A; I _B = 0.4A			1.5	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 4A; I _B = 0.4A			1.5	V
I _{CES}	Collector Cutoff Current	V _{CE} = 150V; R _{BE} = 0			1.0	mA
h _{FE}	DC Current Gain	I _C = 4A; V _{CE} = 5V	20			
t _f	Fall Time	I _C = 3.5A; I _{B1} = 0.45A, L _B = 0			1.0	μs

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