

# **Isc N-Channel MOSFET Transistor**

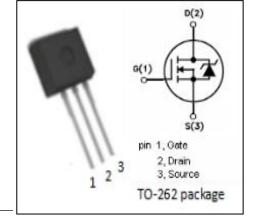
## **IRF3710L**

### • FEATURES

- With To-262 package
- · Low input capacitance and gate charge
- · Low gate input resistance
- · 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

### APPLICATIONS

· Switching applications



## • ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
$V_{DSS}$	Drain-Source Voltage	100	V
$V_{GSS}$	Gate-Source Voltage	±20	٧
I <sub>D</sub>	Drain Current-ContinuousTc=25℃ Tc=100℃	57 40	А
Ірм	Drain Current-Single Pulsed	180	А
$P_D$	Total Dissipation @T <sub>C</sub> =25℃	200	W
T <sub>ch</sub>	Max. Operating Junction Temperature	175	${\mathbb C}$
T <sub>stg</sub>	Storage Temperature	-55~175	${\mathbb C}$

## • THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
Rth(ch-c)	Channel-to-case thermal resistance	0.75	°C/W

D2	LAZ D3 HZ	A3		F
		n	ım	
	DIM	MIN	MAX	
	A	4.37	4.77	
	A1	1.22	1.42	
	A2	2.47	2.87	
	Ъ	0.70	0.97	
	Ъ2	1.17	1.42	
	С	0.28	0.53	
	D	23.20	24.02	
	D1	8.38	8.90	
	D2	6.00	=8	
	E	9.90	10.39	]
	E4	7.30	_	]
	е	2.54BSC		
	G	1.25	1.50	]
	H2	( <del>-</del>	1.31	1
	L	13.34	14.10	1
	L1	3.30	4.06	
	L3	0.95	1.15	



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#### **ELECTRICAL CHARACTERISTICS**

T<sub>c</sub>=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V; I <sub>D</sub> = 0.25mA	100			V
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS}$ = $V_{GS}$ ; $I_D$ =0.25mA	2.0		4.0	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> = 10V; I <sub>D</sub> =28A			25	mΩ
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±20V;V <sub>DS</sub> = 0V			±0.1	μА
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =100V; V <sub>GS</sub> = 0V;Tj=25°C V <sub>DS</sub> =100V; V <sub>GS</sub> = 0V;Tj=125°C			20 250	μА
$V_{SDF}$	Diode forward voltage	I <sub>SD</sub> =28A, V <sub>GS</sub> = 0 V			1.3	V

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