

General Description

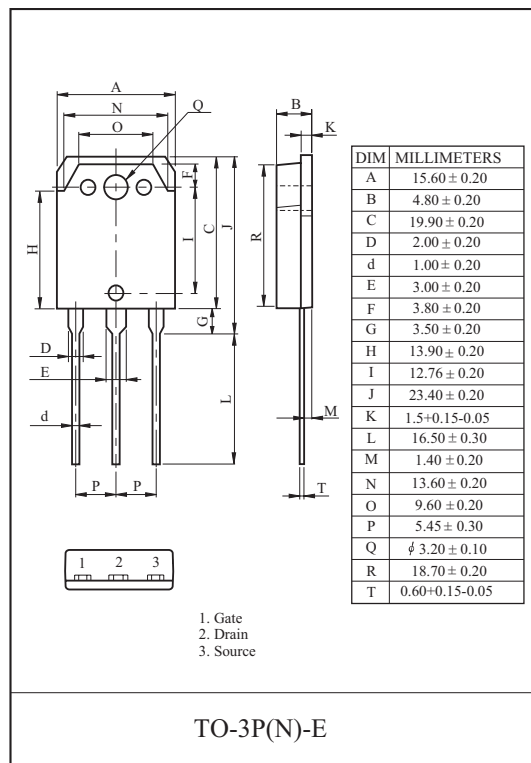
This planar stripe MOSFET has better characteristics, such as fast switching time, low on resistance, low gate charge and excellent avalanche characteristics. It is mainly suitable for switching mode power supplies.

FEATURES

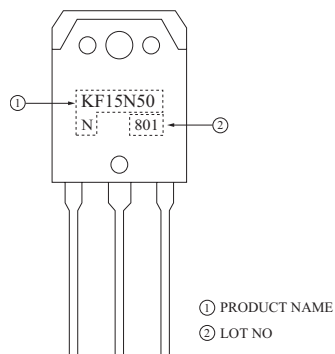
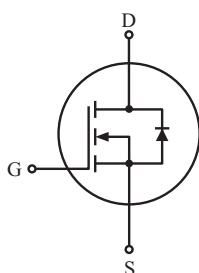
- $V_{DSS(Min.)} = 500V$, $I_D = 15A$
- Drain-Source ON Resistance :
 $R_{DS(ON)} = 0.42(Max.)\Omega$ @ $V_{GS} = 10V$
- $Q_g(typ.) = 35nC$

MAXIMUM RATING (Tc=25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Drain-Source Voltage		V_{DSS}	500	V
Gate-Source Voltage		V_{GSS}	± 30	V
Drain Current	@T _C =25°C	I_D	15	A
	Pulsed (Note1)	I_{DP}	40	
Single Pulsed Avalanche Energy (Note 2)		E_{AS}	860	mJ
Repetitive Avalanche Energy (Note 1)		E_{AR}	19.5	mJ
Peak Diode Recovery dv/dt (Note 3)		dv/dt	4.5	V/ns
Drain Power Dissipation	Tc=25°C	P_D	215	W
	Derate above 25°C		1.72	W/°C
Maximum Junction Temperature		T_j	150	°C
Storage Temperature Range		T_{stg}	-55 150	°C
Thermal Characteristics				
Thermal Resistance, Junction-to-Case		R_{thJC}	0.58	°C/W
Thermal Resistance, Junction-to-Ambient		R_{thJA}	40	°C/W



Marking



KF15N50N

ELECTRICAL CHARACTERISTICS (Tc=25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =250 μA, V _{GS} =0V	500	-	-	V
Breakdown Voltage Temperature Coefficient	Δ BV _{DSS} / Δ T _j	I _D =250 μA, Referenced to 25°C	-	0.6	-	V/°C
Drain Cut-off Current	I _{DSS}	V _{DS} =500V, V _{GS} =0V,	-	-	10	μA
Gate Threshold Voltage	V _{th}	V _{DS} =V _{GS} , I _D =250 μA	2.0	-	4.0	V
Gate Leakage Current	I _{GSS}	V _{GS} =±30V, V _{DS} =0V	-	-	±100	nA
Drain-Source ON Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =7.5A	-	0.34	0.42	Ω
Dynamic						
Total Gate Charge	Q _g	V _{DS} =400V, I _D =15A V _{GS} =10V (Note4,5)	-	35	-	nC
Gate-Source Charge	Q _{gs}		-	9	-	
Gate-Drain Charge	Q _{gd}		-	13	-	
Turn-on Delay time	t _{d(on)}	V _{DD} =250V, R _G =25Ω, I _D =15A (Note4,5)	-	28	-	ns
Turn-on Rise time	t _r		-	45	-	
Turn-off Delay time	t _{d(off)}		-	105	-	
Turn-off Fall time	t _f		-	55	-	
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V, f=1.0MHz	-	1445	-	pF
Output Capacitance	C _{oss}		-	210	-	
Reverse Transfer Capacitance	C _{rss}		-	20	-	
Source-Drain Diode Ratings						
Continuous Source Current	I _S	V _{GS} <V _{th}	-	-	15	A
Pulsed Source Current	I _{SP}		-	-	60	
Diode Forward Voltage	V _{SD}	I _S =15A, V _{GS} =0V	-	0.9	1.4	V
Reverse Recovery Time	t _{rr}	I _S =15A, V _{GS} =0V,	-	360	-	ns
Reverse Recovery Charge	Q _{rr}	dI _S /dt=100A/ μs	-	4.4	-	μC

Note 1) Repetivity rating : Pulse width limited by junction temperature.

Note 2) $L = 6.8mH$, $I_{AS}=15A$, $V_{DD}=50V$, $R_G = 25\Omega$, Starting $T_j = 25^\circ C$

Note 3) $I_S \leq 15A$, $dI/dt \leq 200A/\mu s$, $V_{DD} \leq BV_{DSS}$, Starting $T_j = 25^\circ C$.

Note 4) Pulse Test : Pulse width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.

Note 5) Essentially independent of operating temperature.

Fig1. $I_D - V_{DS}$

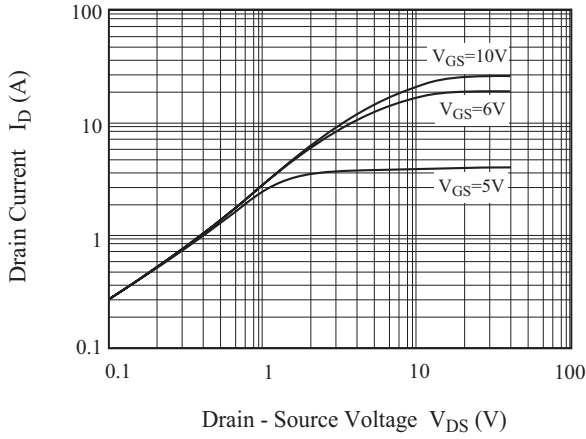


Fig2. $I_D - V_{GS}$

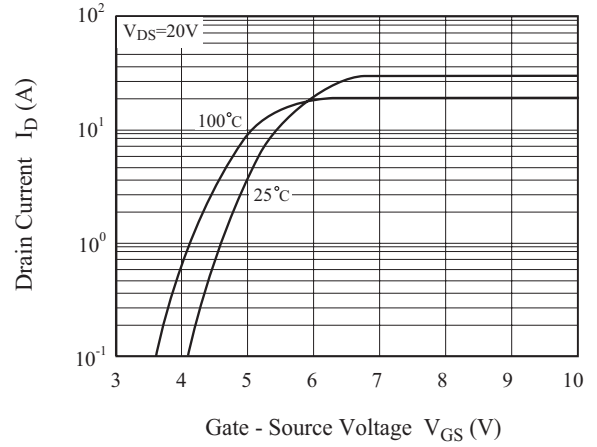


Fig3. $BV_{DSS} - T_j$

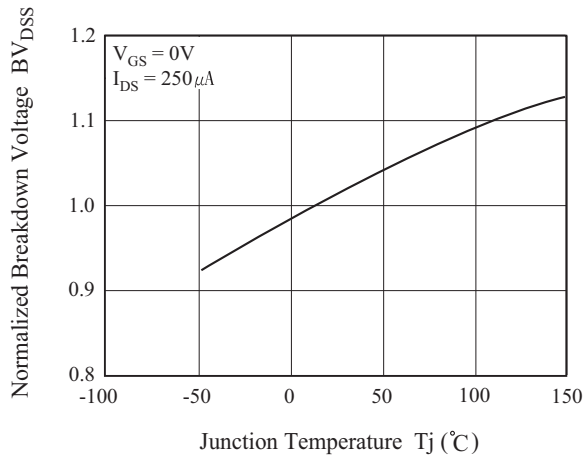


Fig4. $R_{DS(ON)} - I_D$

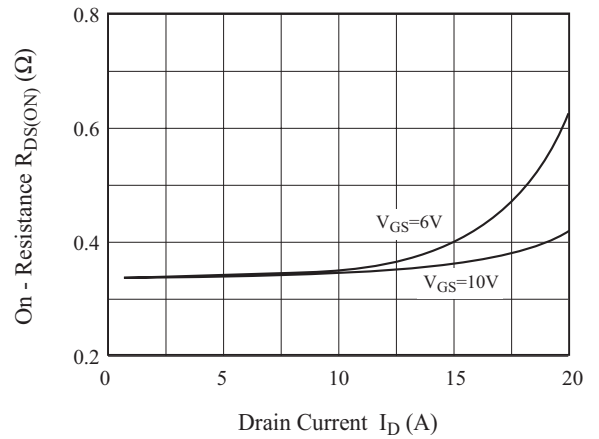


Fig5. $I_S - V_{SD}$

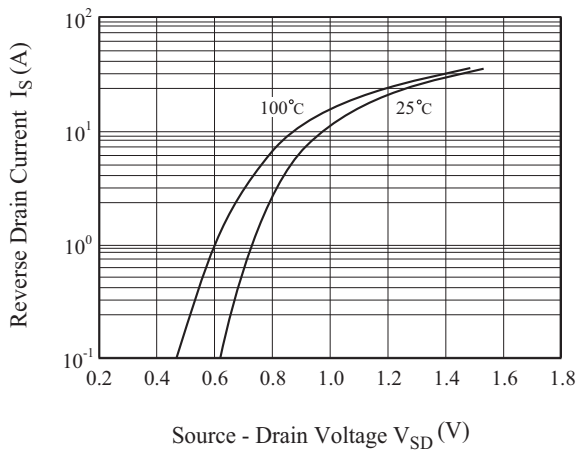
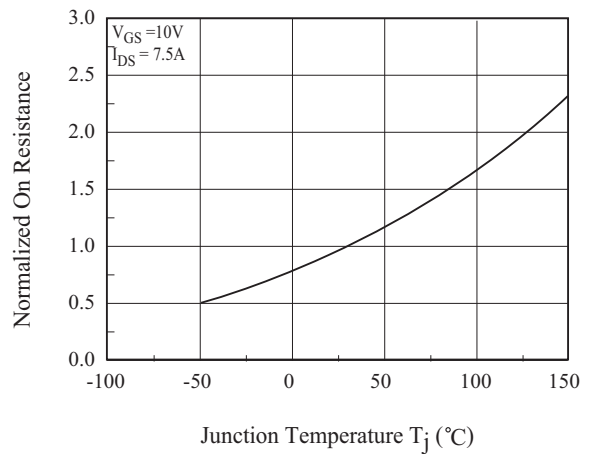


Fig6. $R_{DS(ON)} - T_j$



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Fig 7. C - V_{DS}

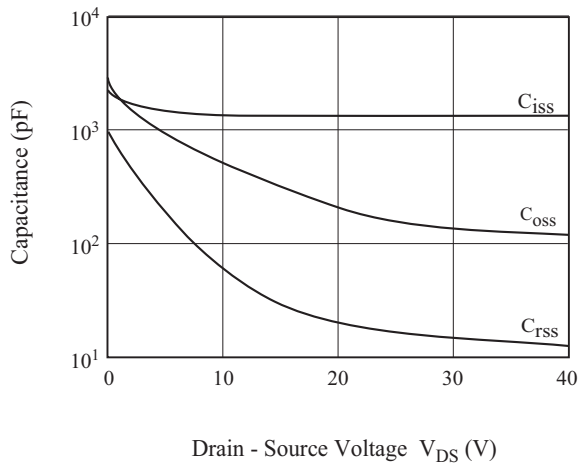


Fig8. Q_g - V_{GS}

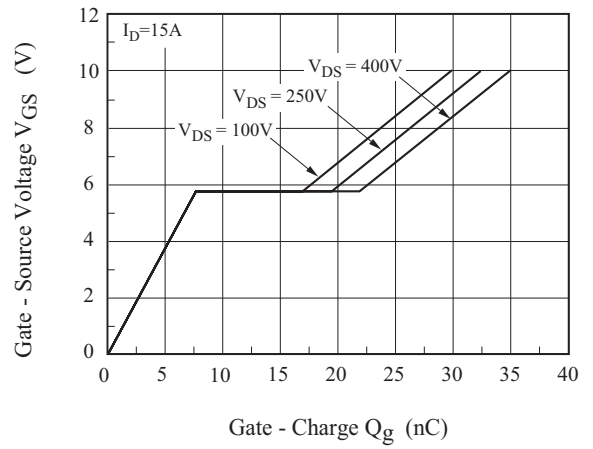


Fig9. Safe Operation Area

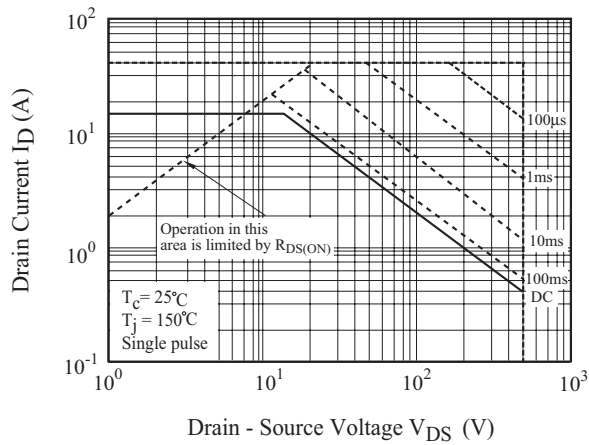


Fig10. I_D - T_j

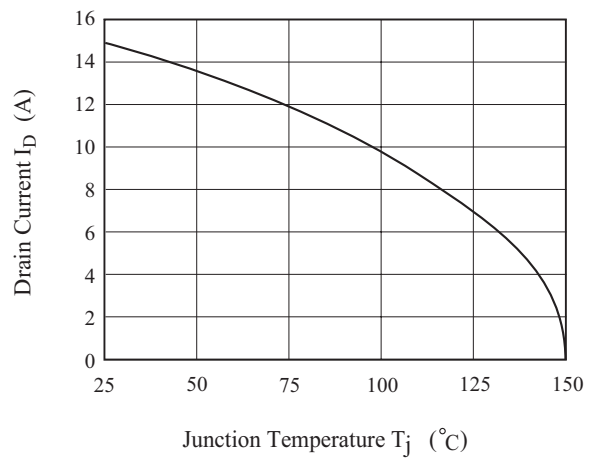


Fig11. Transient Thermal Response Curve

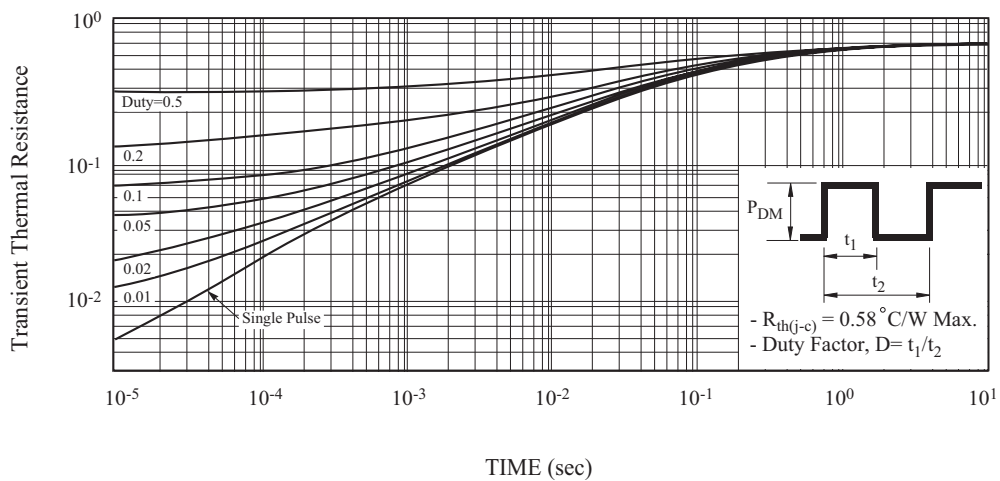


Fig12. Gate Charge

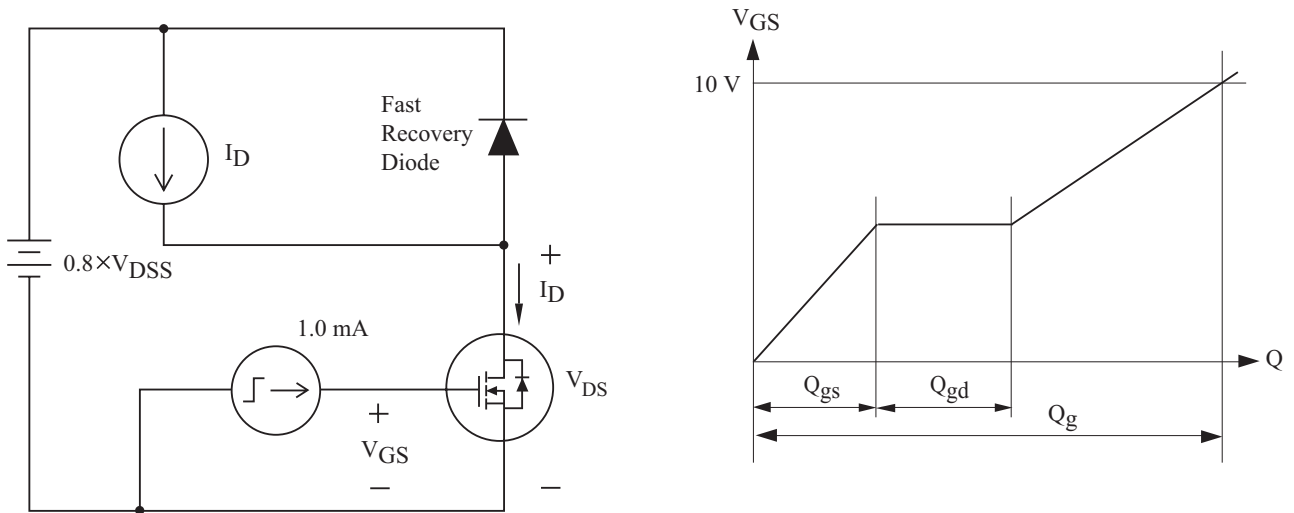


Fig13. Single Pulsed Avalanche Energy

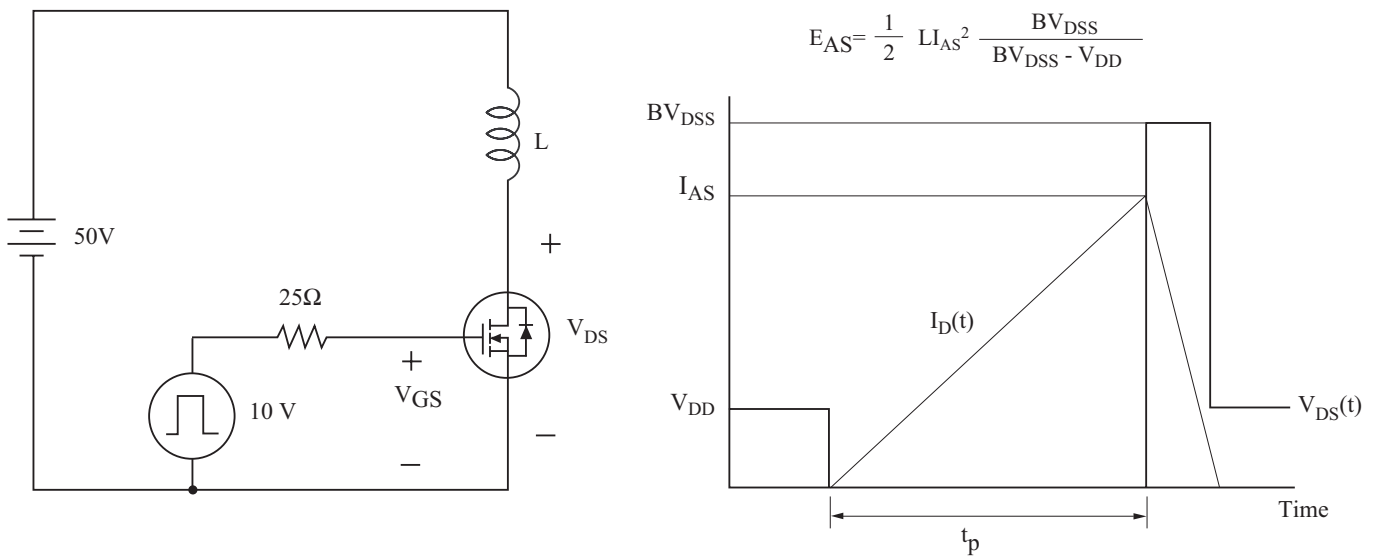


Fig14. Resistive Load Switching

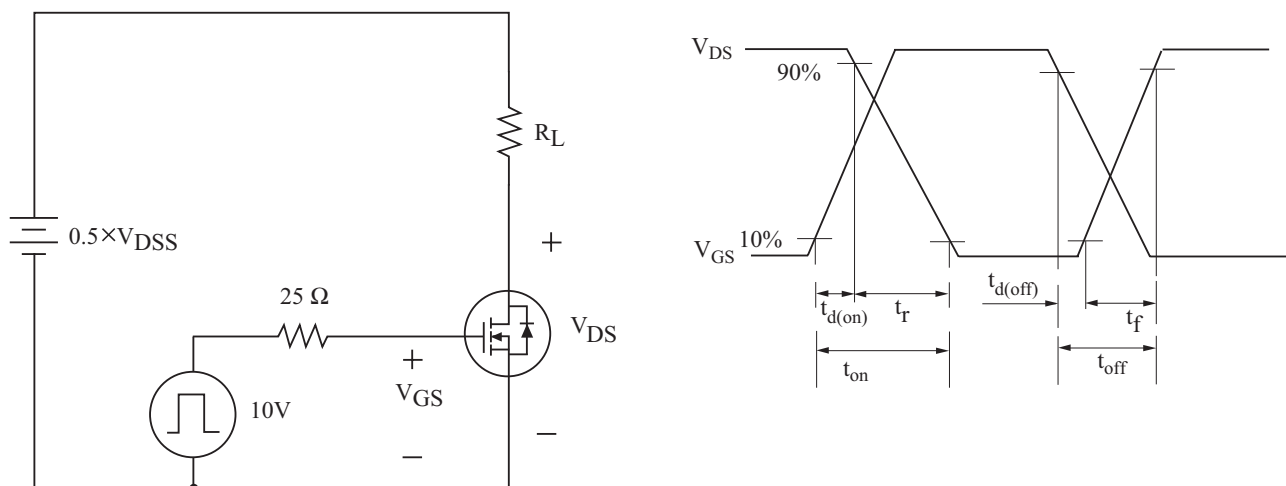


Fig15. Source - Drain Diode Reverse Recovery and dv/dt

