

PJU3NA50 / PJD3NA50

500V N-Channel MOSFET

Voltage

500 V

Current

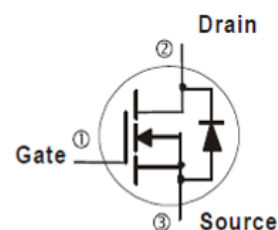
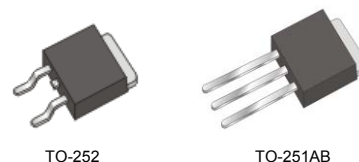
3 A

Features

- $R_{DS(ON)}, V_{GS}@10V, I_D@1.5A < 3.2\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std.
(Halogen Free)

Mechanical Data

- Case : TO-251AB , TO-252 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- TO-251AB Approx. Weight : 0.0104 ounces, 0.297grams
- TO-252 Approx. Weight : 0.0104 ounces, 0.297grams



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER		SYMBOL	TO-251AB	TO-252	UNITS
Drain-Source Voltage		V_{DS}	500		V
Gate-Source Voltage		V_{GS}	± 30		V
Continuous Drain Current		I_D	3		A
Pulsed Drain Current		I_{DM}	12		A
Single Pulse Avalanche Energy ^(Note 1)		E_{AS}	120		mJ
Power Dissipation	$T_C=25^\circ\text{C}$	P_D	34	34	W
	Derate above 25°C		0.27	0.27	W/ $^\circ\text{C}$
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~150		$^\circ\text{C}$
Typical Thermal resistance					$^\circ\text{C/W}$
- Junction to Case		$R_{\theta JC}$	3.68	3.68	
- Junction to Ambient		$R_{\theta JA}$	110	110	

- Limited only By Maximum Junction Temperature



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Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

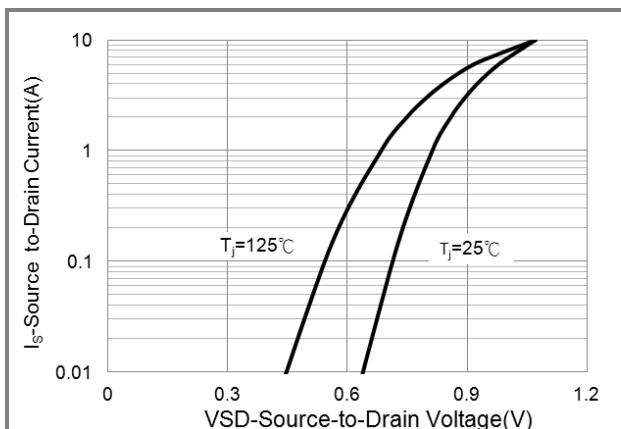
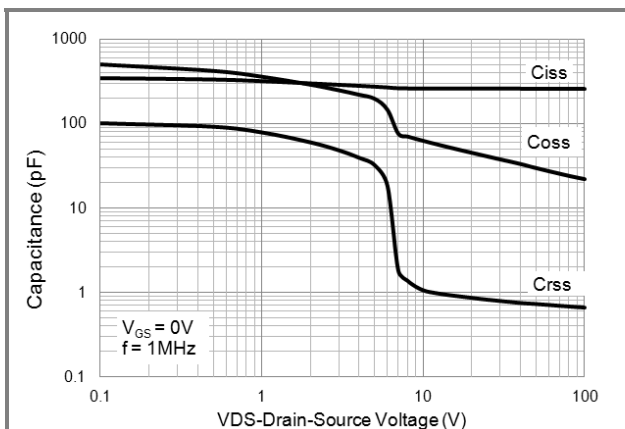
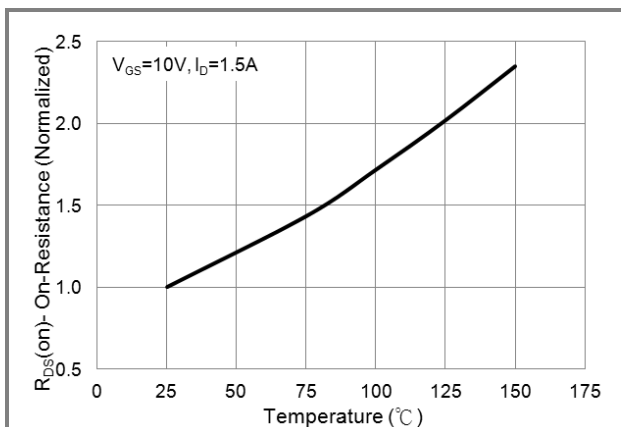
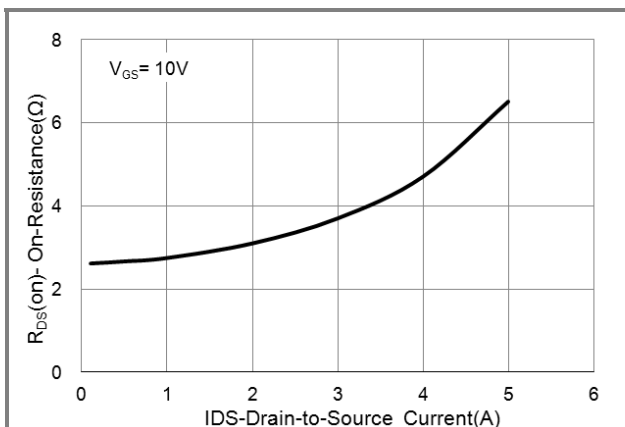
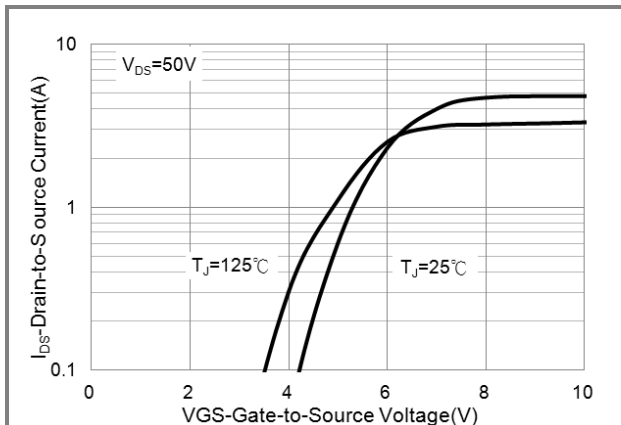
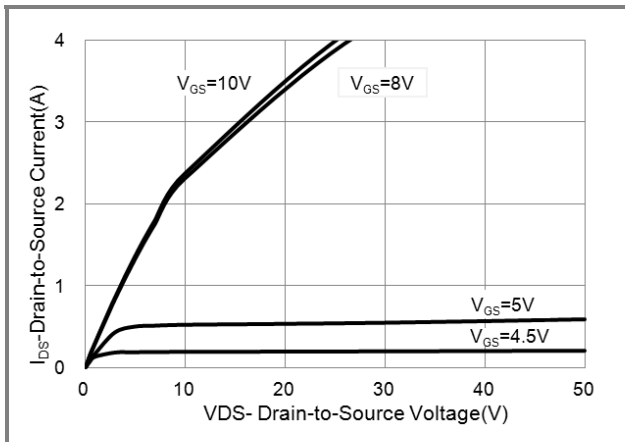
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	500	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	2	3.08	4	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =1.5A	-	3	3.2	Ω
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =500V, V _{GS} =0V	-	0.01	1.0	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±30V, V _{DS} =0V	-	±10	±100	nA
Diode Forward Voltage	V _{SD}	I _S =3A, V _{GS} =0V	-	0.9	1.4	V
Dynamic (Note 4)						
Total Gate Charge	Q _g	V _{DS} =400V, I _D =3A, V _{GS} =10V (Note 2,3)	-	6.5	-	nC
Gate-Source Charge	Q _{gs}		-	2	-	
Gate-Drain Charge	Q _{gd}		-	2.8	-	
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V, f=1.0MHZ	-	260	-	pF
Output Capacitance	C _{oss}		-	41.3	-	
Reverse Transfer Capacitance	C _{rss}		-	0.8	-	
Turn-On Delay Time	td _(on)	V _{DD} =250V, I _D =3A, R _G =25Ω (Note 2,3)	-	6.1	-	ns
Turn-On Rise Time	t _r		-	20.6	-	
Turn-Off Delay Time	td _(off)		-	8.4	-	
Turn-Off Fall Time	t _f		-	21.4	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I _S	---	-	-	3	A
Maximum Pulsed Drain-Source Diode Forward Current	I _{SM}	---	-	-	12	A
Reverse Recovery Time	trr	V _{GS} =0V, I _S =3A	-	381	-	ns
Reverse Recovery Charge	Qrr	dl _F / dt=100A/us (Note 2)	-	1.4	-	uC

NOTES :

1. $L=30\text{mH}, I_{AS}=2.75A, V_{DD}=50V, R_G=25\Omega$, Starting $T_J=25^{\circ}\text{C}$
2. Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$
3. Essentially independent of operating temperature typical characteristics.
4. Guaranteed by design, not subject to production testing

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TYPICAL CHARACTERISTIC CURVES



PJU3NA50 / PJD3NA50

TYPICAL CHARACTERISTIC CURVES

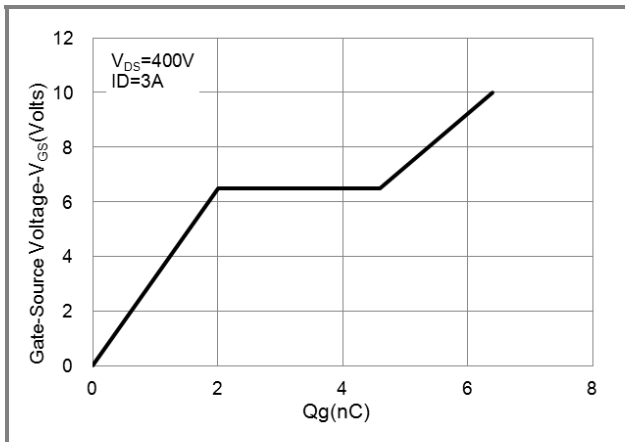


Fig.7 Gate-Charge Characteristics

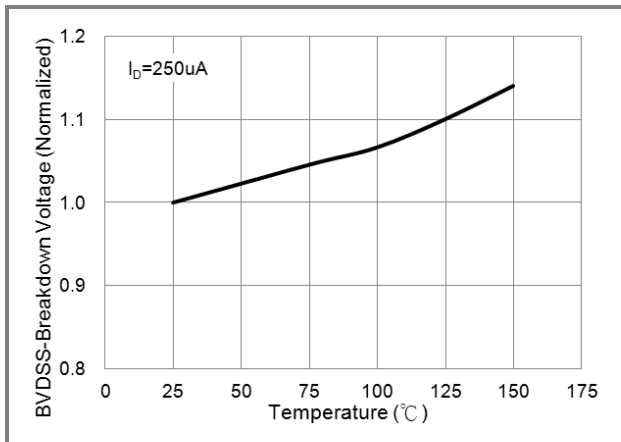


Fig.8 Breakdown Voltage Variation vs. Temperature

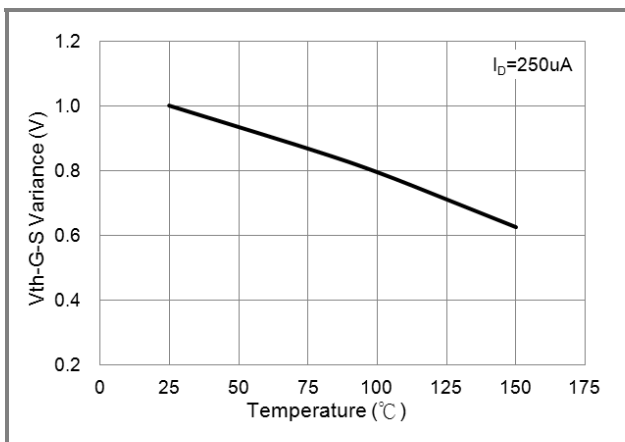


Fig.9 Threshold Voltage Variation with Temperature

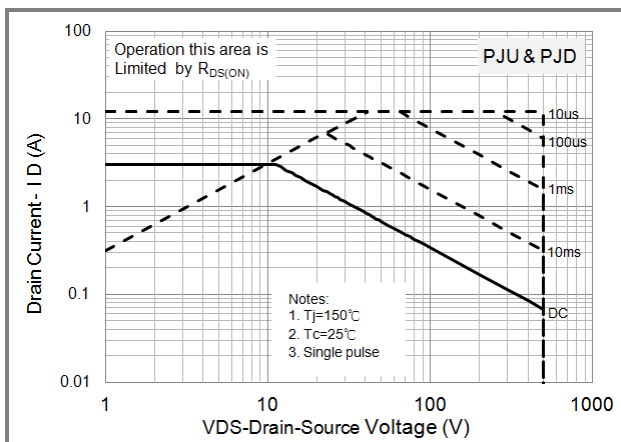


Fig.10 Maximum Safe Operating Area

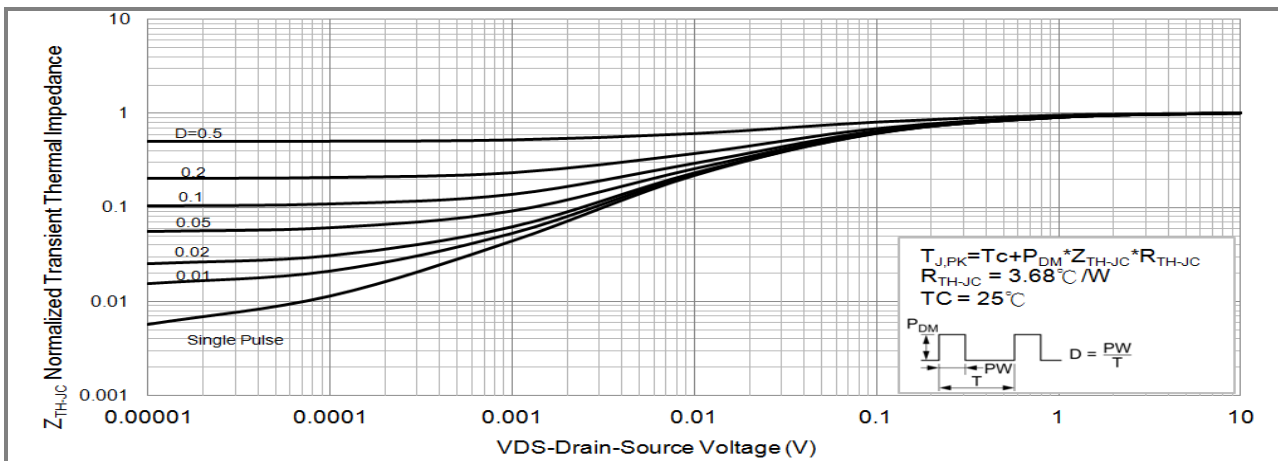
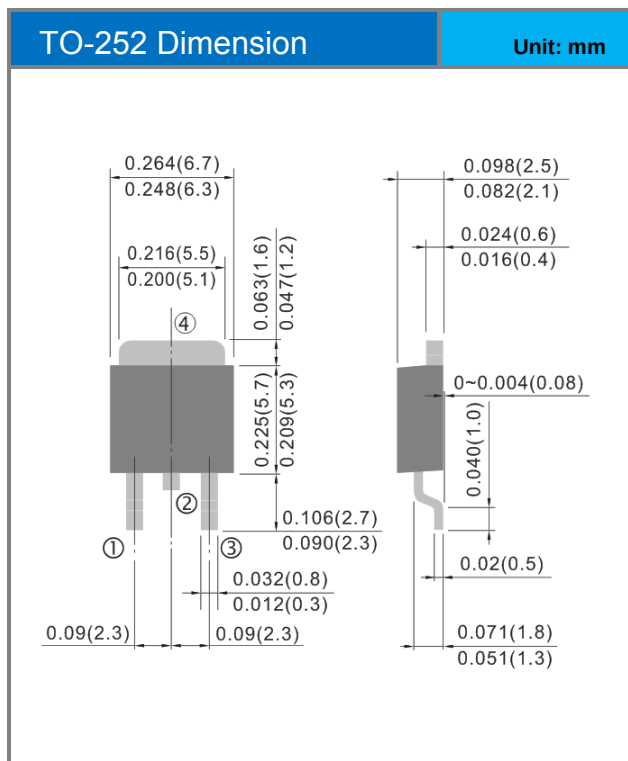
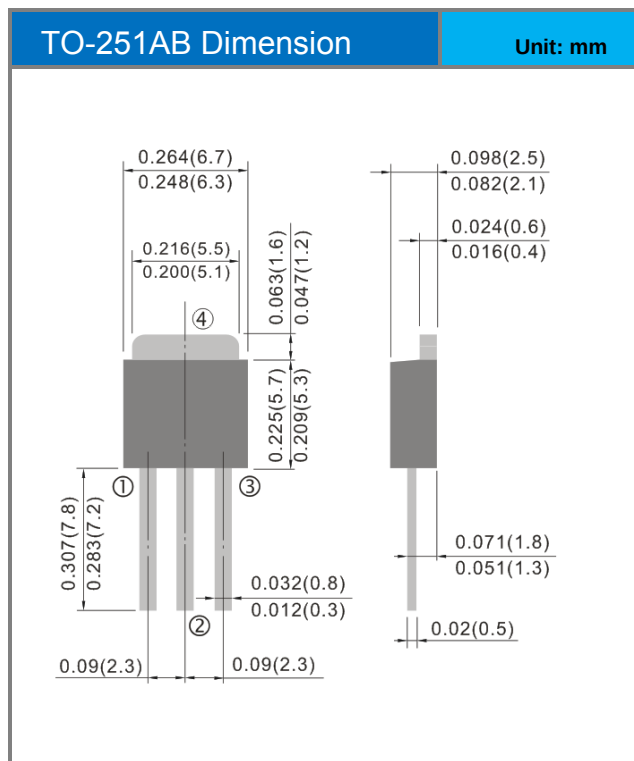


Fig.11 PJU/PJD Normalized Transient Thermal Impedance vs. Pulse Width

PJU3NA50 / PJD3NA50

Packaging Information





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PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJU3NA50_T0_00001	TO-251AB	80pcs / Tube	U3NA50	Halogen free
PJD3NA50_L2_00001	TO-252	3,000pcs / 13" reel	D3NA50	Halogen free



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