4V Drive Pch+SBD MOSFET us5u35

●Structure

Silicon P-channel MOSFET Schottky Barrier DIODE

Features

- 1) The US5U35 combines Pch MOSFET with a Schottky barrier diode in a TUMT5 package.
- 2) With fast switching.
- 3) Built-in schottky barrier diode has low forward voltage.

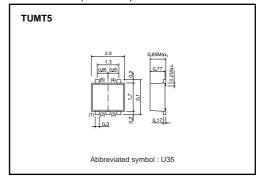
Applications

Switching

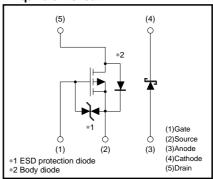
Packaging specifications

	Package	Taping		
Туре	Code	TR		
	Basic ordering unit (pieces)	3000		
US5U35		0		

●Dimensions (Unit: mm)



●Equivalent circuit



● Absolute maximum ratings (Ta=25°C)

<MOSFET>

Parameter		Symbol	Limits	Unit
Drain-source voltage	V _{DSS}	-45	V	
Gate-source voltage	V _{GSS}	±20	V	
Drain current	Continuous	ΙD	±0.7	A
	Pulsed	I _{DP} *1	±2.8	A
Source current	Continuous	Is	-0.4	А
(Body diode)	Pulsed	I _{SP} *1	-2.8	A
Channel temperature	Tch	150	°C	
Power dissipation	P _D *3	0.7	W / ELEMENT	
<di></di>				
Repetitive peak reverse volt	V _{RM}	45	V	
Reverse voltage	V_R	40	V	
Forward current	l _F	100	mA	
Forward current surge peak	I _{FSM} *2	1.0	Α	
Junction temperature	Tj	150	°C	
Power dissipation	P _D *3	0.5	W / ELEMENT	
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Power dissipation	P _D *3	1.0	W / TOTAL	
Range of storage temperatu	Tstg	-55 to +150	°C	

^{*1} Pw≤10μs, Duty cycle≤1% *2 60Hz•1cyc. *3 Mounted on a ceramic board

●Electrical characteristics (Ta=25°C)

<MOSFET>

WOOT LTZ						
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	-	-	±10	μА	Vgs=±20V, Vps=0V
Drain-source breakdown voltage	V(BR) DSS	-45	_	_	V	ID=-1mA, VGS=0V
Zero gate voltage drain current	IDSS	_	-	-1	μА	Vps=-45V, Vgs=0V
Gate threshold voltage	VGS (th)	-1.0	_	-2.5	V	VDS=-10V, ID=-1mA
Static drain-source on-state resistance	RDS (on)	-	0.6	0.8	Ω	ID=-0.7A, VGS=-10V
		_	0.9	1.3	Ω	ID=-0.7A, VGS=-4.5V
		_	1.0	1.4	Ω	ID=-0.35A, VGS=-4.0V
Forward transfer admittance	Y _{fs} *	0.6	_	_	S	Vps=-10V, Ip=-0.7A
Input capacitance	Ciss	-	120	_	pF	V _{DS} =-10V
Output capacitance	Coss	-	14	_	pF	Vgs=0V
Reverse transfer capacitance	Crss	_	11	_	pF	f=1MHz
Turn-on delay time	td (on) *	_	6	_	ns	ID=-0.35A
Rise time	tr *	_	5	_	ns	V _{DD} ≒–25V V _{GS} =–10V
Turn-off delay time	td (off) *	_	17	_	ns	VGS=-10V RL≒71Ω
Fall time	t _f *	-	6	_	ns	R _G =10Ω
Total gate charge	Qg	-	1.7	_	nC	V _{DD} ≒-25V, V _{GS} =-5V
Gate-source charge	Qgs	_	0.8	-	nC	ID=-0.7A
Gate-drain charge	Qgd	-	0.5	_	nC	$R_L = 36\Omega$, $R_G = 10\Omega$
* Pulsed						·

<Body diode (source-drain)>

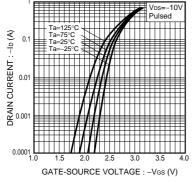
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsp *	-	_	-1.2	٧	Is=-0.7A, Vgs=0V
* Pulsed						

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Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	VF	_	-	0.55	V	I _F =100mA
Reverse current	l _R	_	_	30	μΑ	V _R =10V



Electrical characteristic curves



DRAIN CURRENT : -ID (A)

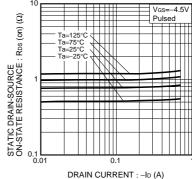


Fig.1 Typical Transfer Characteristics

Fig.2 Static Drain-Source On-State Resistance vs. Drain Current(I)

Fig.3 Static Drain-Source On-State Resistance vs. Drain Current(II)

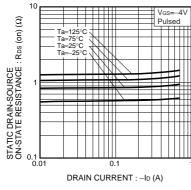


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current(III)

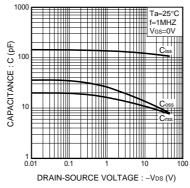


Fig.5 Typical Capacitance vs. Drain-Source Voltage

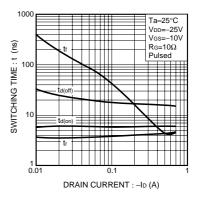


Fig.6 Switching Characteristics

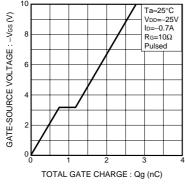


Fig.7 Dynamic Input Characteristics

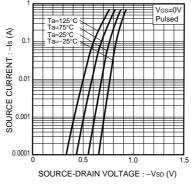


Fig.8 Source Current vs. Source-Drain Voltage

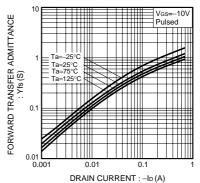


Fig.9 Forward Transfer Admittance vs.Drain Current

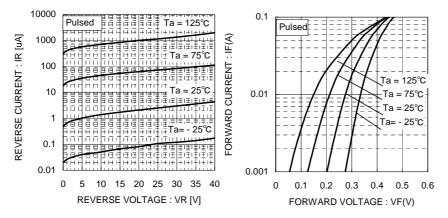


Fig.10 Reverse Current vs. Reverse Voltage

Fig.11 Forward Current vs. Forward Voltage

Notice

- 1. SBD has a large reverse leak current compared to other type of diode. Therefore; it would raise a junction temperature, and increase a reverse power loss. Further rise of inside temperature would cause a thermal runaway.
 This built-in SBD has low V_F characteristics and therefore, higher leak current. Please consider enough the surrounding temperature, generating heat of MOSFET and the reverse current.
- 2. This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

Measurement circuits

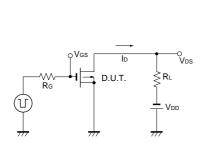


Fig.12 Switching Time Measurement Circuit

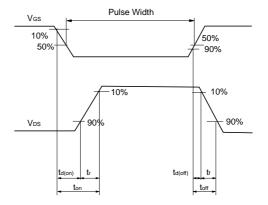


Fig.13 Switching Waveforms

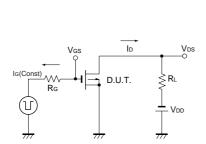


Fig.14 Gate Charge Measurement Circuit

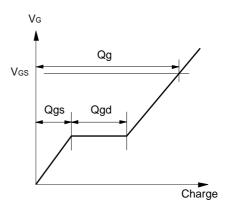


Fig.15 Gate Charge Waveforms

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