

**FTS1012**

Load Switching Applications

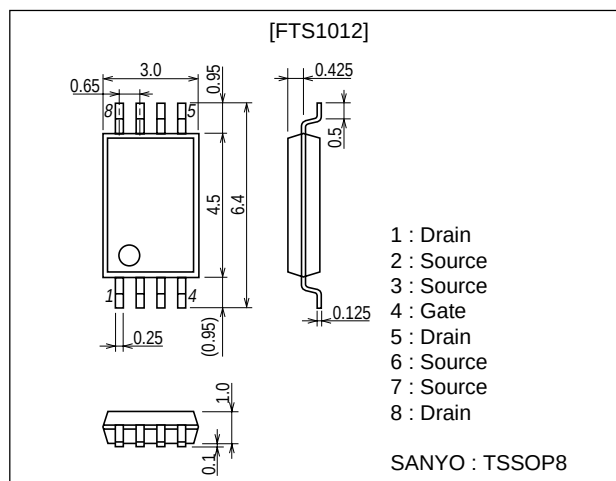
Features

- Low ON-resistance.
- 4.0V drive.
- Mounting height 1.1mm.

Package Dimensions

unit : mm

2147A



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DS}		-30	V
Gate-to-Source Voltage	V_{GS}		± 20	V
Drain Current (DC)	I_D		-6	A
Drain Current (Pulse)	I_{DP}	$PW \leq 10\mu s$, duty cycle $\leq 1\%$	-32	A
Allowable Power Dissipation	P_D	Mounted on a ceramic board (1000mm ² X0.8mm)	1.3	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = -1mA$, $V_{GS} = 0$	-30			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -30V$, $V_{GS} = 0$			-1	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS} = \pm 16V$, $V_{DS} = 0$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS} = -10V$, $I_D = -1mA$	-1.0		-2.4	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS} = -10V$, $I_D = -6A$	8.7	12		S

Marking : S1012

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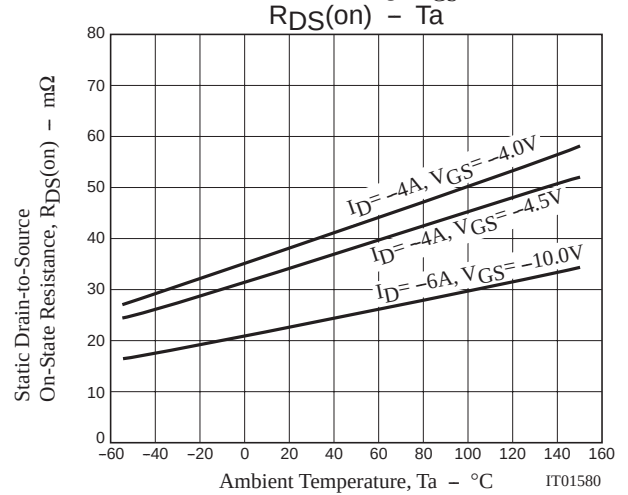
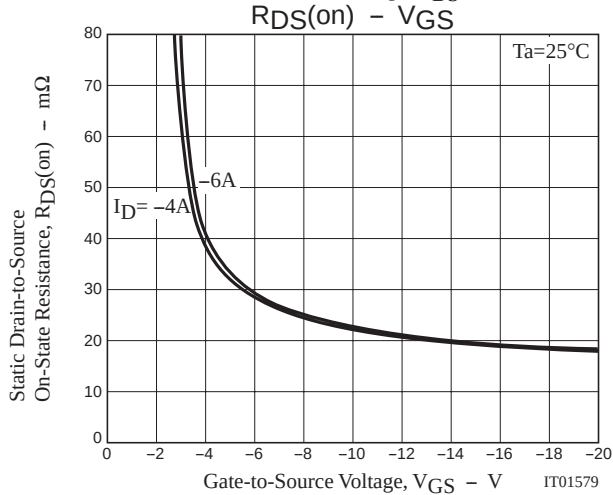
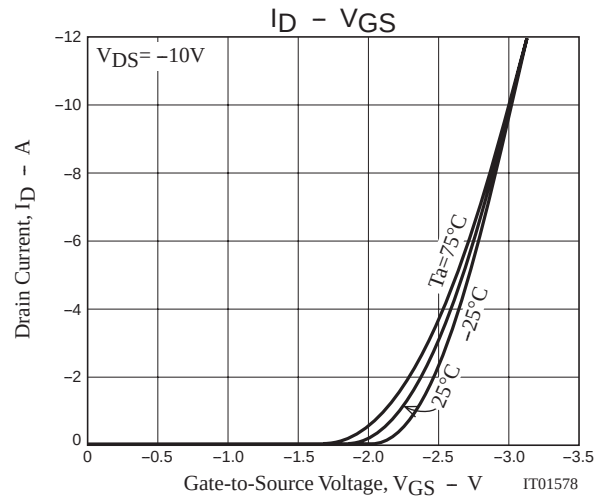
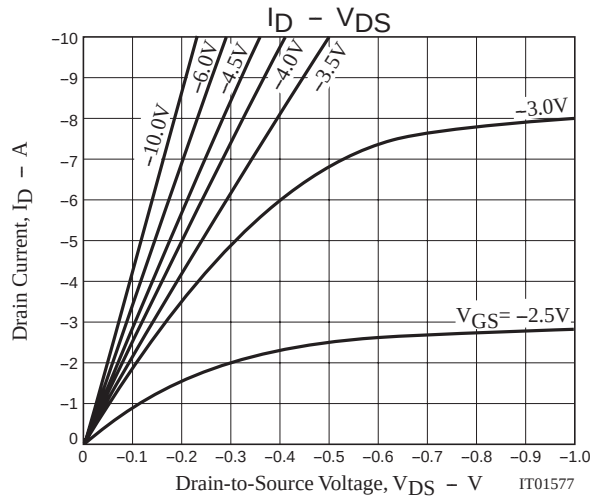
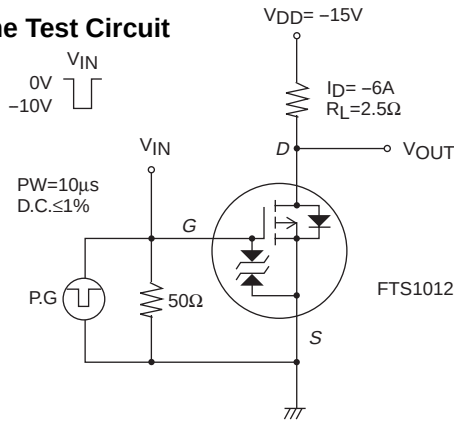
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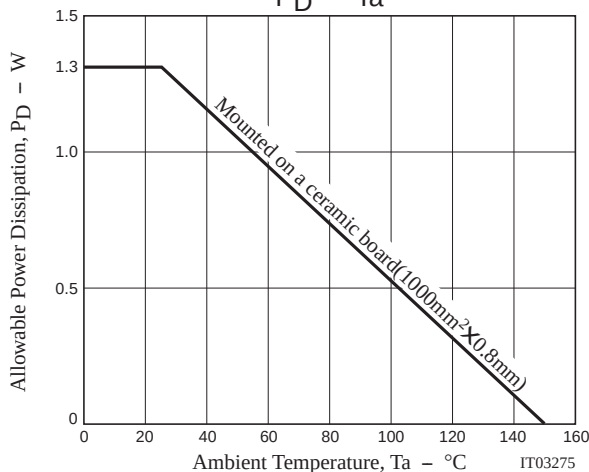
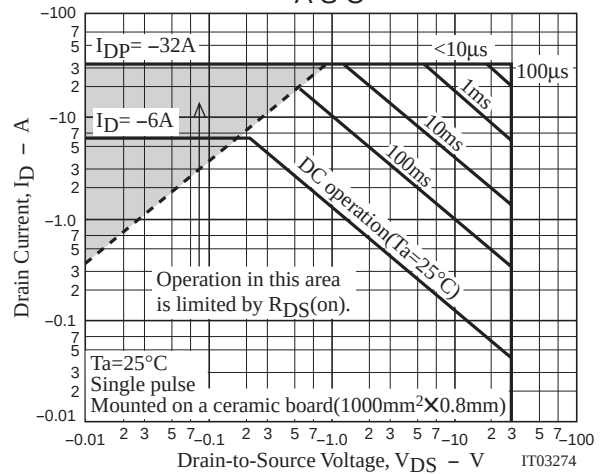
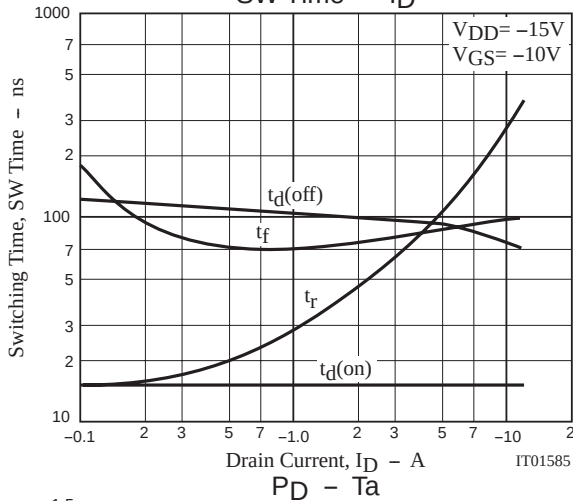
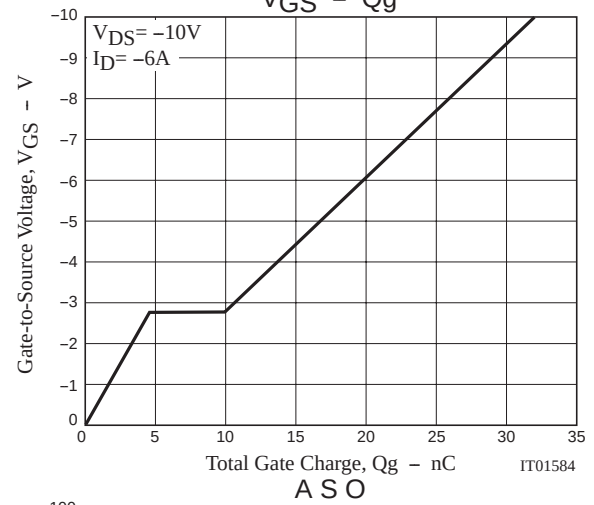
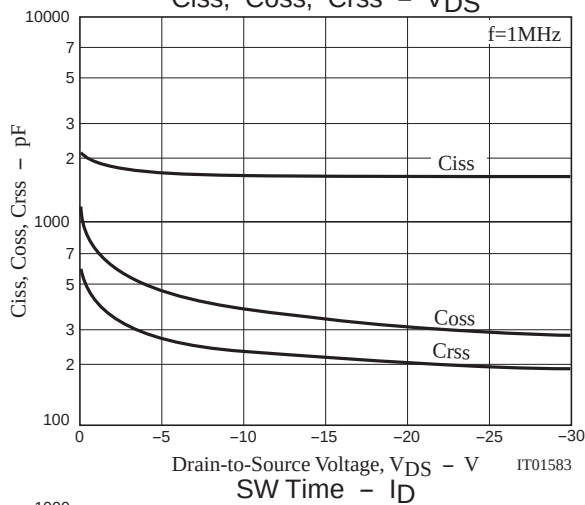
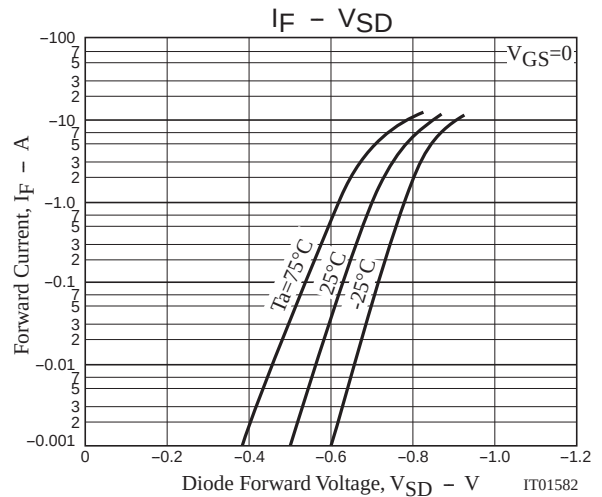
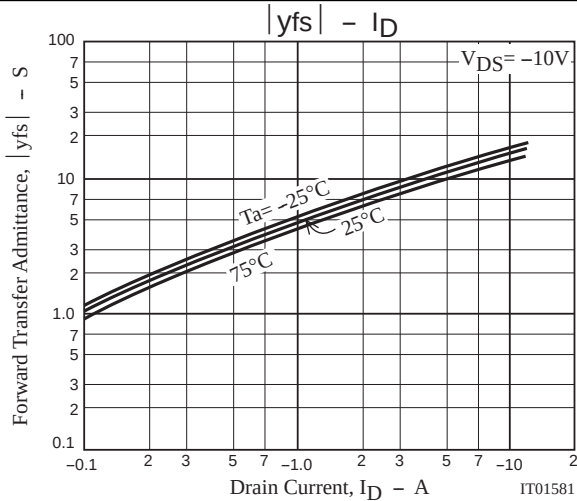
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D = -6A, V_{GS} = -10V$		21	28	$m\Omega$
	$R_{DS(on)2}$	$I_D = -4A, V_{GS} = -4.5V$		33	47	$m\Omega$
	$R_{DS(on)3}$	$I_D = -4A, V_{GS} = -4V$		37	52	$m\Omega$
Input Capacitance	C_{iss}	$V_{DS} = -10V, f = 1MHz$		1700		pF
Output Capacitance	C_{oss}	$V_{DS} = -10V, f = 1MHz$		380		pF
Reverse Transfer Capacitance	C_{rss}	$V_{DS} = -10V, f = 1MHz$		240		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit		15		ns
Rise Time	t_r	See specified Test Circuit		130		ns
Turn-OFF Delay Time	$t_d(off)$	See specified Test Circuit		110		ns
Fall Time	t_f	See specified Test Circuit		85		ns
Total Gate Charge	Q_g	$V_{DS} = -10V, V_{GS} = -10V, I_D = -6A$		32		nC
Gate-to-Source Charge	Q_{gs}	$V_{DS} = -10V, V_{GS} = -10V, I_D = -6A$		4.5		nC
Gate-to-Drain "Miller" Charge	Q_{gd}	$V_{DS} = -10V, V_{GS} = -10V, I_D = -6A$		5		nC
Diode Forward Voltage	V_{SD}	$I_S = -6A, V_{GS} = 0$		-0.79	-1.5	V

Switching Time Test Circuit





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