

KSH13006A



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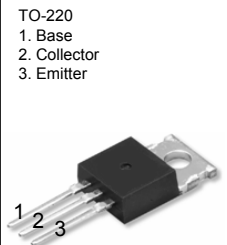
Switch Mode series NPN silicon Power Transistor

- High voltage, high speed power switching
- Suitable for switching regulator, inverters motor controls

Absolute Maximum Ratings $T_C=25^{\circ}\text{C}$ unless otherwise noted

CHARACTERISTICS	SYMBOL	RATING	UNIT
Collector-Base Voltage	V_{CBO}	700	V
Collector-Emitter Voltage	V_{CEO}	400	V
Emitter-Base Voltage	V_{EBO}	9	V
Collector Current(DC)	I_C	8	A
Collector Current(Pulse)	I_{CP}	16	A
Base Current	I_B	4	A
Collector Dissipation($T_C=25^{\circ}\text{C}$)	P_C	80	W
Junction Temperature	T_J	150	$^{\circ}\text{C}$
Storage Temperature	T_{STG}	-65~150	$^{\circ}\text{C}$

8 Amperes
NPN Silicon Power Transistor
80 Watts



Electrical Characteristics $T_C=25^{\circ}\text{C}$ unless otherwise noted

CHARACTERISTICS	SYMBOL	Test Condition	Min	Typ.	Max	Unit
Collector-Emitter Breakdown Voltage	V_{CEO}	$I_C=10\text{mA}, I_B=0$	400			V
Emitter Cut-off Current	I_{EBO}	$V_{EB}=9\text{V}, I_C=0$			1	mA
*DC Current Gain	h_{FE1} h_{FE2}	$V_{CE}=5\text{V}, I_C=2\text{A}$ $V_{CE}=5\text{V}, I_C=5\text{A}$	8 5		60 30	
*Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C=2\text{A}, I_B=0.4\text{A}$ $I_C=5\text{A}, I_B=1\text{A}$ $I_C=8\text{A}, I_B=2\text{A}$			1 2 3	V V V
*Base-Emitter Saturation Voltage	$V_{BE(sat)}$	$I_C=2\text{A}, I_B=0.4\text{A}$ $I_C=5\text{A}, I_B=1\text{A}$			1.2 1.6	V V
Output Capacitance	C_{ob}	$V_{CB}=10\text{V}, f=0.1\text{MHz}$		80		pF
Current Gain Bandwidth Product	f_T	$V_{CE}=10\text{V}, I_C=0.5\text{A}$	4			MHz
Turn on Time	t_{on}	$V_{CC}=125\text{V}, I_C=5\text{A}$ $I_{B1}=1\text{A}, I_{B2}=-1\text{A}$ $R_L=50\Omega$			1.6	μs
Storage Time	t_{stg}				3.0	μs
Fall Time	t_F				0.7	μs

* Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$

Note.

Package Mark information.

hFE1 Classification	R	15 ~ 28	S YWW Z KSH13006A	S	SemiHow Symbol
	O	26 ~ 39		YWW	Y; year code, WW; week code
		O1(26~33), O2(31~39)		Z	hFE1 Classification
	Y	37 ~ 50			

Typical Characteristics



Figure 1. DC current Gain

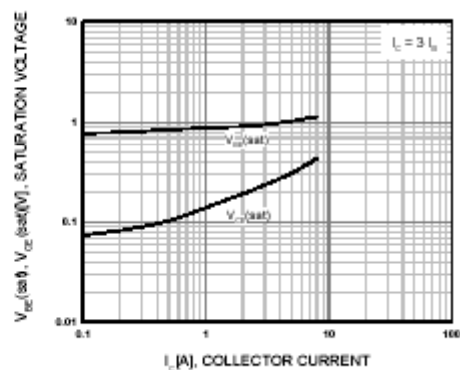
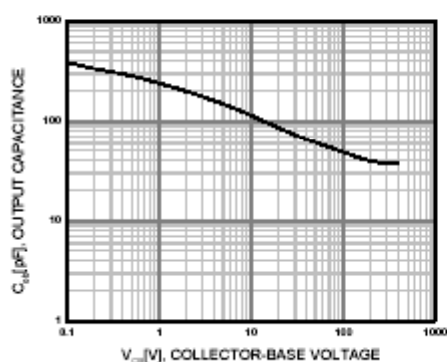
Figure 2. Base-Emitter Saturation Voltage
Collector-Emitter Saturation Voltage

Figure 3. Collector Output Capacitance

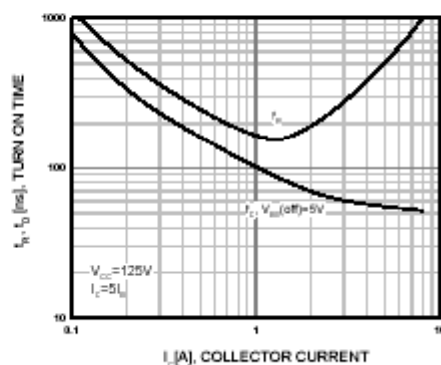


Figure 4. Turn On Time

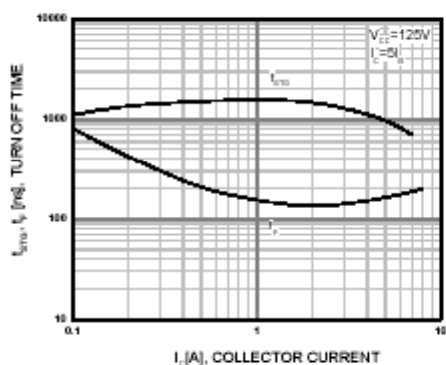


Figure 5. Turn Off Time

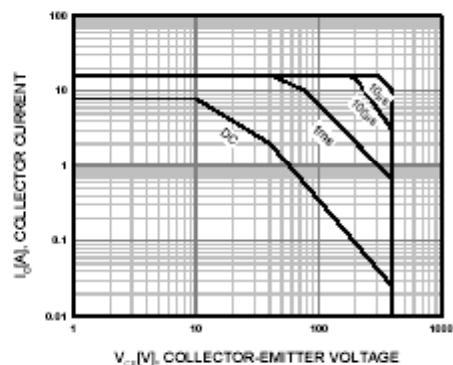


Figure 6. Forward Bias Safe Operating Area

Typical Characteristics (Continued)

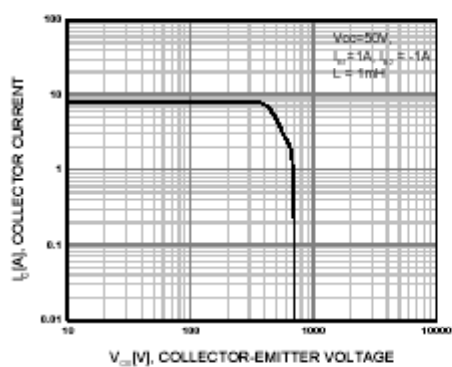


Figure 7. Reverse Bias Safe Operating Area

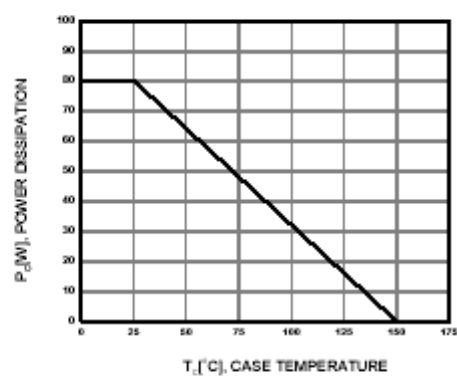
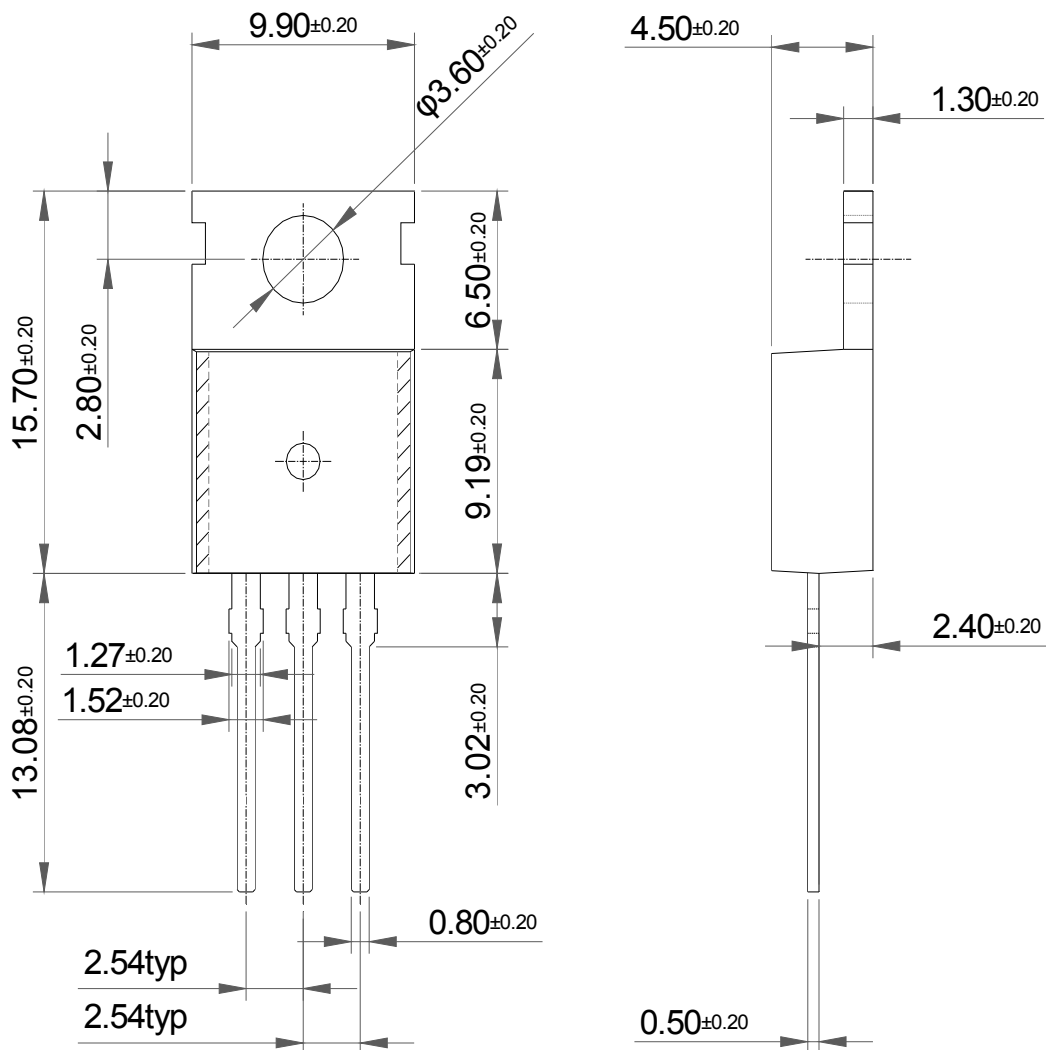


Figure 8. Power Derating

Package Dimension

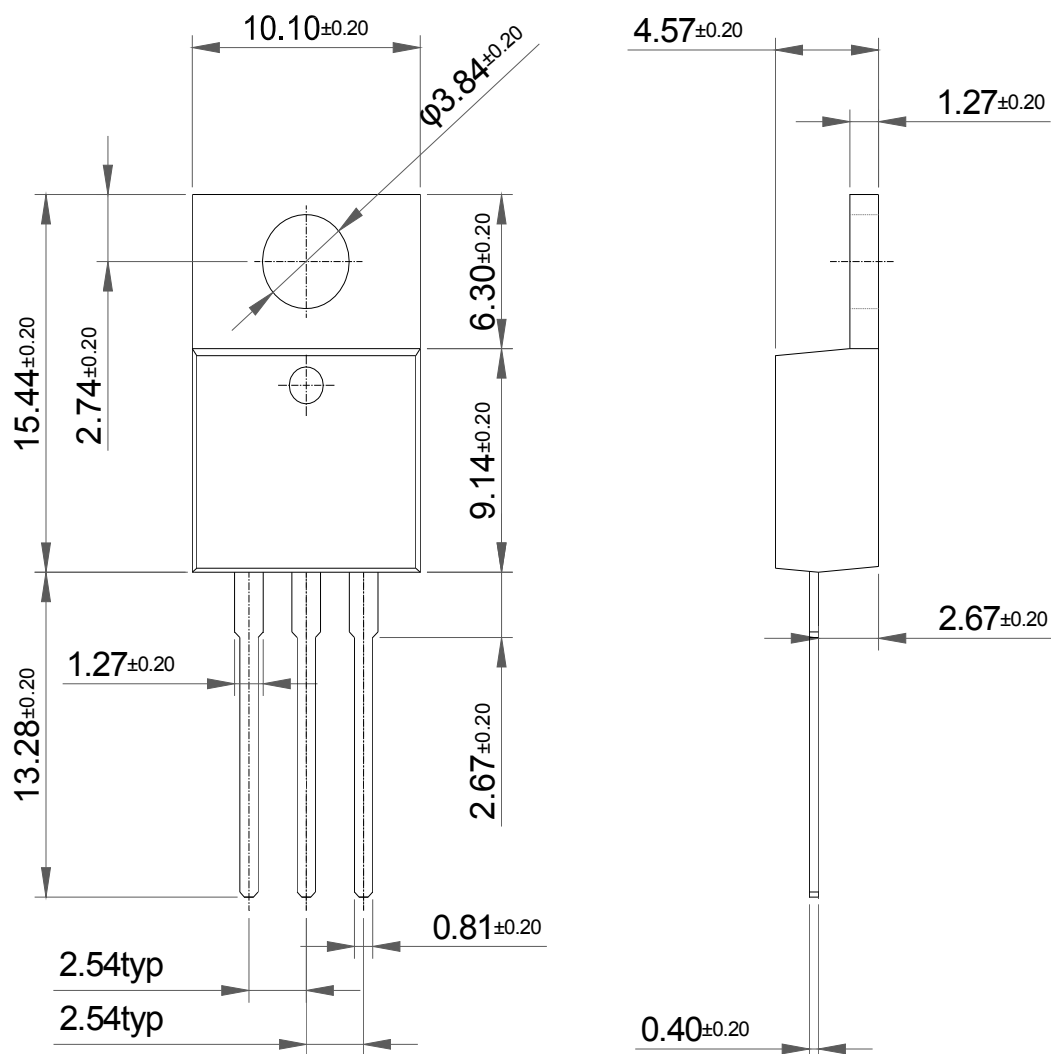
TO-220 (A)



Dimensions in Millimeters

Package Dimension

TO-220 (B)



Dimensions in Millimeters