

P-Channel Enhancement Mode MOSFET

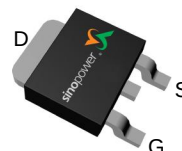
Features

- -30V/-8A,
 $R_{DS(ON)} = 95m\Omega$ (typ.) @ $V_{GS} = -10V$
 $R_{DS(ON)} = 140m\Omega$ (typ.) @ $V_{GS} = -4.5V$
- 100% UIS + R_g Tested
- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)
- ESD Protection

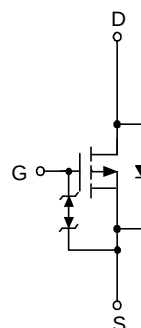
Applications

- Power Management in Desktop Computer or DC/DC Converters.

Pin Description



Top View of TO-252-2



P-Channel MOSFET

Ordering and Marking Information

APM3095P □□-□□ □ └─ Assembly Material └─ Handling Code └─ Temperature Range └─ Package Code	Package Code U : TO-252-2 Operating Junction Temperature Range C : -55 to 150 °C Handling Code TR : Tape & Reel Assembly Material L : Lead Free Device G : Halogen and Lead Free Device
APM3095P U :  APM3095P XXXXX	XXXXX - Lot Code

Note : SINOPOWER lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. SINOPOWER lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020D for MSL classification at lead-free peak reflow temperature. SINOPOWER defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

SINOPOWER reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.

Absolute Maximum Ratings

Symbol	Parameter		Rating	Unit
Common Ratings (T _A = 25°C)				
V _{DSS}	Drain-Source Voltage		-30	V
V _{GSS}	Gate-Source Voltage		±25	
T _J	Maximum Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-55 to 150	°C
I _S	Diode Continuous Forward Current		-2.5	A
I _{AR} ^a	Avalanche Current		20	A
E _{AR} ^a	Repetitive Avalanche Energy (L=0.1mH)		20	mJ
Mounted on Large Heat Sink				
I _{DP}	300μs Pulse Drain Current Tested	T _C =25°C	-20	A
		T _C =100°C	-10	
I _D	Continuous Drain Current	T _C =25°C	-8	A
		T _C =100°C	-4	
P _D	Maximum Power Dissipation	T _C =25°C	50	W
		T _C =100°C	20	
R _{θJC}	Thermal Resistance-Junction to Case		2.5	°C/W
Mounted on PCB of 1in ² Pad Area				
I _{DP}	300μs Pulse Drain Current Tested	T _A =25°C	-20	A
		T _A =100°C	-10	
I _D	Continuous Drain Current	T _A =25°C	-4	A
		T _A =100°C	-2	
P _D	Maximum Power Dissipation	T _A =25°C	2.5	W
		T _A =100°C	1	
R _{θJA}	Thermal Resistance-Junction to Ambient		50	°C/W
Mounted on PCB of Minimum Footprint				
I _{DP}	300μs Pulse Drain Current Tested	T _A =25°C	-20	A
		T _A =100°C	-10	
I _D	Continuous Drain Current	T _A =25°C	-3	A
		T _A =100°C	-2	
P _D	Maximum Power Dissipation	T _A =25°C	1.6	W
		T _A =100°C	0.6	
R _{θJA}	Thermal Resistance-Junction to Ambient		75	°C/W

Note a : UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature $T_J=25^\circ\text{C}$).

Electrical Characteristics (T_A = 25°C unless otherwise noted)

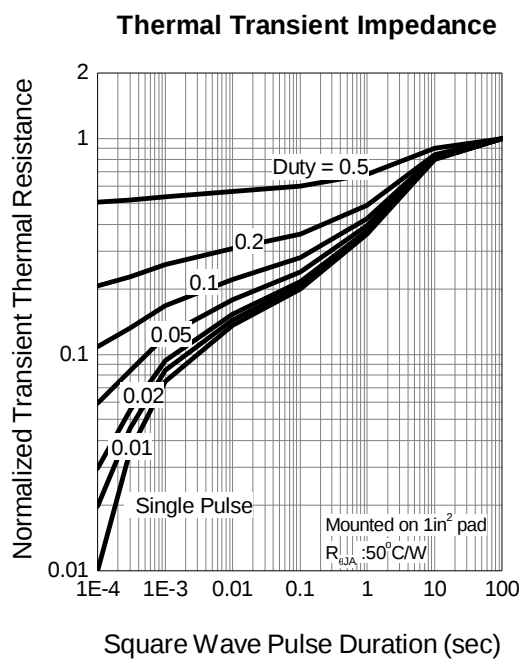
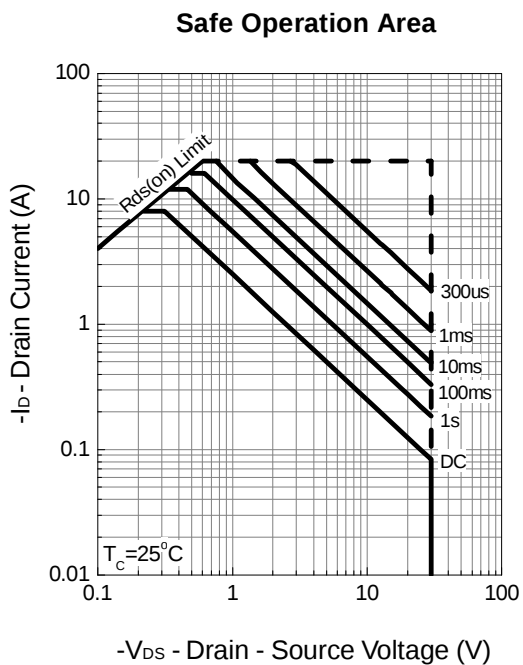
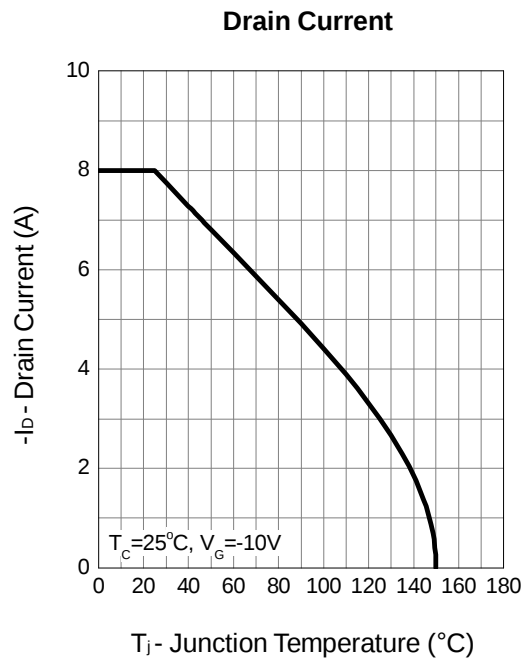
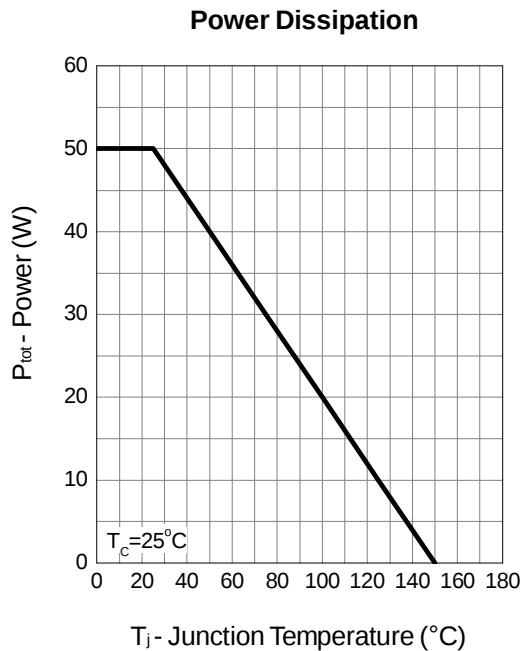
Symbol	Parameter	Test Conditions	APM3095PU			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =-250μA	-30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-24V, V _{GS} =0V	-	-	-1	μA
		T _J =85°C	-	-	-30	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =-250μA	-1	-1.5	-2.3	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±10	μA
R _{DS(ON)} ^a	Drain-Source On-state Resistance	V _{GS} =-10V, I _{DS} =-6A	-	95	110	mΩ
		V _{GS} =-4.5V, I _{DS} =-3A	-	140	160	
Diode Characteristics						
V _{SD} ^a	Diode Forward Voltage	I _{SD} =-2.5A, V _{GS} =0V	-	-0.7	-1.3	V
Dynamic Characteristics ^b						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	-	13	-	Ω
C _{iSS}	Input Capacitance	V _{GS} =0V, V _{DS} =-25V, Frequency=1.0MHz	-	550	-	pF
C _{oSS}	Output Capacitance		-	120	-	pF
C _{rSS}	Reverse Transfer Capacitance		-	75	-	pF
t _{d(ON)}	Turn-on Delay Time		-	10	20	ns
T _r	Turn-on Rise Time	V _{DD} =-15V, R _L =15Ω, I _{DS} =-1A, V _{GEN} =-10V, R _G =6Ω	-	8	20	ns
t _{d(OFF)}	Turn-off Delay Time		-	25	50	ns
T _f	Turn-off Fall Time		-	5	15	ns
Gate Charge Characteristics ^b						
Q _g	Total Gate Charge	V _{DS} =-15V, V _{GS} =-10V, I _{DS} =-6A	-	10	13	nC
Q _{gs}	Gate-Source Charge		-	2	-	nC
Q _{gd}	Gate-Drain Charge		-	1.2	-	nC

Notes:

a : Pulse test ; pulse width≤300μs, duty cycle≤2%.

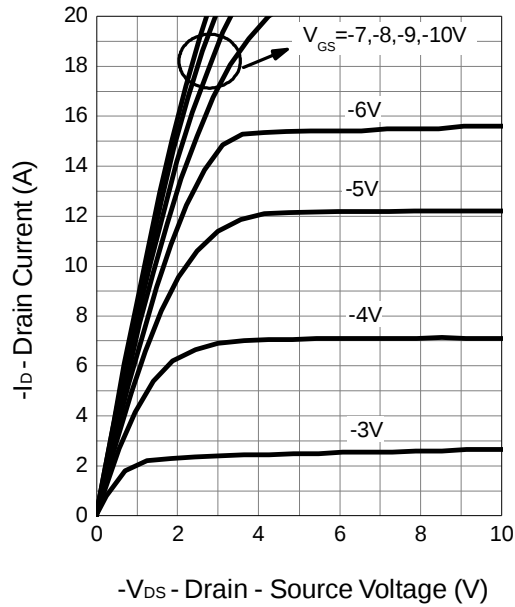
b : Guaranteed by design, not subject to production testing.

Typical Characteristics

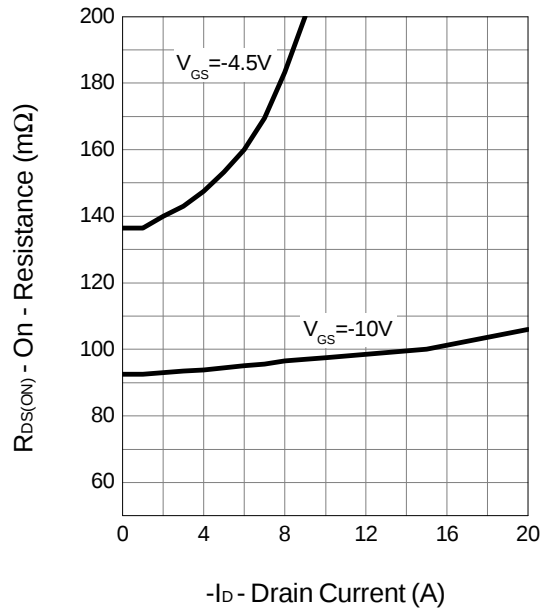


Typical Characteristics (Cont.)

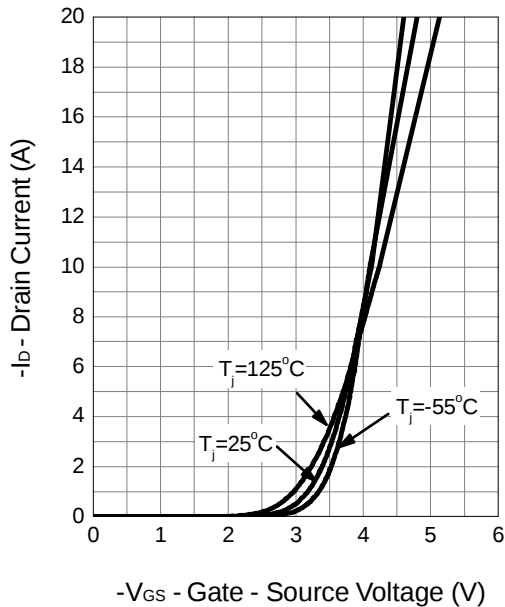
Output Characteristics



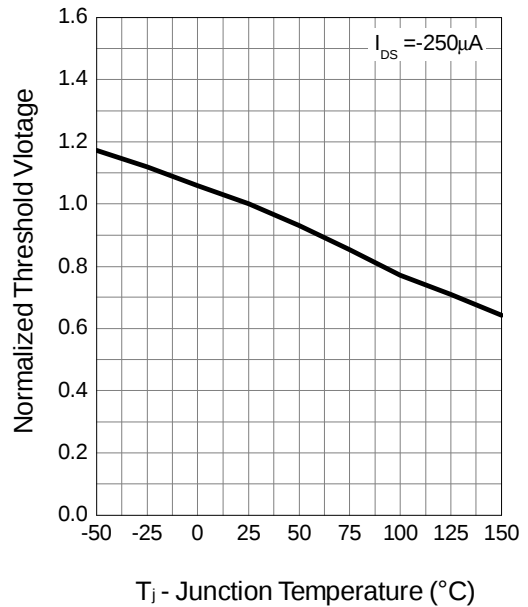
Drain-Source On Resistance



Transfer Characteristics

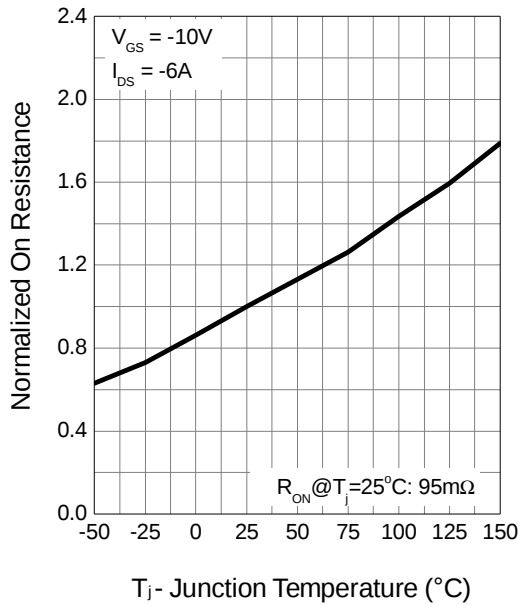


Gate Threshold Voltage

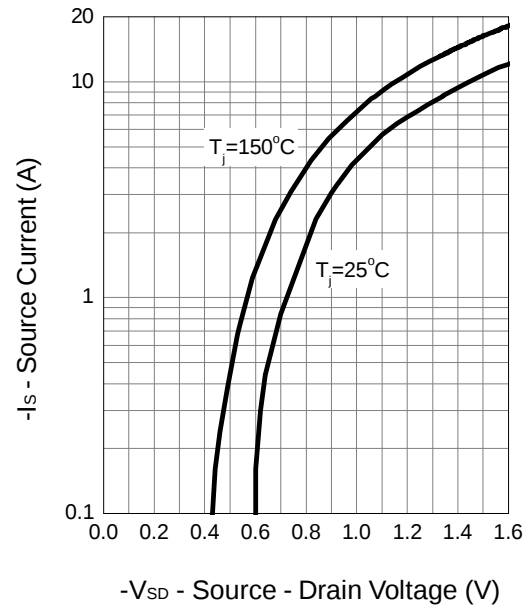


Typical Characteristics (Cont.)

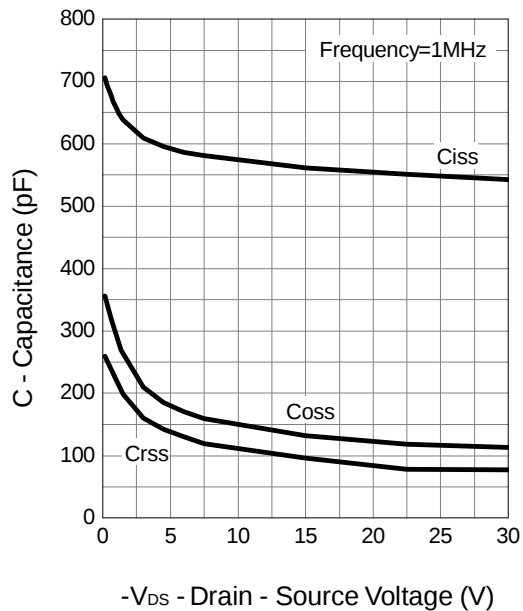
Drain-Source On Resistance



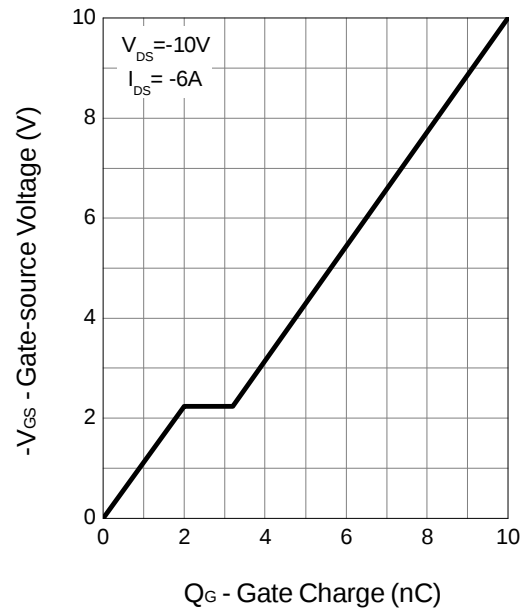
Source-Drain Diode Forward



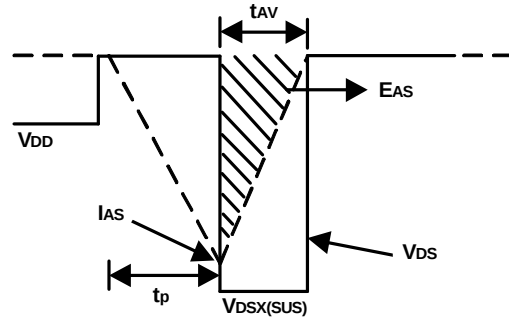
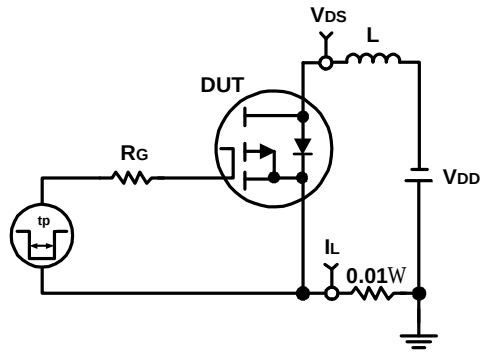
Capacitance



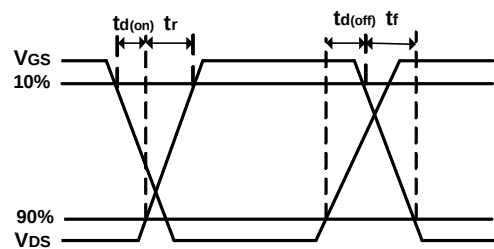
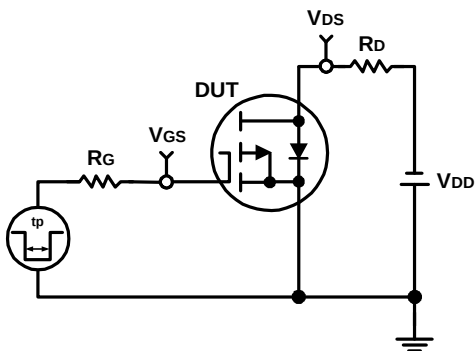
Gate Charge



Avalanche Test Circuit and Waveforms



Switching Time Test Circuit and Waveforms



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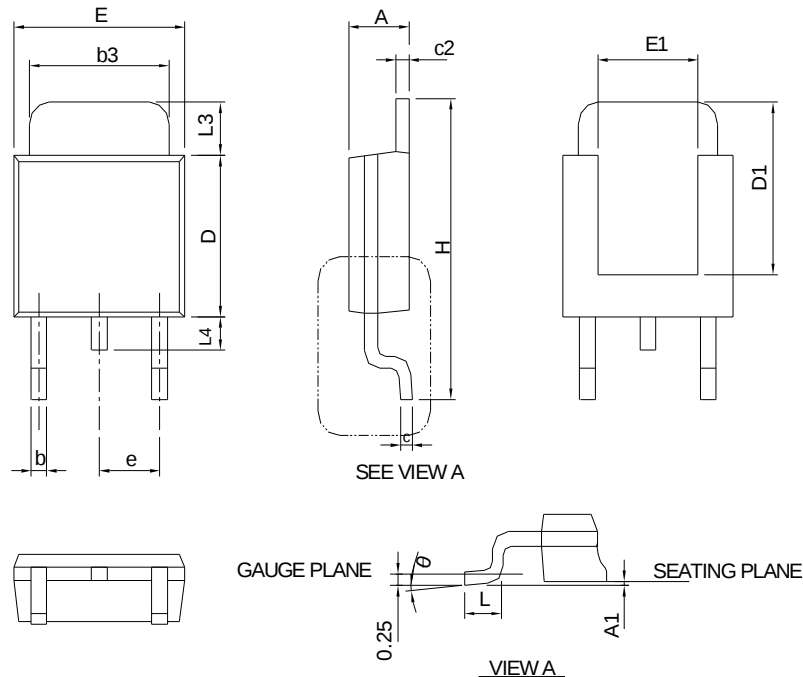
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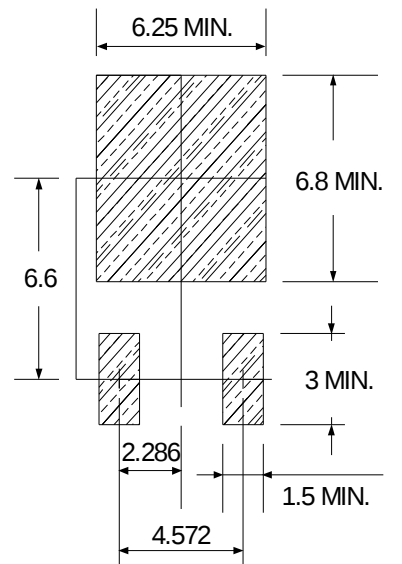
Package Information



SYMBOL	TO-252-2			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	2.18	2.39	0.086	0.094
A1	-	0.13	-	0.005
b	0.50	0.89	0.020	0.035
b3	4.95	5.46	0.195	0.215
c	0.46	0.61	0.018	0.024
c2	0.46	0.89	0.018	0.035
D	5.33	6.22	0.210	0.245
D1	4.57	6.00	0.180	0.236
E	6.35	6.73	0.250	0.265
E1	3.81	6.00	0.150	0.236
e	2.29 BSC		0.090 BSC	
H	9.40	10.41	0.370	0.410
L	0.90	1.78	0.035	0.070
L3	0.89	2.03	0.035	0.080
L4	-	1.02	-	0.040
θ	0°	8°	0°	8°

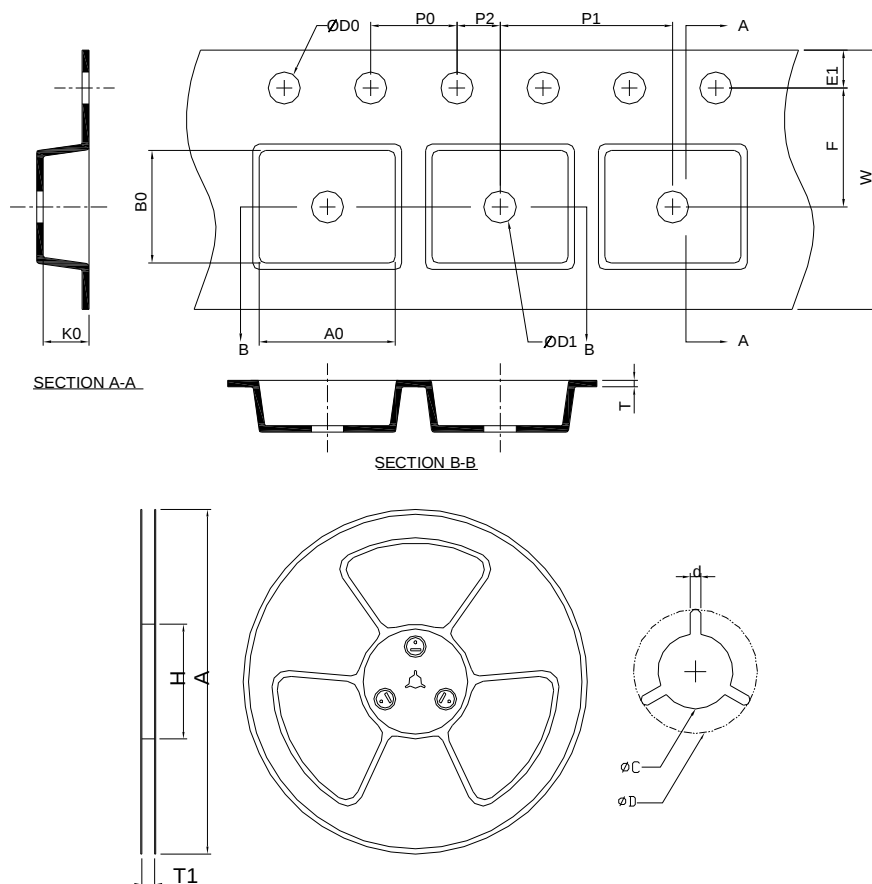
Note : Follow JEDEC TO-252 .

RECOMMENDED LAND PATTERN



UNIT: mm

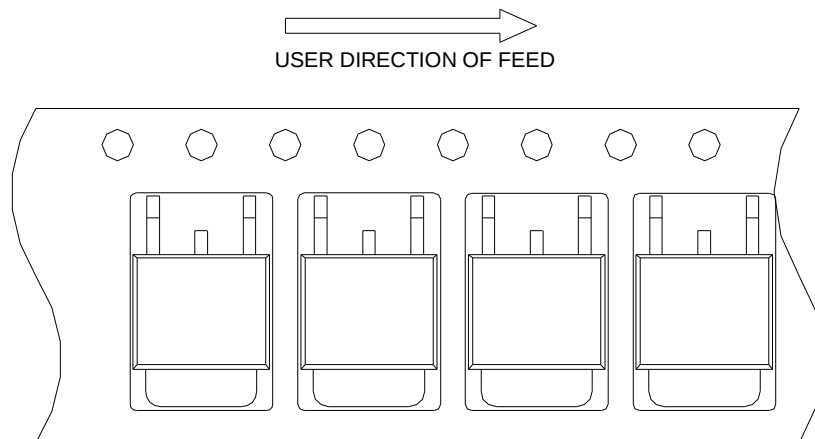
Carrier Tape & Reel Dimensions



Application	A	H	T1	C	d	D	W	E1	F
TO-252-2	330.0 ± 2.00	50 MIN.	$16.4 + 2.00 - 0.00$	$13.0 + 0.50 - 0.20$	1.5 MIN.	20.2 MIN.	16.0 ± 0.30	1.75 ± 0.10	7.50 ± 0.05
	P0	P1	P2	D0	D1	T	A0	B0	K0
	4.0 ± 0.10	8.0 ± 0.10	2.0 ± 0.05	$1.5 + 0.10 - 0.00$	1.5 MIN.	$0.6 + 0.00 - 0.40$	6.80 ± 0.20	10.40 ± 0.20	2.50 ± 0.20

(mm)

TO-252-2



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak Temperature min (T_{smin}) Temperature max (T_{smax}) Time (T_{smin} to T_{smax}) (t_s)	100 °C 150 °C 60-120 seconds	150 °C 200 °C 60-120 seconds
Average ramp-up rate (T_{smax} to T_p)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L) Time at liquidous (t_L)	183 °C 60-150 seconds	217 °C 60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_p)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_p to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum. ** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.		

Table 1. SnPb Eutectic Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	1000 Hrs, 80% of VDS max @ T_{jmax}
HTGB	JESD-22, A108	1000 Hrs, 100% of VGS max @ T_{jmax}
PCT	JESD-22, A102	168 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -65°C~150°C

Customer Service

Sinopower Semiconductor, Inc.

5F, No. 6, Dusing 1St Rd., Hsinchu Science Park,
Hsinchu, 30078, Taiwan
TEL: 886-3-5635818 Fax: 886-3-5635080