

N - CHANNEL ENHANCEMENT MODE POWER MOS TRANSISTORS

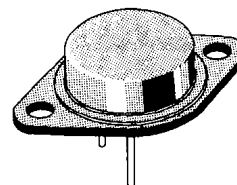
TYPE	V _{DSS}	R _{DS(on)}	I _D
IRF150	100 V	0.055 Ω	40 A
IRF151	60 V	0.055 Ω	40 A
IRF152	100 V	0.08 Ω	33 A
IRF153	60 V	0.08 Ω	33 A

- 60-100 VOLTS - FOR DC/DC CONVERTERS
- HIGH CURRENT
- RATED FOR UNCLAMPED INDUCTIVE SWITCHING (ENERGY TEST) ♦
- ULTRA FAST SWITCHING
- EASY DRIVE- FOR REDUCED COST AND SIZE

INDUSTRIAL APPLICATIONS:

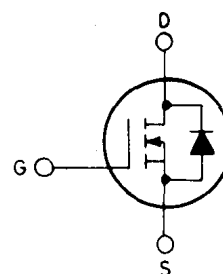
- UNINTERRUPTIBLE POWER SUPPLIES
- MOTOR CONTROLS

N - channel enhancement mode POWER MOS field effect transistors. Easy drive and very fast switching times make these POWER MOS transistors ideal for high speed switching applications. Applications include DC/DC converters, UPS, battery chargers, secondary regulators, servo control, power audio amplifiers and robotics.



TO-3

INTERNAL SCHEMATIC DIAGRAM



ABSOLUTE MAXIMUM RATINGS

		IRF			
		150	151	152	153
V _{DS} *	Drain-source voltage (V _{GS} = 0)	100	60	100	60 V
V _{DGR} *	Drain-gate voltage (R _{GS} = 20 KΩ)	100	60	100	60 V
V _{GS}	Gate-source voltage		± 20		V
I _D	Drain current (cont.) at T _c = 25°C	40	40	33	33 A
I _D	Drain current (cont.) at T _c = 100°C	25	25	20	20 A
I _{DM} (*)	Drain current (pulsed)	160	160	132	132 A
P _{tot}	Total dissipation at T _c < 25°C		150		W
	Derating factor		1.2		W/°C
T _{stg}	Storage temperature		- 55 to 150		°C
T _j	Max. operating junction temperature		150		°C

* T_j = 25°C to 125°C

(*) Repetitive Rating: Pulse width limited by max junction temperature

♦ Introduced in 1988 week 44

THERMAL DATA

$R_{thj - case}$	Thermal resistance junction-case	max	0.83	$^{\circ}\text{C/W}$
R_{thc-s}	Thermal resistance case-sink	typ	0.1	$^{\circ}\text{C/W}$
$R_{thj-amb}$	Thermal resistance junction-ambient	max	30	$^{\circ}\text{C/W}$
T_l	Maximum lead temperature for soldering purpose		300	$^{\circ}\text{C}$

ELECTRICAL CHARACTERISTICS ($T_{case} = 25^{\circ}\text{C}$ unless otherwise specified)

Parameters	Test Conditions	Min.	Typ.	Max.	Unit
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OFF

$V_{(BR) DSS}$	Drain-source breakdown voltage	$I_D = 250 \mu\text{A}$ for IRF150/IRF152 for IRF151/IRF153	$V_{GS} = 0$	100 60		V V
I_{DSS}	Zero gate voltage drain current ($V_{GS} = 0$)	$V_{DS} = \text{Max Rating}$ $V_{DS} = \text{Max Rating} \times 0.8$	$T_c = 125^{\circ}\text{C}$		250 1000	μA μA
I_{GSS}	Gate-body leakage current ($V_{DS} = 0$)	$V_{GS} = \pm 20 \text{ V}$			± 100	nA

ON **

$V_{GS(th)}$	Gate threshold voltage	$V_{DS} = V_{GS}$	$I_D = 250 \mu\text{A}$	2		4	V
$I_{D(on)}$	On-state drain current	$V_{DS} > I_{D(on)} \times R_{DS(on) max}$ for IRF150/IRF151 for IRF152/IRF153	$V_{GS} = 10 \text{ V}$	40 33			A A
$R_{DS(on)}$	Static drain-source on resistance	$V_{GS} = 10 \text{ V}$ for IRF150/IRF151 for IRF152/IRF153	$I_D = 20 \text{ A}$			0.055 0.08	Ω Ω

ENERGY TEST

I_{UIS}	Unclamped inductive switching current (single pulse)	$V_{DD} = 30 \text{ V}$ starting $T_j = 25^{\circ}\text{C}$ for IRF150/IRF151 for IRF152/IRF153	$L = 100 \mu\text{H}$	40 33			A A
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DYNAMIC

g_{fs}^{**}	Forward transconductance	$V_{DS} > I_{D(on)} \times R_{DS(on) max}$ $I_D = 20 \text{ A}$		9			mho
C_{iss} C_{oss} C_{rss}	Input capacitance Output capacitance Reverse transfer capacitance	$V_{DS} = 25 \text{ V}$ $V_{GS} = 0$	$f = 1 \text{ MHz}$			3000 1500 500	pF pF pF

ELECTRICAL CHARACTERISTICS (Continued)

Parameters	Test Conditions	Min.	Typ.	Max.	Unit
t_d (on) t_r t_d (off) t_f	Turn-on time Rise time Turn-off delay time Fall time $V_{DD} = 24\text{ V}$ $I_D = 20\text{ A}$ $R_i = 4.7\ \Omega$ (see test circuit)			35 100 125 100	ns ns ns ns
Q_g	Total Gate Charge $V_{GS} = 10\text{ V}$ $V_{DS} = \text{Max Rating} \times 0.8$ (see test circuit)			120	nC

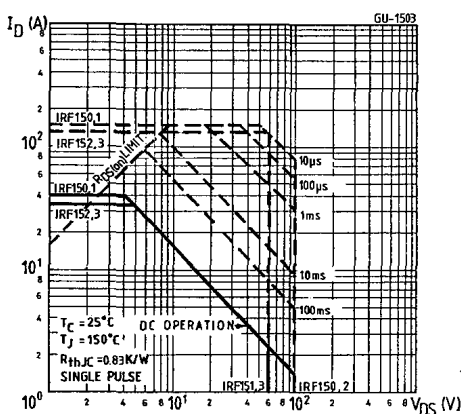
SOURCE DRAIN DIODE

I_{SD}	Source-drain current	for IRF150/IRF151 for IRF152/IRF153			40 33	A A
I_{SDM} (*)	Source-drain current (pulsed)	for IRF150/IRF151 for IRF152/IRF153			160 132	A A
V_{SD}^{**}	Forward on voltage	$V_{GS} = 0$ for IRF150/IRF151 for IRF152/IRF153	$I_{SD} = 40\text{ A}$ $I_{SD} = 33\text{ A}$		2.5 2.3	V V
t_{rr} Q_{rr}	Reverse recovery time Reverse recovered charge	$T_j = 150^\circ\text{C}$ $I_{SD} = 40\text{ A}$	$di/dt = 100\text{ A}/\mu\text{s}$		600 3.3	ns μC

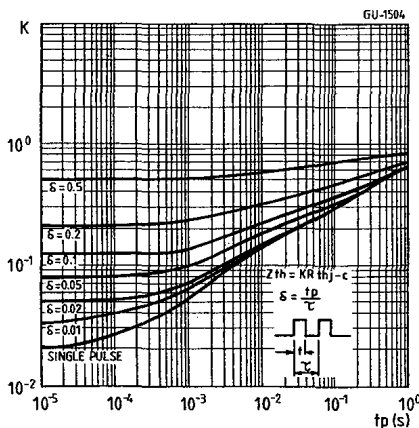
** Pulsed: Pulse duration $\leq 300\ \mu\text{s}$, duty cycle $\leq 2\%$

(*) Repetitive Rating: Pulse width limited by max junction temperature

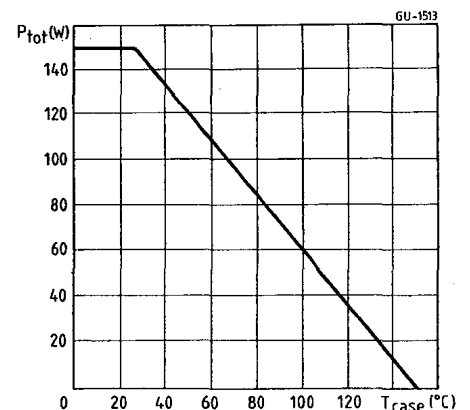
Safe operating areas



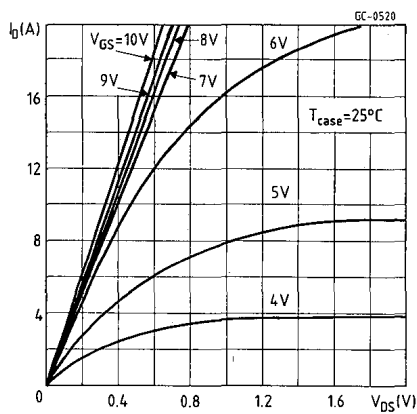
Thermal impedance



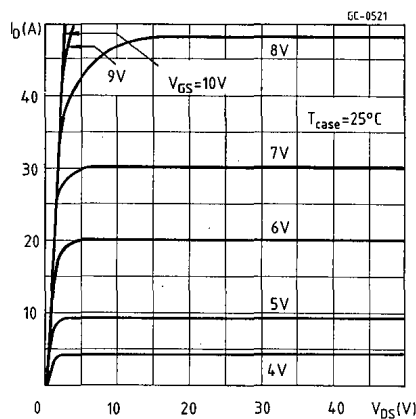
Derating curve



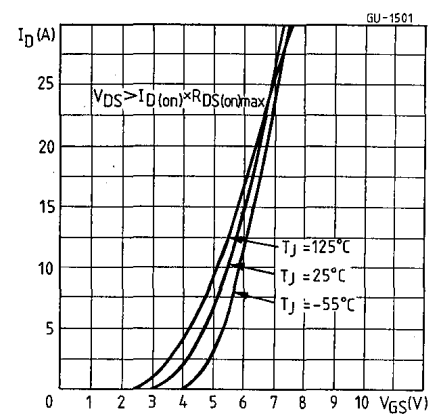
Output characteristics



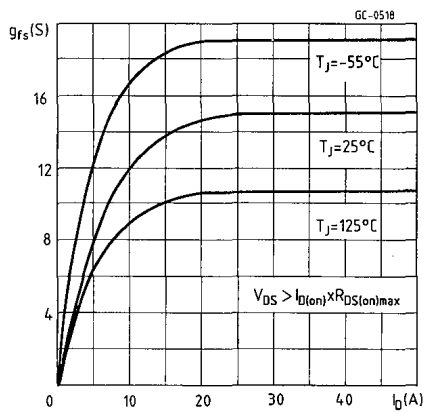
Output characteristics



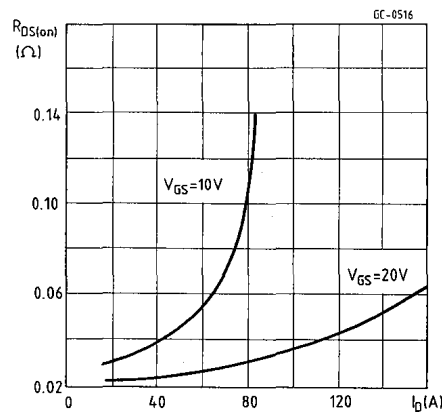
Transfer characteristics



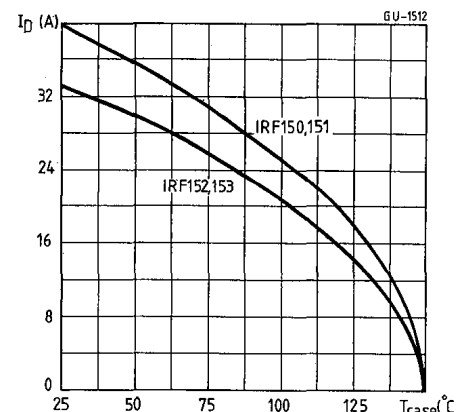
Transconductance



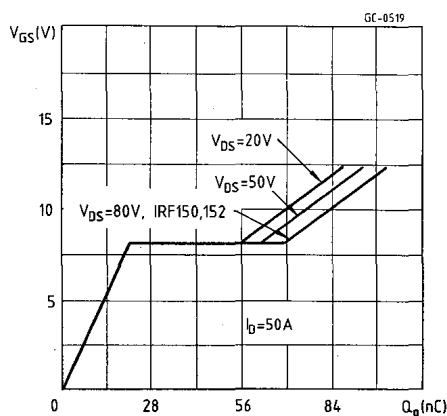
Static drain-source on resistance



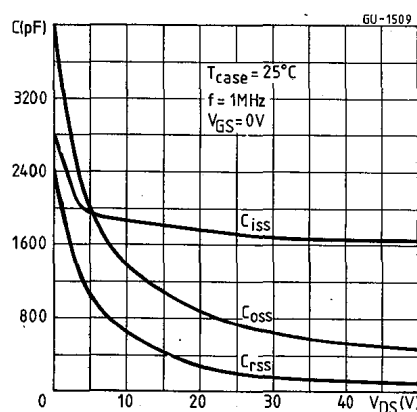
Maximum drain current vs temperature



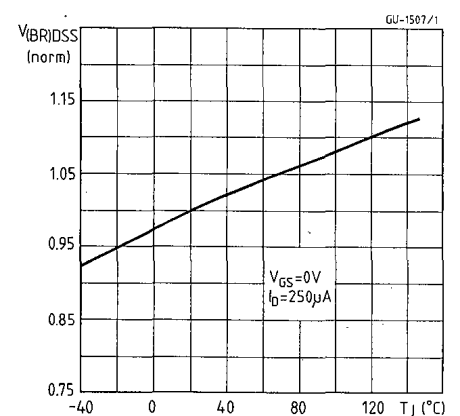
Gate charge vs gate-source voltage



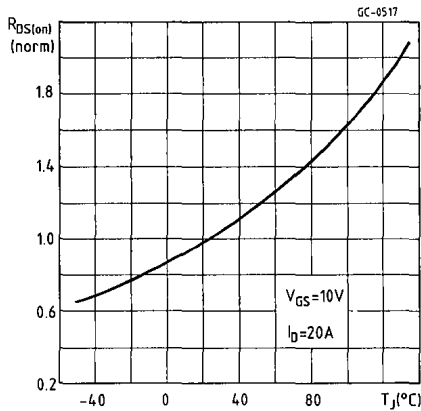
Capacitance variation



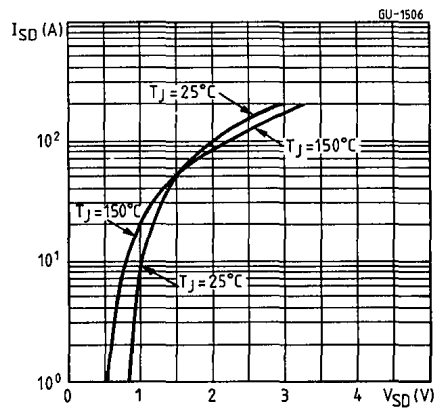
Normalized breakdown voltage vs temperature



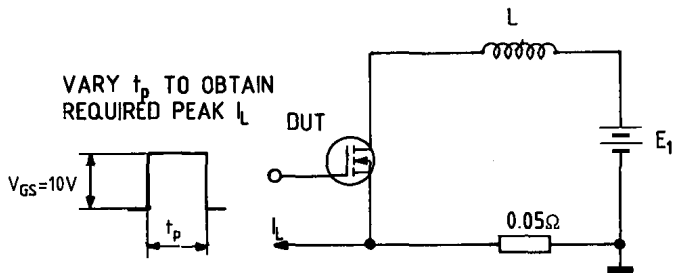
Normalized on resistance
vs temperature



Source-drain diode forward
characteristics

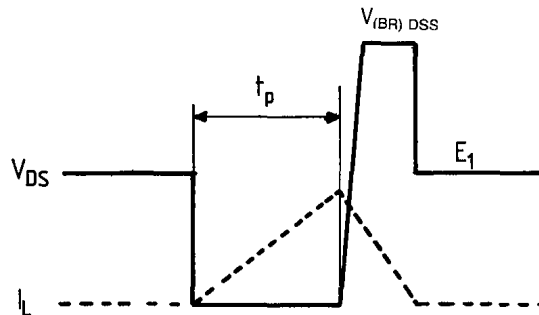


Unclamped inductive test circuit



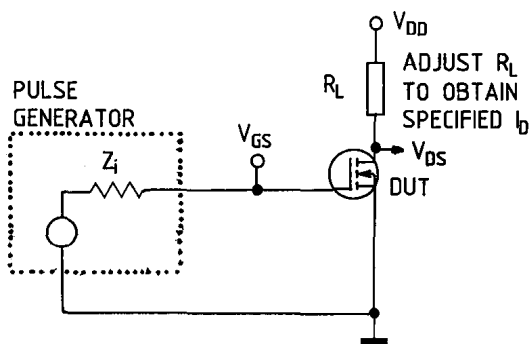
SC-0339

Unclamped inductive waveforms



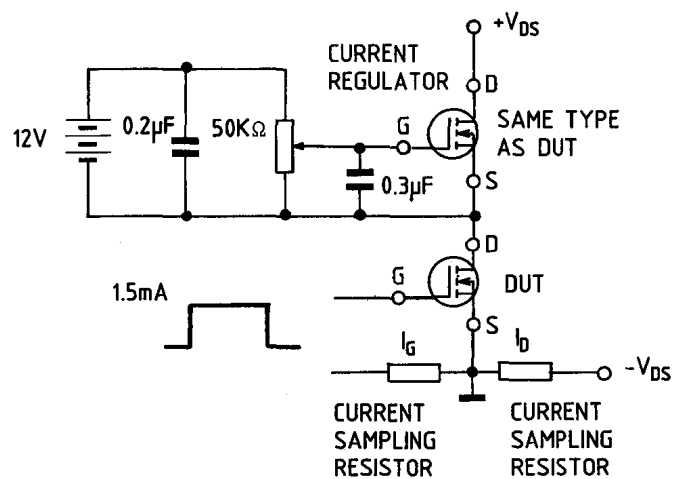
SC-0338

Switching times test circuit



SC-0246

Gate charge test circuit



SC-0244