Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOSIII)

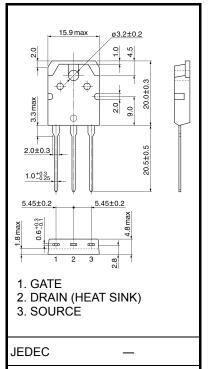
## 2SK3845

# Switching Regulator, DC-DC Converter Applications and Motor Drive Applications

- Low drain-source ON resistance: RDS (ON) =  $4.7 \text{ m}\Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 88 \text{ S (typ.)}$
- Low leakage current:  $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 60 \text{ V)}$
- Enhancement model:  $V_{th} = 2.0 \text{ to } 4.0 \text{ V (VDS} = 10 \text{ V, ID} = 1 \text{ mA)}$

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	60	V	
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )		$V_{DGR}$	60	V	
Gate-source voltage		$V_{GSS}$	±20	V	
Drain current	DC (Note 1)	ID	70	Α	
	Pulse (Note 1)	I <sub>DP</sub>	280		
Drain power dissipation (Tc = 25°C)		$P_{D}$	125	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	328	mJ	
Avalanche current		I <sub>AR</sub>	70	Α	
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	12.5	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to150	°C	



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Weight: 4.6 g (typ.)

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Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

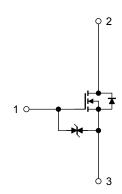
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	1.0	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	50	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD} = 25$  V,  $T_{ch} = 25$  °C (initial), L = 91  $\mu H$ ,  $R_G = 25$   $\Omega$ ,  $I_{AR} = 70$  A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.



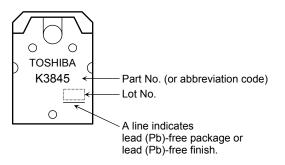
### **Electrical Characteristics (Ta = 25°C)**

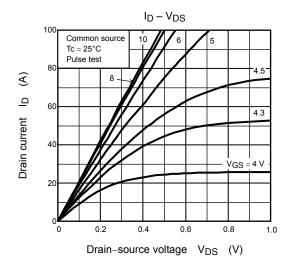
Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I <sub>GSS</sub>	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Drain cut-OFF cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0 V	_	_	100	μА
Drain-source breakdown voltage		V (BR) DSS	I <sub>D</sub> = 10mA, V <sub>GS</sub> = 0 V	60	_	_	V
		V (BR) DSX	$I_D = 10 \text{mA}, \ V_{GS} = -20 \ \text{V}$	35	_	_	
Gate threshold vo	oltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source ON	resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 35 A	_	4.7	5.8	mΩ
Forward transfer	Forward transfer admittance $ Y_{fs} $ $V_{DS} = 10 \text{ V}, I_{C}$		V <sub>DS</sub> = 10 V, I <sub>D</sub> = 35 A	44	88	_	S
Input capacitance	t capacitance C <sub>iss</sub>		_	12400	_		
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	700	_	pF
Output capacitance		Coss		_	1100	_	
Switching time	Rise time	t <sub>r</sub>	$V_{GS}$ $0 \text{ V}$ $V_{GS}$ $0 \text{ V}$ $0  V$		17		
	Turn-ON time	t <sub>on</sub>		_	44	_	ne
	Fall time	t <sub>f</sub>		_	35	_	ns ns
	Turn-OFF time	t <sub>off</sub>		_	200	_	
Total gate charge (gate-source plus gate-drain)		Qg			196		nC
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \simeq 48 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 70 \text{ A}$	_	148	_	
Gate-drain ("mille	Gate-drain ("miller") charge			_	48	_	

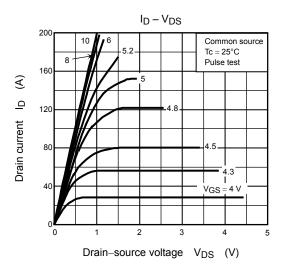
## **Source-Drain Ratings and Characteristics (Ta = 25°C)**

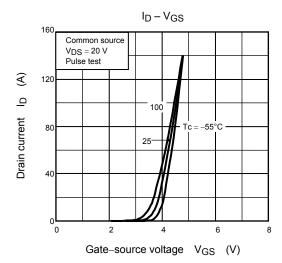
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	$I_{DR}$	_	_	_	70	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	280	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 70 A, V <sub>GS</sub> = 0 V	_	_	-1.5	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = 70 \text{ A}, V_{GS} = 0 \text{ V},$	_	70	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dI <sub>DR</sub> /dt = 50 A/μs		77	_	nC

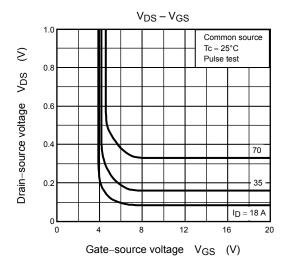
### Marking

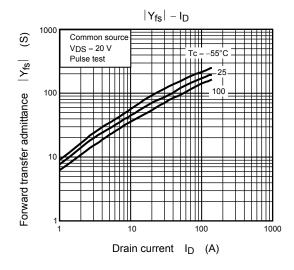


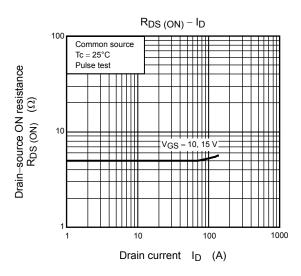


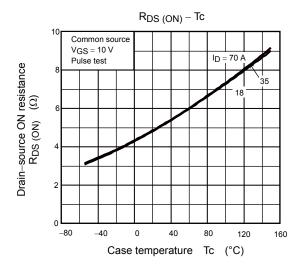


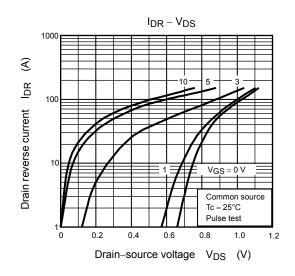


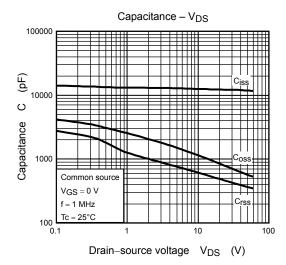


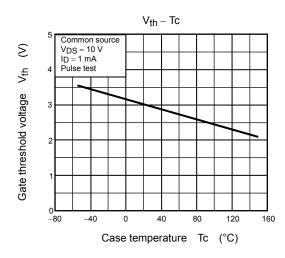


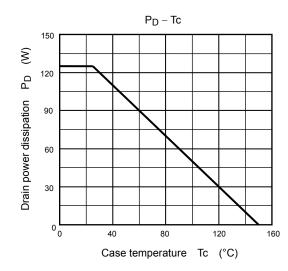


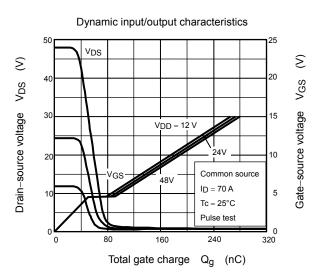


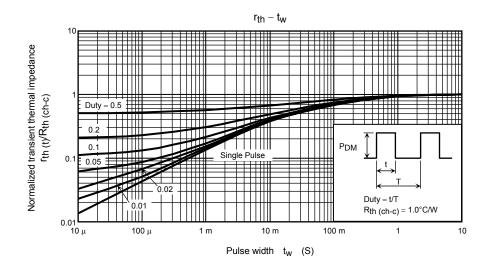


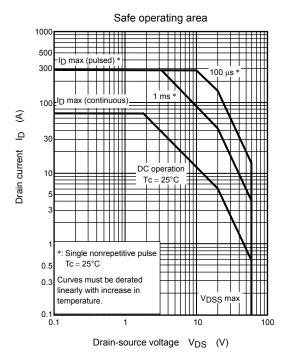


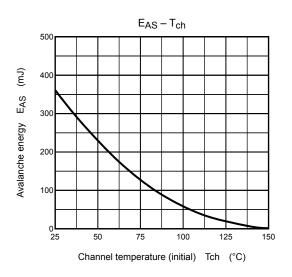


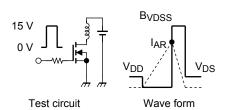












$$R_G = 25~\Omega$$
 
$$V_{DD} = 25~V,~L = 91~\mu H$$

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{BVDSS}{BVDSS - VDD} \right)$$

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