TOSHIBA Field Effect Transistor Silicon P-Channel MOS Type

SSM3J111TU

High Speed Switching Applications

• 2.5V drive

• Low on-resistance: R_{On} = 480m Ω (max) (@V_{GS} = -4 V) R_{On} = 680m Ω (max) (@V_{GS} = -2.5 V)

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Drain-Source voltage		V_{DS}	-20	V	
Gate-Source voltage		V_{GSS}	± 12	V	
Drain current	DC	I _D	-1	Α	
	Pulse	I _{DP}	-2		
Drain power dissipation		P _{D (Note 1)}	800	mW	
		P _{D (Note 2)}	500		
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	

Note:

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

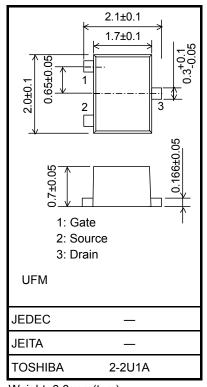
Note 1: Mounted on ceramic board.

 $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 0.8 \text{ mm}, \text{ Cu Pad: } 645 \text{ mm}^2)$

Note 2: Mounted on FR4 board.

 $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 1.6 \text{ mm}, \text{Cu Pad: } 645 \text{ mm}^2)$

Unit: mm



Weight: 6.6 mg (typ.)

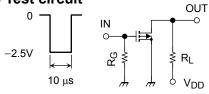
Electrical Characteristics (Ta = 25°C)

Charact	eristic	Symbol	Test Conditions	Min	Тур.	Max	Unit
Drain-Source break	down voltage	$V_{(BR)DSS}$ $I_D = -1 \text{ mA}, V_{GS} = 0$		-20	_	_	V
Drain cut-off current		I _{DSS}	V _{DS} = -20 V, V _{GS} = 0	_	_	-1	μА
Gate leakage curre	nt	I _{GSS}	$V_{GS} = \pm 12V, V_{DS} = 0$	_	_	±1	μА
Gate threshold volta	age	V _{th}	$V_{DS} = -3 \text{ V}, I_D = -0.1 \text{ mA}$	-0.6	_	-1.1	V
Forward transfer ac	Imittance	Y _{fs}	$V_{DS} = -3 \text{ V}, I_D = -0.3 \text{ A}$ (Note3)	0.6	1.2	_	S
Drain-Source on-resistance		R _{DS} (ON)	$I_D = -0.3 \text{ A}, V_{GS} = -4.0 \text{ V}$ (Note3)	_	380	480	mΩ
			$I_D = -0.3 \text{ A}, V_{GS} = -2.5 \text{ V}$ (Note3)	_	530	680	
Input capacitance		C _{iss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	160	_	pF
Output capacitance		C _{oss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	90	_	pF
Reverse transfer capacitance		C _{rss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	25	_	pF
Switching time	Turn-on time	t _{on}	$V_{DD} = -10 \text{ V}, I_D = -0.3 \text{ A},$	_	27	_	
	Turn-off time	t _{off}	$V_{GS} = 0 \sim -2.5 \text{ V}, R_G = 4.7 \Omega$	_	43	_	ns
Drain-Source forward voltage		V _{DSF}	I _D = 1A, V _{GS} = 0 V (Note3)	_	0.85	1.2	V

Note3: Pulse test

Switching Time Test Circuit





 $V_{DD} = -10 \text{ V}$

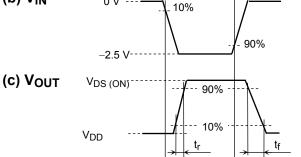
 $R_G=4.7\;\Omega$

 $D.U. \leqq 1\%$

 $V_{IN}\text{: }t_{r}\text{, }t_{f}<5\text{ ns}$ Common Source

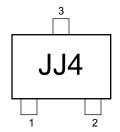
 $Ta = 25^{\circ}C$

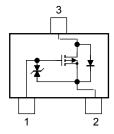
(b) V_{IN}



Marking

Equivalent Circuit (top view)





Precaution

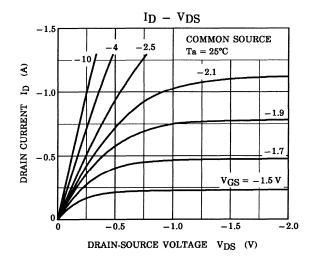
V_{th} can be expressed as the voltage between gate and source when the low operating current value is I_D=-0.1mA for this product. For normal switching operation, VGS (on) requires a higher voltage than Vth, and VGS (off) requires a lower voltage than Vth.

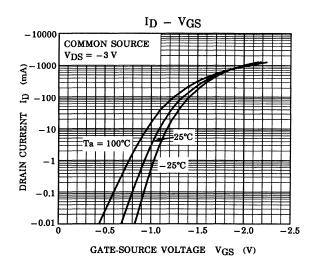
(The relationship can be established as follows: V_{GS} (off) < V_{th} < V_{GS} (on))

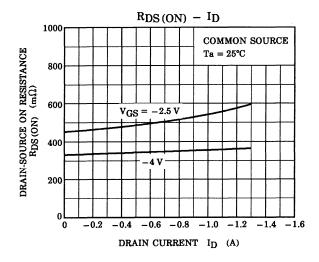
Take this into consideration when using the device.

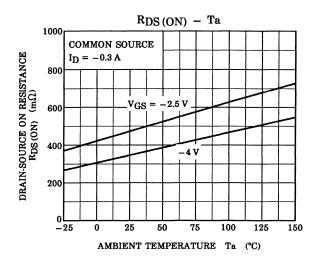
Handling Precaution

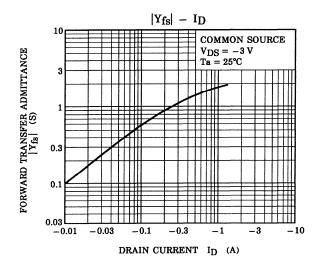
When handling individual devices which are not yet mounted on a circuit board, be sure that the environment is protected against electrostatic discharge. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

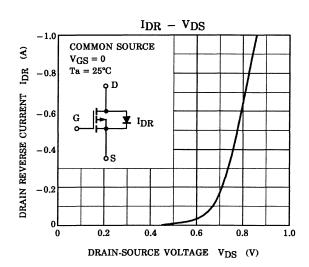


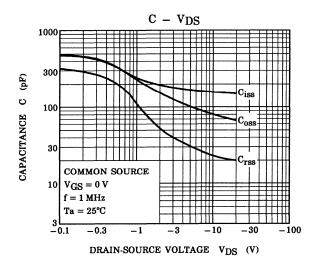


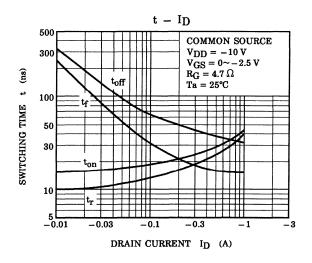


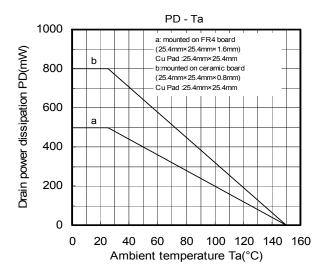


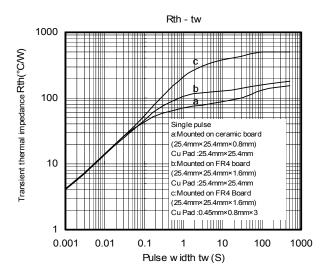












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