

MOSFETs Silicon N-Channel MOS (π-MOSVII)

# TK10A60D5

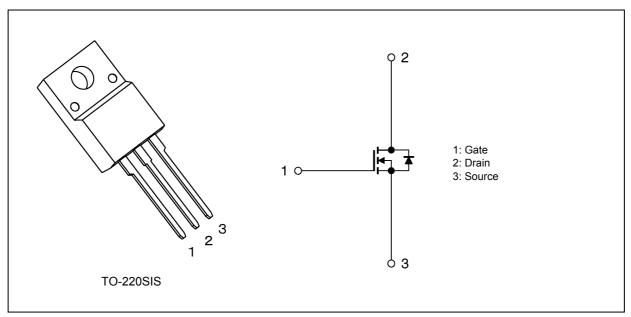
#### 1. Applications

• Switching Voltage Regulators

#### 2. Features

- (1) Fast reverse recovery time:  $t_{rrf} = 50$  ns (typ.),  $t_{rr} = 90$  ns (typ.)
- (2) Low drain-source on-resistance:  $R_{DS(ON)} = 0.8 \Omega$  (typ.)
- (3) High forward transfer admittance:  $|Y_{fs}| = 6.0 \text{ S (typ.)}$
- (4) Low leakage current:  $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 600 \text{ V)}$
- (5) Enhancement mode:  $V_{th}$  = 2.5 to 4.5 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

### 3. Packaging and Internal Circuit



### 4. Absolute Maximum Ratings (Note) (Ta = 25°C unless otherwise specified)

Characteris	Symbol	Rating	Unit		
Drain-source voltage			V <sub>DSS</sub>	600	V
Gate-source voltage			V <sub>GSS</sub>	±30	
Drain current (DC)		(Note 1)	I <sub>D</sub>	10	Α
Drain current (pulsed)	(t = 1 ms)	(Note 1)	I <sub>DP</sub>	40	7
Power dissipation	$(T_c = 25^{\circ}C)$		P <sub>D</sub>	45	W
Single-pulse avalanche energy		(Note 2)	E <sub>AS</sub>	364	mJ
Avalanche current			I <sub>AR</sub>	10	А
Repetitive avalanche energy		(Note 3)	E <sub>AR</sub>	4.5	mJ
Channel temperature			T <sub>ch</sub>	150	°C
Storage temperature			T <sub>stg</sub>	-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



#### 5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance	R <sub>th(ch-c)</sub>	2.78	°C/W
Channel-to-ambient thermal resistance	R <sub>th(ch-a)</sub>	62.5	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 6.36 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 10 A

Note 3: Repetitive rating; pulse width limited by maximum channel temperature

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.



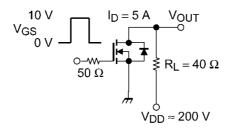
#### 6. Electrical Characteristics

# 6.1. Static Characteristics (T<sub>a</sub> = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μА
Drain cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V	_	_	10	
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	600	_	_	V
Gate threshold voltage	$V_{th}$	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.5	_	4.5	
Drain-source on-resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5 A	_	0.8	1.05	Ω
Forward transfer admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 5 A	1.5	6.0	_	S

# 6.2. Dynamic Characteristics (T<sub>a</sub> = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	1350		pF
Reverse transfer capacitance	$C_{rss}$		_	15		
Output capacitance	C <sub>oss</sub>		_	135	_	
Switching time (rise time)	t <sub>r</sub>	See Figure 6.2.1.	_	22	_	ns
Switching time (turn-on time)	t <sub>on</sub>		_	55	_	
Switching time (fall time)	t <sub>f</sub>		_	15	_	
Switching time (turn-off time)	t <sub>off</sub>		-	100		



Duty  $\leq$  1%,  $t_W=10~\mu s$ 

Fig. 6.2.1 Switching Time Test Circuit

# 6.3. Gate Charge Characteristics (T<sub>a</sub> = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$	_	25		nC
Gate-source charge	$Q_{gs}$			16		
Gate-drain charge	Q <sub>gd</sub>		_	9	_	



# 6.4. Source-Drain Characteristics (Ta = 25°C unless otherwise specified)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (DC)	(Note 4)	I <sub>DR</sub>	_	_	_	10	Α
Reverse drain current (pulsed)	(Note 4)	I <sub>DRP</sub>	_	_	_	40	
Diode forward voltage		$V_{DSF}$	I <sub>DR</sub> = 10 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time		t <sub>rrf</sub>	I <sub>DR</sub> = 10 A, V <sub>GS</sub> = 0 V, -dI <sub>DR</sub> /dt = 100 A/μs,	_	50	_	ns
		t <sub>rr</sub>	See Figure 6.4.1	_	90		
Reverse recovery charge		Q <sub>rr</sub>	$I_{DR}$ = 10 A, $V_{GS}$ = 0 V, - $dI_{DR}$ / $dt$ = 100 A/ $\mu$ s	1	130	1	nC

Note 4: Ensure that the channel temperature does not exceed 150°C.

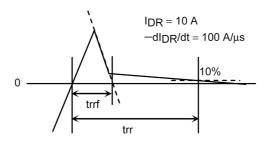


Fig. 6.4.1 Reverse Recovery Time

### 7. Marking (Note)

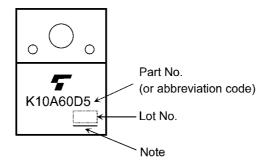


Fig. 7.1 Marking

Note: A line under a Lot No. identifies the indication of product Labels.

[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

### 8. Characteristics Curves (Note)

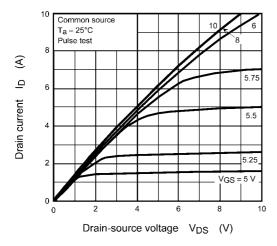


Fig. 8.1 I<sub>D</sub> - V<sub>DS</sub>

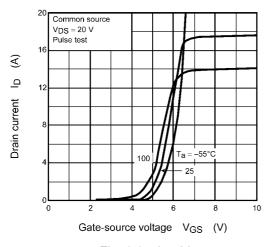


Fig. 8.3 I<sub>D</sub> - V<sub>GS</sub>

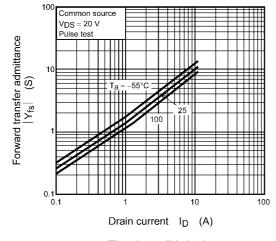


Fig. 8.5 |Y<sub>fs</sub>| - I<sub>D</sub>

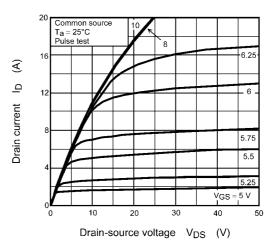


Fig. 8.2 I<sub>D</sub> - V<sub>DS</sub>

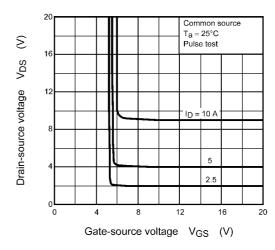


Fig. 8.4  $V_{DS}$  -  $V_{GS}$ 

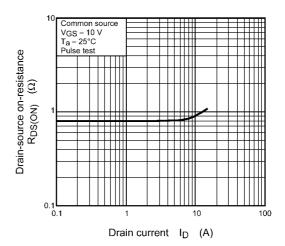


Fig. 8.6  $R_{DS(ON)}$  -  $I_D$ 

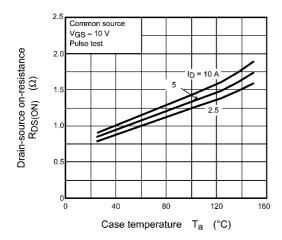


Fig. 8.7 RDS(ON) - Ta

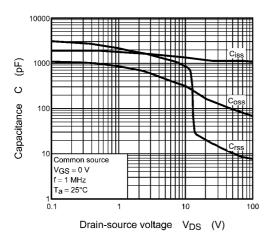


Fig. 8.9 C - V<sub>DS</sub>

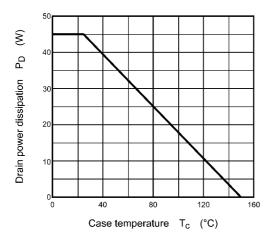


Fig. 8.11 P<sub>D</sub> - T<sub>c</sub> (Guaranteed Maximum)

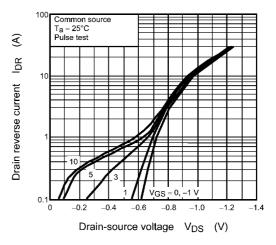


Fig. 8.8  $I_{DR}$  -  $V_{DS}$ 

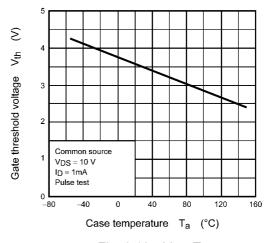


Fig. 8.10 V<sub>th</sub> - T<sub>a</sub>

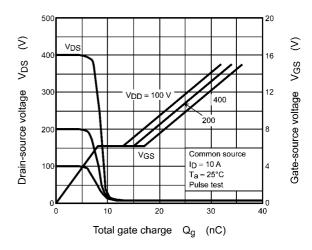


Fig. 8.12 Dynamic Input/Output Characteristics

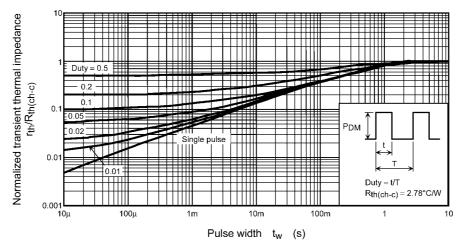


Fig. 8.13  $r_{th}/R_{th(ch-c)} - t_w$  (Guaranteed Maximum)

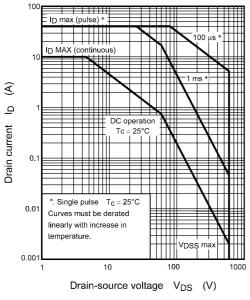


Fig. 8.14 Safe Operating Area (Guaranteed Maximum)

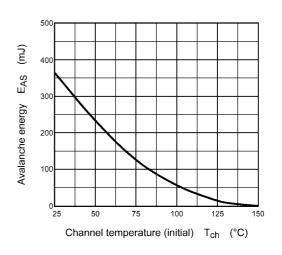


Fig. 8.15 E<sub>AS</sub> - T<sub>ch</sub> (Guaranteed Maximum)

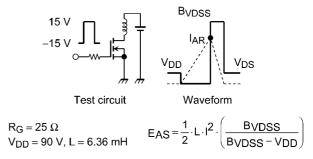


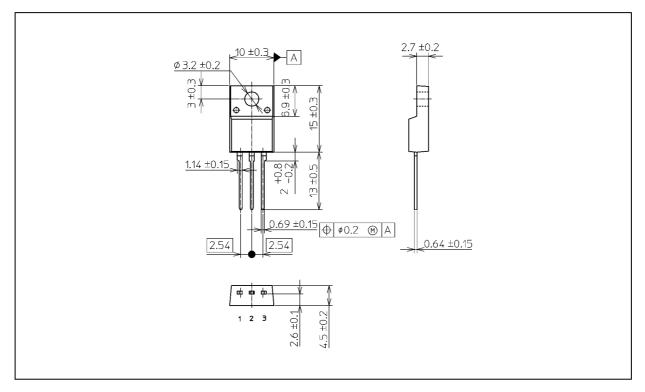
Fig. 8.16 Test Circuit/Waveform

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



# **Package Dimensions**

Unit: mm



Weight: 1.7 g (typ.)

Package Name(s)	
JEITA: SC-67	
TOSHIBA: 2-10U1S	
Nickname: TO-220SIS	



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