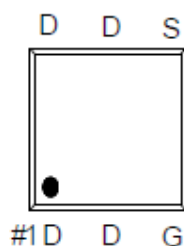


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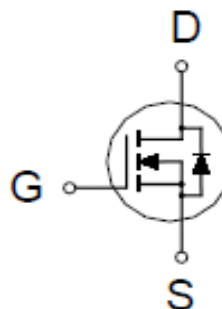
N-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D^3
30V	16mΩ @ $V_{GS} = 10V$	24A



PDFN 2X2S



ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ °C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	±20	
Continuous Drain Current ³	$T_C = 25\text{ °C}$	I_D	24	A
	$T_C = 100\text{ °C}$		15	
	$T_A = 25\text{ °C}$		8.8	
	$T_A = 70\text{ °C}$		7.1	
Pulsed Drain Current ¹		I_{DM}	70	
Avalanche Current		I_{AS}	20.5	
Avalanche Energy	$L = 0.1\text{ mH}$	E_{AS}	21	mJ
Power Dissipation	$T_C = 25\text{ °C}$	P_D	15	W
	$T_C = 100\text{ °C}$		6	
	$T_A = 25\text{ °C}$		2	
	$T_A = 70\text{ °C}$		1.3	
Operating Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient ²	$R_{\theta JA}$		60	°C / W
Junction-to-Case	$R_{\theta JC}$		8	

¹Pulse width limited by maximum junction temperature.

²The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Coppe.

³Package limitation current is 22A.

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ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	30			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1	1.6	2.5	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =24V, V _{GS} = 0V			1	μA
		V _{DS} =20V, V _{GS} = 0V, T _J = 125°C			10	
On-State Drain Current ¹	I _{D(ON)}	V _{DS} = 5V, V _{GS} = 10V	70			A
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = 4.5V, I _D = 6A		18.8	25	mΩ
		V _{GS} =10V, I _D = 8A		12.5	16	
Forward Transconductance ¹	g _{fs}	V _{DS} =10V, I _D = 8A		22		S
DYNAMIC						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 15V, f = 1MHz		501		pF
Output Capacitance	C _{oss}			110		
Reverse Transfer Capacitance	C _{rss}			78		
Gate Resistance	R _g	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz		2.6		Ω
Total Gate Charge ²	Q _{g(VGS=10V)}	V _{DS} = 0.5V _{(BR)DSS} , I _D = 8A		13		nC
	Q _{g(VGS=4.5V)}			7		
Gate-Source Charge ²	Q _{gs}			1.8		
Gate-Drain Charge ²	Q _{gd}			3.4		
Turn-On Delay Time ²	t _{d(on)}	V _{DD} = 15V I _D ≅ 8A, V _{GEN} = 10V, R _G = 6Ω		12		nS
Rise Time ²	t _r			10		
Turn-Off Delay Time ²	t _{d(off)}			27		
Fall Time ²	t _f			10		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T _J = 25 °C)						
Continuous Current ³	I _S				24	A
Forward Voltage ¹	V _{SD}	I _F = 8A, V _{GS} = 0V			1	V
Reverse Recovery Time	t _{rr}	I _F = 8A, dI _F /dt = 100A / μS		12		nS
Reverse Recovery Charge	Q _{rr}			3		nC

¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

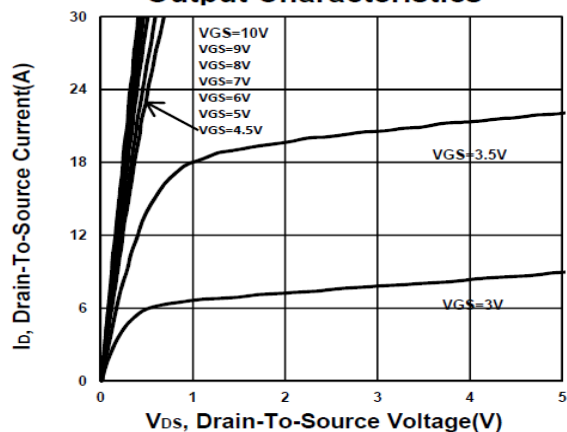
²Independent of operating temperature.

³Package limitation current is 22A.

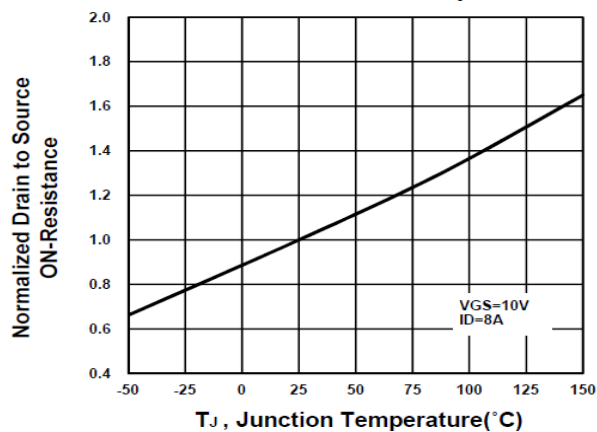
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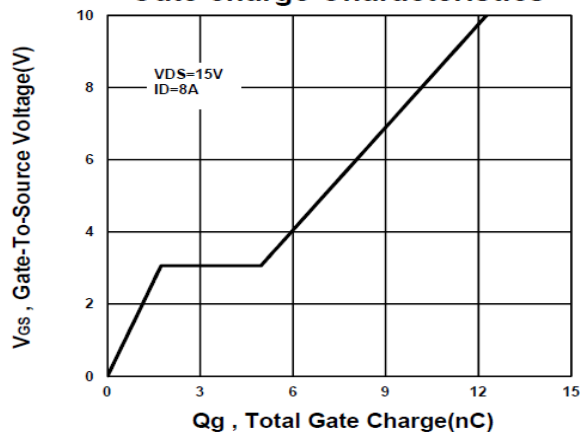
Output Characteristics



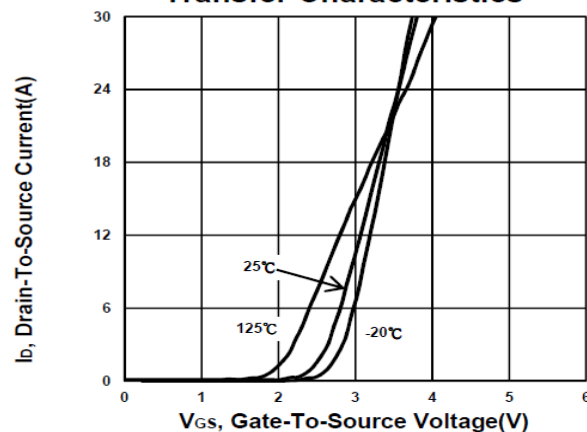
On-Resistance VS Temperature



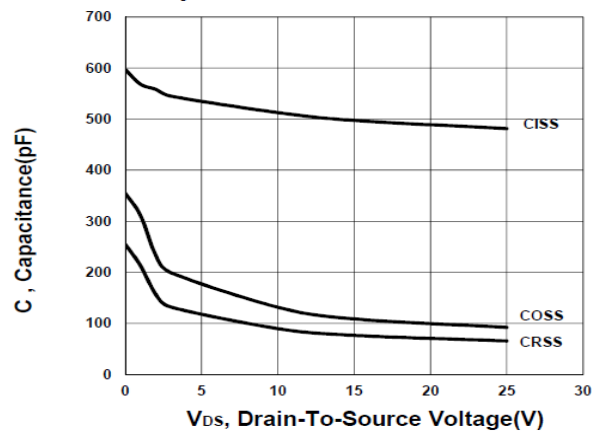
Gate charge Characteristics



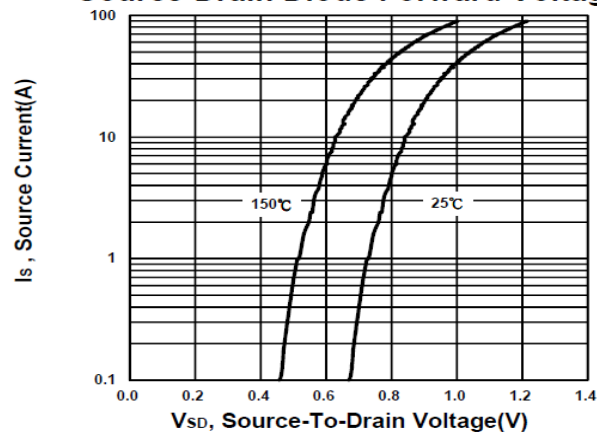
Transfer Characteristics



Capacitance Characteristic



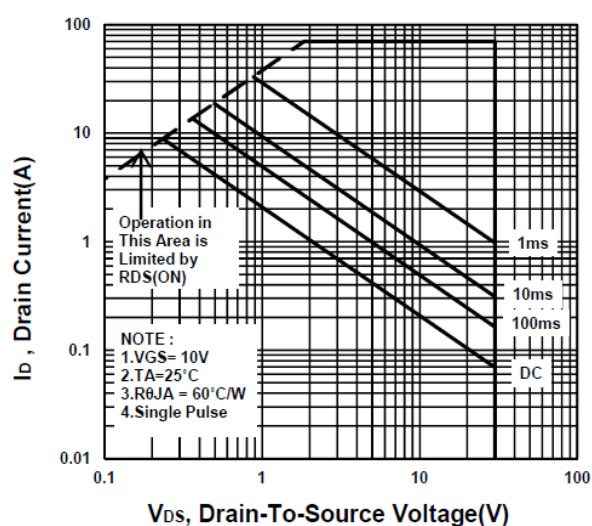
Source-Drain Diode Forward Voltage



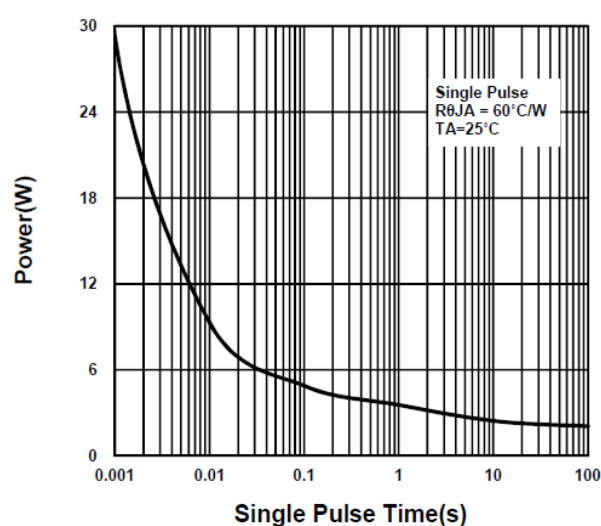
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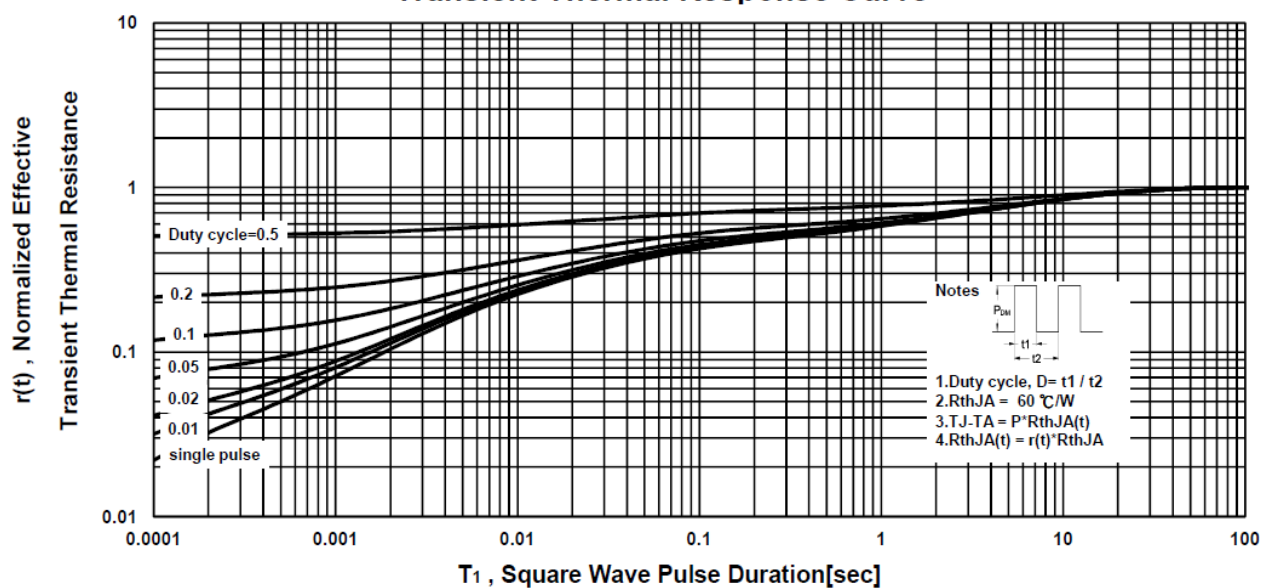
Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



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Package Dimension

PDFN 2x2S MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	1.9		2.1	I	0		0.05
B	1.9		2.1	J		0.203	
C	0.55	0.65	0.75	K	0.55		0.8
D	0.85		1.25	L	0.2		0.4
E	0.174	0.25	0.326	M	0.46		0.85
F	0.25		0.35	N		0.15	
G		0.2		O		0.23	
H	0.8		1.15				

