UNISONIC TECHNOLOGIES CO., LTD

12N70 Power MOSFET

12A, 700V N-CHANNEL POWER MOSFET

DESCRIPTION

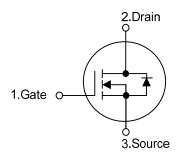
The UTC 12N70 are N-Channel enhancement mode power MOSFET which are produced using UTC's proprietary, planar stripe, DMOS technology.

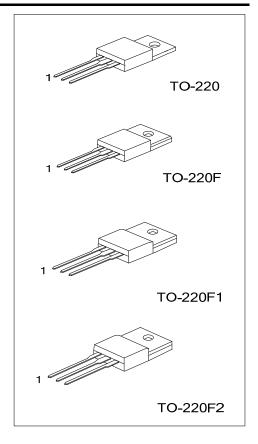
These devices are suited for high efficiency switch mode power supply. To minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode the advanced technology has been especially tailored.

FEATURES

- * $R_{DS(ON)} = 1.0\Omega @V_{GS} = 10 V$
- * Ultra low gate charge (typical 42 nC)
- * Low reverse transfer capacitance (C_{RSS} = typical 25 pF)
- * Fast switching capability
- * Avalanche energy specified
- * Improved dv/dt capability, high ruggedness

SYMBOL

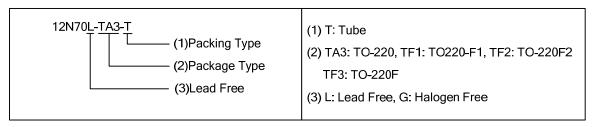




ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
12N70L-TA3-T	12N70G-TA3-T	TO-220	G	D	S	Tube	
12N70L-TF1-T	12N70G-TF1-T	TO-220F1	G	D	S	Tube	
12N70L-TF2-T	12N70G-TF2-T	TO-220F2	G	D	S	Tube	
12N70L-TF3-T	12N70G-TF3-T	TO-220F	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source



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■ ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	700	V
Gate-Source Voltage		V_{GSS}	±30	V
Avalanche Current (Note 2)		I _{AR}	12	Α
Drain Current	Continuous	I _D	12	Α
	Pulsed (Note 2)	I_{DM}	48	Α
	Single Pulsed (Note 3)	E _{AS}	790	mJ
Avalanche Energy	Repetitive (Note 2)	E _{AR}	24	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Power Dissipation	TO-220		225	°C/W
	TO-220F/TO-220F1	P_D	52	°C/W
	TO-220F2		55	°C/W
Junction Temperature		TJ	+150	°C
Operating Temperature		T _{OPR}	-55 ~ +150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating : Pulse width limited by maximum junction temperature
- 3. L = 10mH, I_{AS} = 12A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25 $^{\circ}$ C
- 4. $I_{SD} \le 12A$, di/dt $\le 200A/s$, $V_{DD} \le BV_{DSS}$ Starting $T_J = 25^{\circ}C$

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient		θ_{JA}	62.5	°C/W
Junction to Case	TO-220		0.56	°C/W
	TO-220F/TO-220F1	θ_{JC}	2.40	°C/W
	TO-220F2		2.27	°C/W

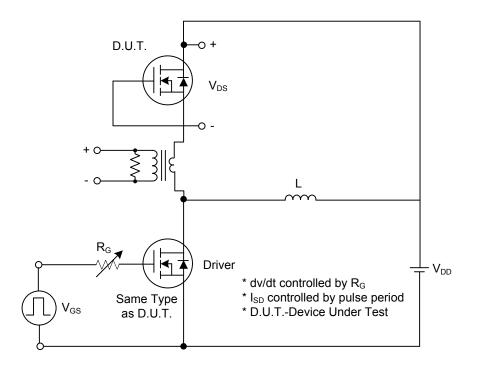
■ ELECTRICAL CHARACTERISTICS (T_C =25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT			
OFF CHARACTERISTICS									
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0 V, I _D = 250 μA	700			V			
Drain-Source Leakage Current	I _{DSS}	V _{DS} = 700 V, V _{GS} = 0 V			10	μA			
Gate-Source Leakage Current	I _{GSS}	V _{GS} = ±30 V, V _{DS} = 0 V			±100	nA			
Breakdown Voltage Temperature Coefficient	$\triangle BV_{DSS}/\triangle T_{J}$	I _D =250μA,Referenced to 25°C		0.7		V/°C			
ON CHARACTERISTICS									
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V			
Static Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} = 10V, I _D = 6.0A		0.7	1.0	Ω			
DYNAMIC CHARACTERISTICS									
Input Capacitance	C _{ISS}	-V _{DS} = 25 V, V _{GS} = 0 V, -f = 1MHz		1480	1900	pF			
Output Capacitance	Coss			200	270	pF			
Reverse Transfer Capacitance	C_{RSS}			25	35	pF			
SWITCHING CHARACTERISTICS									
Turn-On Delay Time	t _{D(ON)}	$V_{DD} = 300V, I_{D} = 12A,$ $R_{G} = 25\Omega \text{ (Note 1, 2)}$		30	70	ns			
Turn-On Rise Time	t_R			115	240	ns			
Turn-Off Delay Time	$t_{D(OFF)}$			95	200	ns			
Turn-Off Fall Time	t_{F}			85	180	ns			
Total Gate Charge	Q_G	V _{DS} = 480V,I _D = 12A, -V _{GS} = 10 V (Note 1, 2)		42	54	nC			
Gate-Source Charge	Q_GS			8.6		nC			
Gate-Drain Charge	Q_GD			21		nC			
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS									
Drain-Source Diode Forward Voltage	V_{SD}	$V_{GS} = 0 \text{ V}, I_{S} = 12\text{A}$			1.4	V			
Maximum Continuous Drain-Source Diode	_				12	Α			
Forward Current	I _S				12	А			
Maximum Pulsed Drain-Source Diode	lou				48	Α			
Forward Current	I _{SM}				+0				
Reverse Recovery Time	t _{rr}	$V_{GS} = 0 \text{ V}, I_S = 12A,$		380		ns			
Reverse Recovery Charge	Q_{RR}	dI _F /dt = 100 A/μs (Note 1)		3.5		μC			

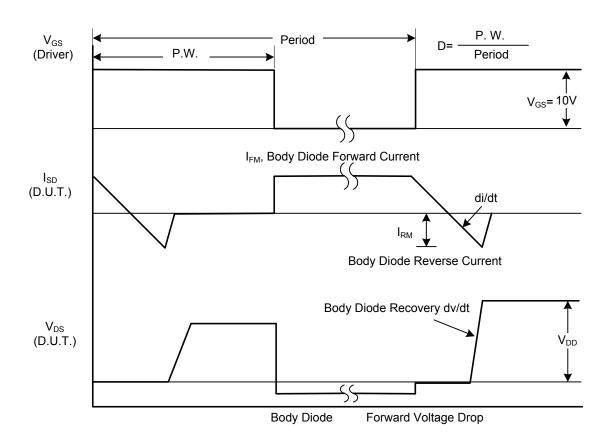
Notes: 1. Pulse Test : Pulse width ≤300µs, Duty cycle ≤ 2%

^{2.} Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

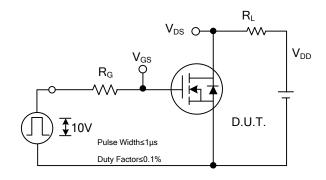


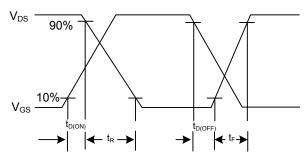
Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

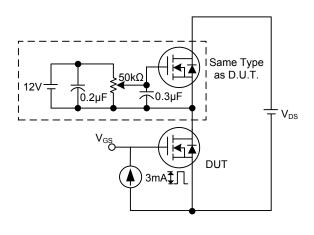
■ TEST CIRCUITS AND WAVEFORMS (Cont.)

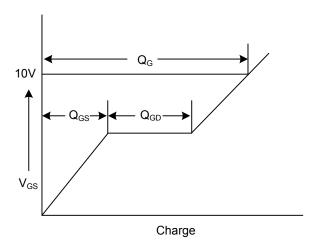




Switching Test Circuit

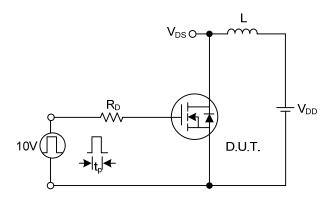
Switching Waveforms

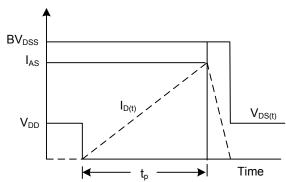




Gate Charge Test Circuit

Gate Charge Waveform

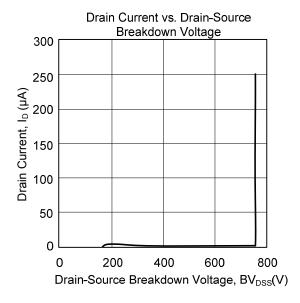


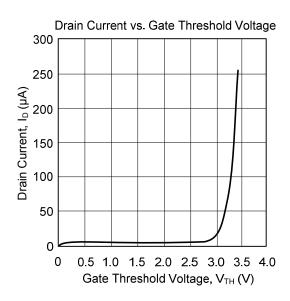


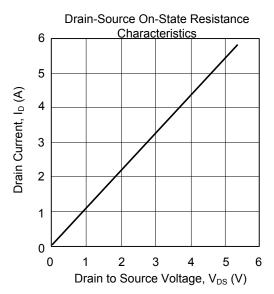
Unclamped Inductive Switching Test Circuit

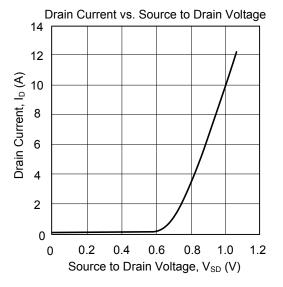
Unclamped Inductive Switching Waveforms

■ TYPICAL CHARACTERISTICS









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